

<b>Board Name:</b>	<b>EISA3/VL</b>
<b>Part #:</b>	<b>09-00181-xx</b>

Size:	Full-Size AT (13.8" x 12.02")	Chip Sets:	Micronics EISA Chip Set
Expansion Slots:	6 EISA 2 EISA/VL	Cache Options:	256K expandable to 512K
Processor Specifications:	486SX25; SX33 486DX33 486DX2/50; 486DX2/66	BIOS:	Phoenix BIOS on 1MB flash BIOS
Memory:	(4) 72-pin SIMM sockets to accommodate:  256K x 36 (1MB); 1MB x 36 (4MB) 4MB x 36 (16MB) MAX: 64MB	Floppy Support:	360K, 1.2MB, 1.44MB or 2.88MB
		Communication Ports:	2 on-board 16550 serial ports 1 on-board bi-directional port
		Keyboard:	Standard AT-Style DIN connector

### Cache Size Selection

Jumper	64K	128K	256K	512K
W8	1-2	2-3	2-3	2-3
W9	1-2	1-2	2-3	2-3
W10	1-2	1-2	1-2	2-3
W11	1-2	2-3	2-3	2-3
W12	1-2	1-2	2-3	2-3
W13	1-2	1-2	1-2	2-3
W14	3-4	4-5	1-2	4-5
W15	2-3	1-2	1-2	1-2

### CPU Selection

Main CPU	CPU Socket	W2	W5	W6
i486SX	None	1-2	1-2	None
i486SX	i486DX2	1-2	2-3	1-2 & 3-4
i486SX	i486DX	2-3	1-2	1-2 & 3-4
i486SX	i486SX	1-2	2-3	1-2 & 3-4
None	i486DX	2-3	1-2	1-2 & 3-4
None	i486DX2	1-2	1-2	1-2 & 3-4
None	i486SX	1-2	2-3	2-3
None	i487SX	1-2	2-3	1-2 & 3-4
None	P24T	2-3	2-3	1-2 & 3-4

### CPU Speed Selection

W3	CPU Speed
On	i486SX-25, i486DX-25 & i486DX2-50
Off	i486SX-33, i486DX-33, i486DX2-66, P24T

### Remaining "W"

Jumper	Function	Setting
W1	Reserved	1-2
W7	Color Monitor Mono Monitor	Short Open
W16	AT Style Keyboard PS/2 Style Keyboard	Open Short
W17	Reserved	1-2
W18	Clear RMA in RTC (may not be present)	Short

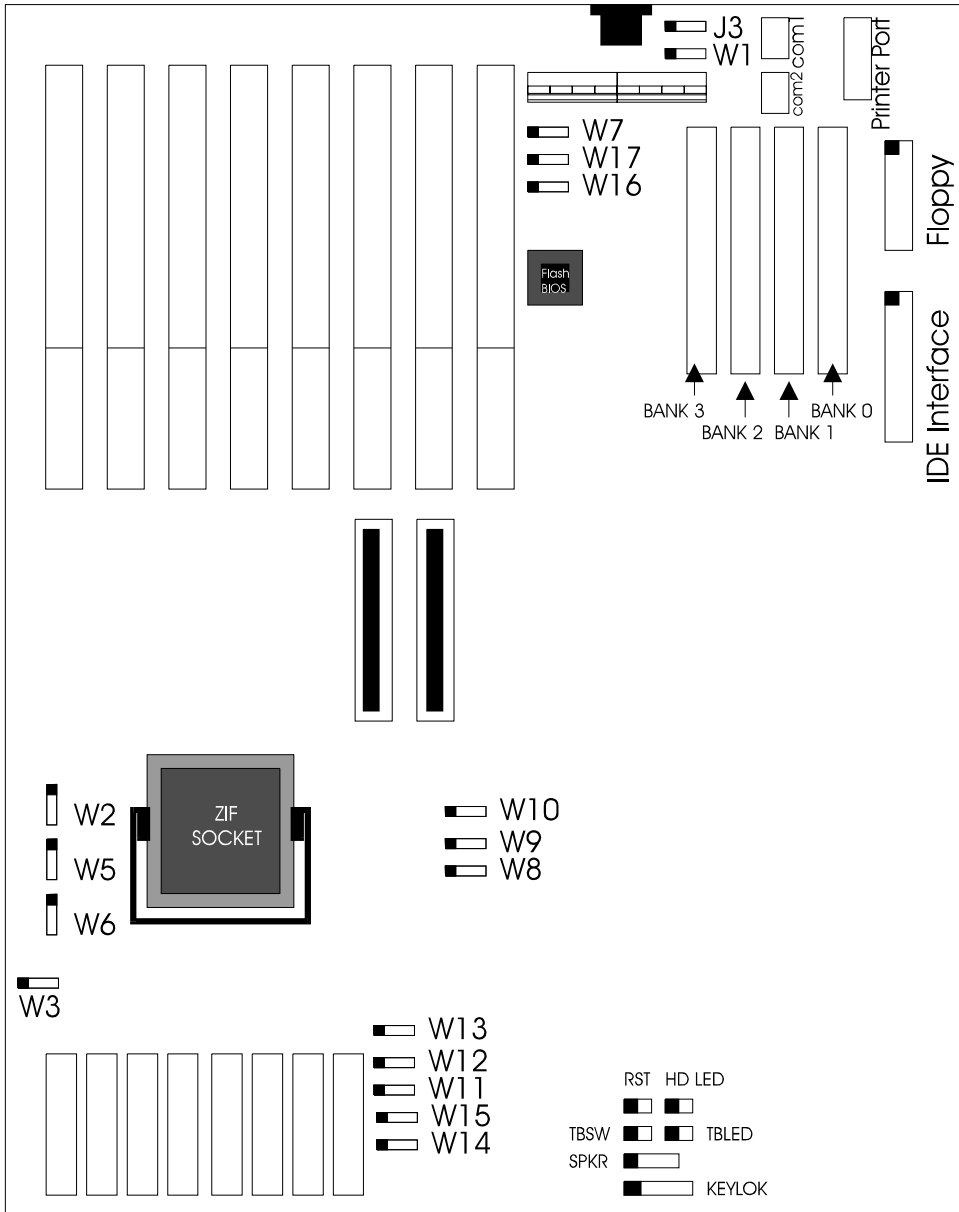
### Case and Peripheral Connections

Jumper	Function	Setting
J31	Parallel Port Connector	
J33	Serial Port A	
J34	Serial Port B	
J12	Keyboard Connector	
J6/J7	Power Supply Connector	
J32	IDE Connector	Primary
J31	Floppy Connector	
J3	External Battery	1-battery positive; 2-N/C, 3-GND; 4-GND
J11	Reset Switch	1-GND; 2-GND
J5	Turbo LED	1-Signal; 2-GND
J4	Turbo Switch	1-Signal; 2-GND
J8	Keylock/Power LED	1-Power; 2-N/C; 3-GND; 4-5V DC
J10	Speaker Connector	1-Speaker; 2-N/C 3-GND; 4-5V DC
J13	External Keyboard	1-Keyb lck, 2-data 3-N/C, 4-GND, 5-<+> 5V

### Memory Configuration

Bank 0	Bank 1	Bank 2	Bank 3	Total
4mb				4mb
4mb	4mb			8mb
4mb	4mb	4mb		12mb
4mb	4mb	4mb	4mb	16mb
16mb				16mb
4mb	4mb	16mb		24mb
16mb	16mb			32mb

4mb	16mb	16mb		36mb
4mb	4mb	16mb	16mb	40mb
16mb	16mb	16mb		48mb
16mb	16mb	16mb	16mb	64mb



EISA 3 VL-Bus: With Any Other CPU Installed