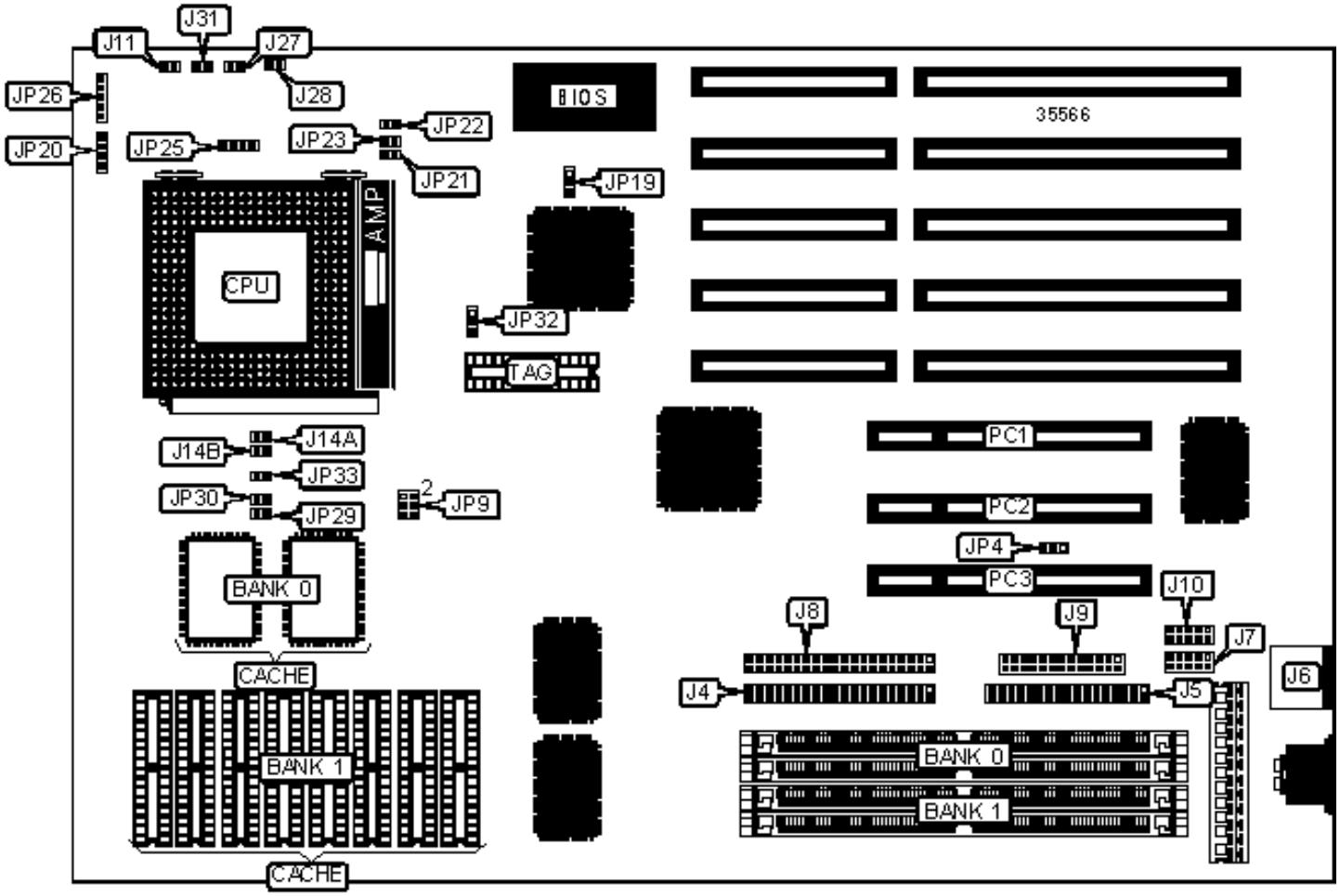
DTK COMPUTER, INC.

PAM-0054I (VER. 1.30)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
IDE interface 1	J4	VRM connector	J14
Floppy drive interface	J5	Reset switch	J27
PS/2 mouse port	J6	Turbo LED	J31
Serial port 1	J7	Speaker	JP20
IDE interface 2	J8	Chassis fan power	JP25
Parallel port	J9	Power LED & keylock	JP26
Serial port 2	J10	32-bit PCI slots	PC1 - PC3
IDE interface LED	J11		

Note: The location of pin 1 on JP25 is unidentified.

USER CONFIGURABLE SETTINGS

	Function	Jumper	Position
	On board I/O enabled	JP4	pins 1 & 2 closed
	On board I/O disabled	JP4	pins 2 & 3 closed
	Flash BIOS programming enabled	JP21	pins 1 & 2 closed
	Flash BIOS programming disabled	JP21	pins 2 & 3 closed
»	CMOS memory normal operation	JP22	Open
	CMOS memory clear	JP22	Closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1
2MB	(2) 256K x 36	NONE
4MB	(2) 512K x 36	NONE
4MB	(2) 256K x 36	(2) 256K x 36

6MB	(2) 512K x 36	(2) 256K x 36
6MB	(2) 256K x 36	(2) 512K x 36
8MB	(2) 1M x 36	NONE
8MB	(2) 512K x 36	(2) 512K x 36
10MB	(2) 1M x 36	(2) 256K x 36
10MB	(2) 256K x 36	(2) 1M x 36
12MB	(2) 1M x 36	(2) 512K x 36
12MB	(2) 512K x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 2M x 36	(2) 256K x 36
18MB	(2) 256K x 36	(2) 2M x 36
20MB	(2) 2M x 36	(2) 512K x 36
20MB	(2) 512K x 36	(2) 2M x 36
24MB	(2) 2M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 4M x 36	(2) 256K x 36
34MB	(2) 256K x 36	(2) 4M x 36

DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
36MB	(2) 4M x 36	(2) 512K x 36
36MB	(2) 512K x 36	(2) 4M x 36
40MB	(2) 4M x 36	(2) 1M x 36

40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
64MB	(2) 4M x 36	(2) 4M x 36
66MB	(2) 8M x 36	(2) 256K x 36
66MB	(2) 256K x 36	(2) 8M x 36
68MB	(2) 8M x 36	(2) 512K x 36
68MB	(2) 512K x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG
256KB (A)	(8) 32K x 8	None	(1) 8/16K x 8
256KB (B)	None	(1) 32K x 32/36	None
512KB	(8) 64K x 8	None	(1) 16/32K x 8

Note: If Bank 1 is used, Bank 0 must be empty.

CACHE JUMPER CONFIGURATION

Size	JP32

256KB (A)	pins 1 & 2 closed
256KB (B)	pins 1 & 2 closed
512KB	pins 2 & 3 closed

DMA CONFIGURATION		
DMA	JP10	JP11
DMA 1	pins 1 & 2 closed	pins 1 & 2 closed
DMA 3	pins 2 & 3 closed	pins 2 & 3 closed

CPU SPEED SELECTION (INTEL, W42C25 CLOCK CHIP)					
CPU speed	Clock speed	Multiplier	JP9	JP21	JP23
75MHz	50MHz	1.5x	5 & 6	Open	Open
90MHz	60MHz	1.5x	1 & 2, 3 & 4	Open	Open
100MHz	66MHz	1.5x	3 & 4	Open	Open
120MHz	60MHz	2x	3 & 4	Closed	Open
150MHz	60MHz	2.5x	1 & 2, 3 & 4	Closed	Closed
166MHz	66MHz	2.5x	3 & 4	Closed	Closed
Note: Pins designated are in the closed position.					

CPU SPEED SELECTION (AM K5, W42C25 CLOCK CHIP)					
CPU speed	Clock speed	Multiplier	JP9	JP21	JP23
75MHz	50MHz	1.5x	5 & 6	Open	Open
90MHz	60MHz	1.5x	1 & 2, 3 & 4	Open	Open
100MHz	66MHz	1.5x	3 & 4	Open	Open
120MHz	60MHz	2x	3 & 4	Closed	Open
133MHz	66MHz	2x	3 & 4	Closed	Open

Note: Pins designated are in the closed position.

CPU SPEED SELECTION (CX 6X86, W42C25 CLOCK CHIP)

CPU speed	Clock speed	Multiplier	JP9	JP21	JP23
90MHz	50MHz	1.5x	1 & 2, 5 & 6	Closed	Open
120MHz	50MHz	2x	5 & 6	Closed	Open
150MHz	60MHz	2x	1 & 2, 3 & 4	Closed	Open
166MHz	66MHz	2x	3 & 4	Open	Open

Note: Pins designated are in the closed position.

CPU SPEED SELECTION (INTEL, MX831801 CLOCK CHIP)

CPU speed	Clock speed	Multiplier	JP9	JP21	JP23
75MHz	50MHz	1.5x	5 & 6	Open	Open
90MHz	60MHz	1.5x	3 & 4	Open	Open
100MHz	66MHz	1.5x	1 & 2, 5 & 6	Open	Open
120MHz	60MHz	2x	3 & 4	Closed	Open
133MHz	66MHz	2x	1 & 2, 5 & 6	Closed	Open
150MHz	60MHz	2.5x	1 & 2, 5 & 6	Closed	Closed
166MHz	66MHz	2.5x	1 & 2, 5 & 6	Closed	Closed

Note: Pins designated are in the closed position.

CPU SPEED SELECTION (AM K5, MX831801 CLOCK CHIP)

CPU speed	Clock speed	Multiplier	JP9	JP21	JP23
75MHz	50MHz	1.5x	5 & 6	Open	Open
90MHz	60MHz	1.5x	3 & 4	Open	Open
100MHz	66MHz	1.5x	1 & 2, 5 & 6	Closed	Open

120MHz	60MHz	2x	3 & 4	Closed	Open
133MHz	66MHz	2x	1 & 2, 5 & 6	Open	Open
Note: Pins designated are in the closed position.					

CPU SPEED SELECTION (CX 6X86, MX831801 CLOCK CHIP)					
CPU speed	Clock speed	Multiplier	JP9	JP21	JP23
90MHz	60MHz	1.5x	1 & 2, 3 & 4	Closed	Open
120MHz	66MHz	1.5x	5 & 6	Closed	Open
150MHz	60MHz	2.5x	3 & 4	Closed	Open
166MHz	66MHz	2.5x	1 & 2, 5 & 6	Closed	Open
Note: Pins designate are in the closed position.					

CPU SPEED SELECTION (INTEL, IMI464 CLOCK CHIP)					
CPU speed	Clock speed	Multiplier	JP9	JP21	JP23
75MHz	50MHz	1.5x	1 & 2	Open	Open
90MHz	60MHz	1.5x	1 & 2, 5 & 6	Open	Open
100MHz	66MHz	1.5x	1 & 2, 3 & 4, 5 & 6	Open	Open
120MHz	60MHz	2x	1 & 2, 5 & 6	Closed	Open
133MHz	66MHz	2x	1 & 2, 3 & 4, 5 & 6	Closed	Open
150MHz	60MHz	2.5x	1 & 2, 5 & 6	Closed	Closed
166MHz	66MHz	2.5x	1 & 2, 3 & 4, 5 & 6	Closed	Closed
Note: Pins designated are in the closed position.					

CPU SPEED SELECTION (AM K5, IMI464 CLOCK CHIP)					
CPU speed	Clock speed	Multiplier	JP9	JP21	JP23
75MHz	50MHz	1.5x	1 & 2, 3 & 4, 5 & 6	Open	Open

90MHz	60MHz	1.5x	1 & 2, 5 & 6	Open	Open
100MHz	66MHz	1.5x	1 & 2, 3 & 4, 5 & 6	Open	Open
120MHz	60MHz	2x	1 & 2, 5 & 6	Closed	Open
133MHz	66MHz	2x	1 & 2, 3 & 4, 5 & 6	Closed	Open
Note: Pins designated are in the closed position.					

CPU SPEED SELECTION (CX 6X86, IMI464 CLOCK CHIP)					
CPU speed	Clock speed	Multiplier	JP9	JP21	JP23
90MHz	50MHz	1.5x	3 & 4	Closed	Open
120MHz	50MHz	2x	1 & 2	Closed	Open
150MHz	60MHz	2x	1 & 2, 5 & 6	Closed	Open
166MHz	60MHz	2.5x	1 & 2, 3 & 4, 5 & 6	Closed	Open
Note: Pins designated are in the closed position.					

CPU VOLTAGE SELECTION (SINGLE)		
Voltage	JP29	JP30
3.4V	Closed	Open
3.5V	Open	Closed

CPU VOLTAGE SELECTION (DUAL)						
V I/O	V Core	J14A	J14B	JP29	JP30	JP33
3.3V	2.5V	Open	Open	Closed	Open	Closed