



Intel[®] Desktop Board DP55WB Specification Update

July 2010

Order Number: E80453-004US

The Intel[®] Desktop Board DP55WB may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

Revision History

Revision	Revision History	Date
-001	This document is the first Specification Update for the Intel® Desktop Board DP55WB	September 2009
-002	Updated the Specification Changes Section	December 2009
-003	Updated the General Information Section	April 2010
-004	Updated the General Information Section	July 2010

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL® PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER, AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT. INTEL PRODUCTS ARE NOT INTENDED FOR USE IN MEDICAL, LIFE SAVING, LIFE SUSTAINING APPLICATIONS.

Intel may make changes to specifications and product descriptions at any time, without notice.

Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined". Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them.

The Intel® Desktop Board DP55WB may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications before placing your product order.

Copies of documents which have an ordering number and are referenced in this document, or other Intel literature, may be obtained from:

Intel Corporation
P.O. Box 5937
Denver, CO 80217-9808

or call in North America 1-800-548-4725, Europe 44-0-1793-431-155, France 44-0-1793-421-777, Germany 44-0-1793-421-333, other Countries 708-296-9333

Intel and the Intel logo are registered trademarks of Intel Corporation in the U.S. and other countries.

* Other names and brands may be claimed as the property of others.

Copyright © 2009, 2010 Intel Corporation.

Contents

Specification Update for the Intel® Desktop Board DP55WB

Terminology	5
General Information	5
Summary of Changes	6
Specification Changes	6

Specification Update for the Intel[®] Desktop Board DP55WB

This document is an update to the specifications contained in the *Intel[®] Desktop Board DP55WB Technical Product Specification* (Order Number: E70716). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel[®] Core™ i7-800 and i5-700 Desktop Processor Series Specification Update* (Order Number: 322166) for specification updates concerning the Core i7 and Core i5 processors that may apply to the desktop board DP55WB. Unless otherwise noted in this document, it should be assumed that any Processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel[®] 5 Series Chipset and Intel[®] 3400 Series Chipset Specification Update* (Order Number: 322169) for specification updates concerning the Intel[®] P55 Express Chipset that may apply to the desktop board DP55WB. Unless otherwise noted in this document, it should be assumed that any Platform Controller Hub errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Terminology

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

General Information

Basic Desktop Board DP55WB Identification Information

AA Revision	BIOS Revision	Notes
E63798-204	WBIBX10J.86A.0122	1,2
E64798-205	WBIBX10J.86A.0122	1,2
E64798-207	WBIBX10J.86A.0293	1,2
E64799-205	WBIBX10J.86A.0122	1,2

Notes:

1. The AA number is found on a small label on the component side of the board.
2. The Intel® 82P55 PCH Express Chipset used on this AA revision consists of the following component:

Device	Stepping	S-Spec Numbers
82P55 Platform Controller Hub (PCH)	B2	SLGWV

Summary of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board DP55WB. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted.

The following notations are used in the table:

Doc:	Document change or update that will be implemented.
Plan Fix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
No Fix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

No.	Plans	Specification Changes
1	Doc	Update to the Note for Figure 3 on page 17 of the Technical Product Specification
2	Doc	Update to Table 10 – Item O to add Intel Z-U130 USB Solid-State Drive or compatible device support
No.	Plans	Errata
		There are no characterized erratum for this product

Specification Changes

The Documentation Changes listed in this section apply to the *Intel® Desktop Board DP55WB Technical Product Specification* (Order Number E70716).

1. Update to the Note for Figure 3 on page 17 of the Technical Product Specification.



NOTE

The Intel Core i7-800 and Intel Core i5-700 processor series requires memory to be populated in the Channel A, DIMM 0 socket.

2. Update to Table 10 – Item O to add Intel Z-U130 USB Solid-State Drive or compatible device support.

Table 1. Component-side Connectors and Headers Shown in Figure 10

Item/callout from Figure 10	Description
A	PCI Conventional bus add-in card connector
B	Front panel audio header
C	PCI Express x1 bus add-in card connector
D	PCI Express x1 bus add-in card connector
E	PCI Express x16 bus add-in card connector
F	Rear chassis fan header
G	Processor fan header
H	Processor core power connector (2 X 2)
I	Main power connector (2 X 12)
J	Front panel header
K	Front chassis fan header
L	Alternate front panel power LED header
M	SATA connectors
N	Front panel USB header
O	Front panel USB header (with Intel Z-U130 USB Solid-State Drive or compatible device support)
P	Front panel USB header
Q	Chassis intrusion header
R	IEEE 1394a front panel header
S	S/PDIF out header