



Intel® Desktop Board D915PLWD Specification Update

Release Date: July 2006

Order Number: D23990-003US

The Intel® Desktop Board D915PLWD may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL® PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER, AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT. INTEL PRODUCTS ARE NOT INTENDED FOR USE IN MEDICAL, LIFE SAVING, LIFE SUSTAINING APPLICATIONS.

Intel may make changes to specifications and product descriptions at any time, without notice.

Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined". Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them.

The Intel® desktop board D915PLWD may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications before placing your product order.

Copies of documents which have an ordering number and are referenced in this document, or other Intel literature, may be obtained from:

Intel Corporation
P.O. Box 5937
Denver, CO 80217-9808

or call in North America 1-800-548-4725, Europe 44-0-1793-431-155, France 44-0-1793-421-777,
Germany 44-0-1793-421-333, other Countries 708-296-9333

Intel, Pentium, Celeron, and the Intel logo are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

* Other names and brands may be claimed as the property of others.

Copyright © 2005, 2006 Intel Corporation.

CONTENTS

REVISION HISTORY v

PREFACE.....vi

Specification Update for the Intel® Desktop Board D915PLWD

GENERAL INFORMATION 3

SPECIFICATION CHANGES 5

ERRATA 6



REVISION HISTORY

Date of Revision	Version	Description
June 3, 2005	-001	This document is the first Specification Update for the Intel® Desktop Board D915PLWD.
September 2005	-002	Added Erratum 1 and updated General Information table.
July 2006	-003	Updated the General Information page, added Specification Changes 1 and Erratum 2.



PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D915PLWD Technical Product Specification* (Order number D23930). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order number 249199) for specification updates concerning the Intel Pentium 4 processor and that may apply to the desktop board D915PLWD. Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 915G/915GV/915GL/915P/915PL/910GL Express Chipset Specification Update* (Order Number 301468) for specification updates concerning the 82915PL MCH Controller and that may apply to the desktop board D915PLWD. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub 6 (ICH6) Family Specification Update* (Order Number 301474) for specification updates concerning the 82801FB I/O Controller Hub and that may apply to the desktop board D915PLWD. Unless otherwise noted in this document, it should be assumed that any ICH 6 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Nomenclature

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board D915PLWD's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

Specification Update for the Intel[®] Desktop Board D915PLWD



GENERAL INFORMATION

Basic Desktop Board D915PLWD Identification Information

AA Revision	BIOS Revision	Notes
D16710-100	VG91510A.86A.0042	1-2
D16710-101	VG91510A.86A.0042	1-2
D16710-102	VG91510A.86A.0048	1-2

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 82910PL Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82915PL GMCH	C2	SL8D6
82801FB ICH6	B2	SL7Y5



Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D915PLWD. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
Plan Fix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
No Fix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	SPECIFICATION CHANGES
1	Doc	Changes to Section 2.7.2.2, Add-in Card Connectors
NO.	PLANS	ERRATA
1	Plan Fix	DMI fields may be overwritten during a BIOS update
2	Plan Fix	The placement of capacitors behind PCI Express* x1 connector slots 1 and 2 may prohibit some PCI Express x1 add-in cards from properly connecting to the motherboard



SPECIFICATION CHANGES

The Specification Changes listed in this section apply to the *Desktop Board D915PLWD Technical Product Specification* (Order Number D23930). All Specification Changes will be incorporated into a future version of that specification.

- The following note will be added to the TPS in Section 2.7.2.2, Add-in Card Connectors:***

NOTE

Capacitors required for the audio circuitry are located within the ATX keep-out area near PCI Express x1 slots 1 and 2, and may limit the use of some PCI Express x1 cards.*



ERRATA

1. *DMI Fields May Be Overwritten During a BIOS Update*

PROBLEM: Updating from a BIOS release prior to VG91510A.86A.0046 could overwrite SMBIOS fields.

IMPLICATION: SMBIOS contents could be overwritten during a BIOS update. Recovery CDs or other software that reads this data before running on a system may not be able to verify the correct board ID.

WORKAROUND: A special BIOS update to BIOS VG91510A.86A.0048 will save and restore the contents during the BIOS update process. Once updated to this BIOS, the boards may be updated to future BIOS releases with no special requirements.

STATUS: This erratum may be fixed in a future board revision.

2. *The placement of capacitors behind PCI Express* x1 connector slots 1 and 2 may prohibit some PCI Express x1 add-in cards from properly connecting to the motherboard.*

PROBLEM: Due to the design and placement of the audio circuitry on this motherboard, it was necessary to position capacitors in the keep-out zone behind PCI Express* x1 connector slots 1 and 2.

IMPLICATION: Some PCI Express x1 add-in cards may not fully connect to PCI Express x1 connector slots 1 and 2, due to interference between capacitors in the keep-out zone and I/O connectors on the add-in card that are located on the I/O bracket, in line with the edge fingers on the card.

WORKAROUND: None

STATUS: MAY BE FIXED IN A LATER HARDWARE REVISION.