



Intel® Desktop Board D915PBL Specification Update

Release Date: July 2006

Order Number: C80885-006US

The Intel® Desktop Board D915PBL may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D915PBL may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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REVISION HISTORY

Date of Revision	Version	Description
June 21, 2004	-001	This document is the first Specification Update for the Intel® Desktop Board D915PBL.
september 2004	-002	Added Documentation Changes 1, 2.
October 2004	-003	Added Erratum 1.
January 2005	-004	Added Erratum 2.
March 2005	-005	Added Erratum 3.
July 2006	-006	Added Specification Changes 1, Erratum 2 and updated General Information table.



PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D915PBL Technical Product Specification* (Order number C68599). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order number 249199) for specification updates concerning the Intel Pentium 4 processor and that may apply to the desktop board D915PBL. Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 915G and 915P Chipset: Specification Update* (Order Number 301468) for specification updates concerning the 82915P MCH Controller and that may apply to the desktop board D915PBL. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub 6 (ICH6) Family Specification Update* (Order Number 301474) for specification updates concerning the 82801ER I/O Controller Hub and that may apply to the desktop board D915PBL. Unless otherwise noted in this document, it should be assumed that any ICH 6 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Nomenclature

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board D915PBL's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

**Specification Update for the
Intel® Desktop Board D915PBL**



GENERAL INFORMATION

Basic Desktop Board D915PBL Identification Information

AA Revision	BIOS Revision	Notes
C64664-203	CY91510A.86A.0012	1-2
C64664-204	CY91510A.86A.0012	1-2
C64664-301	CY91510A.86A.0012	1-2
C64664-302	CY91510A.86A.0026	1-2
C64664-303	CY91510A.86A.0026	1-2
C67720-203	CY91510A.86A.0012	1-2
C67720-204	CY91510A.86A.0012	1-2
C67720-301	CY91510A.86A.0012	1-2
C67720-302	CY91510A.86A.0026	1-2
C67720-303	CY91510A.86A.0026	1-2
C67720-502	CY91510A.86A.0026	1-2
C67720-503	CY91510A.86A.0026	1-2

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 82915P Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82915P MCH	B1	SL7LY
82801FR ICH6	B1	SL79N



Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D915PBL. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

CODES USED IN SUMMARY TABLE

- Doc: Document change or update that will be implemented.
- PlanFix: This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
- Fixed: This erratum has been previously fixed.
- NoFix: There are no plans to fix this erratum.
- Shaded: This erratum is either new or modified from the previous version of the document.

NO.	PLANS	SPECIFICATION CHANGES
1	Doc	Changes to Section 2.8.2.3, Add-in Card Connectors
NO.	PLANS	ERRATA
1	Plan Fix	Alert Standard Format (ASF) is non-functional
2	Plan Fix	The IEEE-1394a interface may lose link connection when transferring files from some IEEE-1394a DVD/CD-ROM devices
3	Plan Fix	Wake On LAN may not function after loss and restore of AC power
4	Plan Fix	The placement of capacitors behind PCI Express* x1 connector slots 1 and 2 may prohibit some PCI Express x1 add-in cards from properly connecting to the motherboard
NO.	PLANS	DOCUMENTATION CHANGES
1	Doc	BIOS ID string is incorrect
2	Doc	BIOS ID string is incorrect within Section 3.1, Introduction



SPECIFICATION CHANGES

The Specification Changes listed in this section apply to the *Desktop Board D915PBL Technical Product Specification* (Order Number C68599). All Specification Changes will be incorporated into a future version of that specification.

- The following note will be added to the TPS in Section 2.8.2.3 Add-in Card Connectors:***

NOTE

Capacitors required for the audio circuitry are located within the ATX keep-out area near PCI Express x1 slots 1 and 2, and may limit the use of some PCI Express x1 cards.*



ERRATA

1. *Alert Standard Format (ASF) is Non-Functional*

PROBLEM: BIOS and device driver support is currently not implemented to support Alert Standard Format (ASF).

IMPLICATION: ASF, an interface that provides access and manageability in OS-absent environments, is currently not supported.

WORKAROUND: None.

STATUS: This erratum may be fixed in a future BIOS and driver revision.

2. *The IEEE-1394a Interface May Lose Link Connection When Transferring Files From Some IEEE-1394a DVD/CD-ROM Devices*

PROBLEM: PCI frequency noise between GND and the IEEE-1394a analog ground (AGND), in excess of 1V peak to peak, has been measured when transferring files from an IEEE-1394a DVD/CD-ROM device and an IDE Hard Disk Drive. The presence of this noise causes an IEEE-1394a link drop off with certain IEEE-1394a DVD/CD-ROM devices.

IMPLICATION: Users may experience the IEEE-1394a link drop when transferring files from some IEEE-1394a DVD/CD-ROM devices. This failure has been observed with a very limited number of IEEE-1394a DVD/CD-ROM devices. The failure is masked when the system is configured with a minimum of two PCI cards. This failure is considered a low risk.

WORKAROUND: None.

STATUS: This erratum may be fixed in a future hardware revision.

3. *Wake On LAN May Not Function After Loss and Restore of AC Power*

PROBLEM: If AC power is removed, Wake On LAN may not function until the LAN circuit is re-initialized. The sequence of events is as follows:

- AC power removed (AC power cord disconnected, for example)
- AC power restored. +5V Standby voltage is re-applied to the board
- Wake On LAN may not be functional
- Switch power on
- Switch power off to put system back into Standby mode (S5 state)
- Wake On LAN functionality restored

IMPLICATION: Wake on LAN will not function if AC power is interrupted.



WORKAROUND: In the BIOS Setup program, set After Power Failure to "Power On" in the Power Menu.

STATUS: This erratum may be fixed in a future BIOS revision.

4. *The placement of capacitors behind PCI Express* x1 connector slots 1 and 2 may prohibit some PCI Express x1 add-in cards from properly connecting to the motherboard.*

PROBLEM: Due to the design and placement of the audio circuitry on this motherboard, it was necessary to position capacitors in the keep-out zone behind PCI Express* x1 connector slots 1 and 2.

IMPLICATION: Some PCI Express x1 add-in cards may not fully connect to PCI Express x1 connector slots 1 and 2, due to interference between capacitors in the keep-out zone and I/O connectors on the add-in card that are located on the I/O bracket, in line with the edge fingers on the card.

WORKAROUND: None

STATUS: MAY BE FIXED IN A LATER HARDWARE REVISION.



DOCUMENTATION CHANGES

The Documentation Changes listed in this section apply to the *Intel® Desktop Board D915PBL Technical Product Specification* (Order number C68599). All Documentation Changes will be incorporated into a future version of that specification.

1 BIOS ID String is Incorrect

On page ii, the first paragraph below the Revision History table will be replaced in its entirety as follows:

This product specification applies to only the standard Intel® Desktop Board D915PBL with BIOS identifier CY91510A.86A.

2 BIOS ID String is Incorrect Within Section 3.1, Introduction

Section 3.1, Introduction, page 83, the second paragraph will be replaced in its entirety as follows:

The BIOS displays a message during POST identifying the type of BIOS and a revision code. The initial production BIOSs are identified as CY91510A.86A.