



Intel[®] Desktop Board D410PTL and D410PTLW Specification Update

February 2010

Order Number: E91522-001US

The Intel[®] Desktop Board D410PTL and D410PTLW may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

Revision History

Revision	Revision History	Date
-001	This document is the first Specification Update for the Intel® Desktop Board D410PTL and D410PTLW	February 2010

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The Intel® Desktop Board D410PTL and D410PTLW may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications before placing your product order.

Copies of documents which have an ordering number and are referenced in this document, or other Intel literature, may be obtained from:

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or call in North America 1-800-548-4725, Europe 44-0-1793-431-155, France 44-0-1793-421-777, Germany 44-0-1793-421-333, other Countries 708-296-9333

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Specification Update for the Intel[®] Desktop Board D410PTL and D410PTLW

This document is an update to the specifications contained in the *Intel[®] Desktop Board D410PTL and D410PTLW Technical Product Specification* (Order Number: E87536). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel[®] Atom Processor D400 Series Specification Update* (Order Number: 435404) for specification updates concerning the Intel Atom D400 series processors that may apply to the desktop board D410PTL and D410PTLW. Unless otherwise noted in this document, it should be assumed that any Processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel[®] NM10 Express Chipset Specification Update* (Order Number: 417137) for specification updates concerning the Intel[®] NM10 Express Chipset that may apply to the desktop board D410PTL and D410PTLW. Unless otherwise noted in this document, it should be assumed that any Platform Controller Hub errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Terminology

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

General Information

Basic Desktop Board D410PTL Identification Information

AA Revision	BIOS Revision	Notes
E86256-300	MOPNV10N.86A.159	1,2
E80287-300	MOPNV10N.86A.159	1,2

Basic Desktop Board D410PTLW Identification Information

AA Revision	BIOS Revision	Notes
E86257-300	MOPNV10N.86A.159	1,2

Notes:

1. The AA number is found on a small label on the component side of the board.
2. The Intel® CG82NM10 Express Chipset used on this AA revision consists of the following component:

Device	Stepping	S-Spec Numbers
CG82NM10	NO	SLGXX

Summary of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel Desktop Board D410PTL and D410PTLW. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted.

The following notations are used in the table:

- Doc: Document change or update that will be implemented.
- Plan Fix: This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
- Fixed: This erratum has been previously fixed.
- No Fix: There are no plans to fix this erratum.
- Shaded: This erratum is either new or modified from the previous version of the document.

No.	Plans	Errata
		There are no characterized erratum for this product