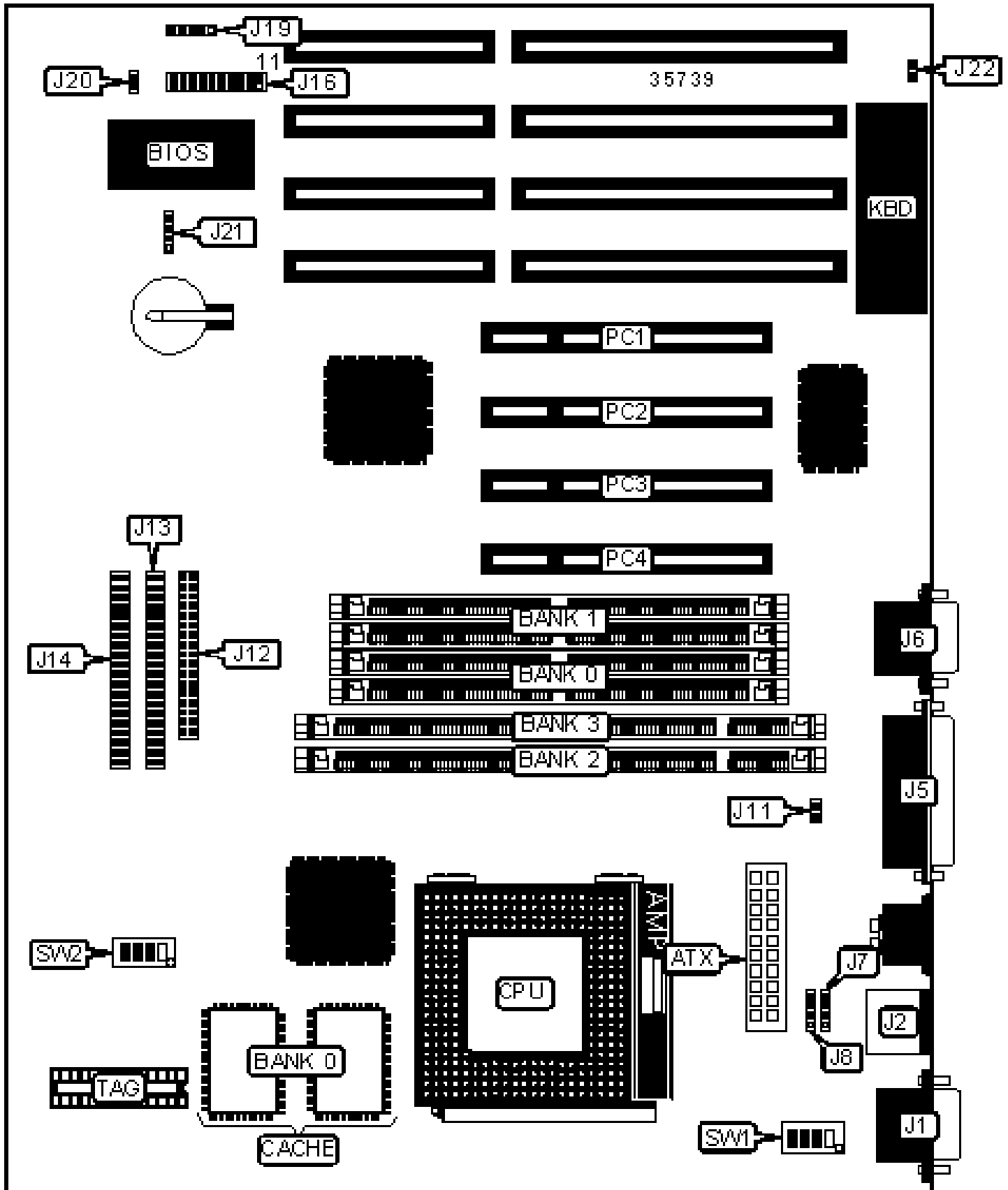


CALIFORNIA GRAPHICS & PERIPHERALS, INC.

SUNRAY EVOLUTION TX (ATX VERSION)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
ATX power connector	ATX	Speaker	J16/pins 1 - 4
Serial port 1	J1	Green PC connector	J16/pins 7 & 17
PS/2 mouse port	J2	Turbo LED	J16/pins 8 & 18
Parallel port	J5	Reset switch	J16/pins 9 & 19
Serial port 2	J6	IDE interface LED	J16/pins 10 & 20
USB connector	J7	Power LED & keylock	J16/pins 11 - 15
USB connector	J8	IR connector	J19
Floppy drive interface	J12	Soft off power supply	J20
IDE interface 2	J13	External battery	J21
IDE interface 1	J14	32-bit PCI slots	PC1 – PC4

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	J11	Unidentified
Battery type select internal	J21	Pins 1 & 2 closed
Battery type select external	J21	Open
» CMOS memory normal operation	J22	Open
CMOS memory clear	J22	Closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36

24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36

SIMM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory.		

DIMM CONFIGURATION		
Size	Bank 2	Bank 3
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None

16MB	(1) 1M x 64	(1) 1M x 64
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None
64MB	(1) 4M x 64	(1) 4M x 64
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
Note: Board accepts SDRAM memory.		

CACHE CONFIGURATION		
Size	Bank 0	TAG
256KB	(2) 32K x 32	Unidentified

CPU SPEED SELECTION (CX 6X86L)						
CPU speed	Clock speed	Multiplier	SW2/1	SW2/2	SW2/3	SW2/4
150MHz	60MHz	2x	On	On	On	Off
166MHz	66MHz	2x	Off	Off	On	Off

CPU SPEED SELECTION (AM K5)						
CPU speed	Clock speed	Multiplier	SW2/1	SW2/2	SW2/3	SW2/4
100MHz	66MHz	1.5x	Off	Off	Off	Off

Voltage	SW1/1	SW1/2	SW1/3	SW1/4
3.5v	On	On	On	On

CPU VOLTAGE SELECTION (DUAL)					
Voltage	V core	SW1/1	SW1/2	SW1/3	SW1/4
3.3v	2.5v	On	Off	On	Off
3.3v	2.6v	Off	On	On	Off
3.3v	2.7v	On	On	On	Off
3.3v	2.8v	Off	Off	Off	On
3.3v	2.9v	On	Off	Off	On
3.3v	3.0v	Off	On	Off	On
3.3v	3.2v	Off	Off	On	On
3.3v	3.5v	On	On	On	On