

AX6B PLUS

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ÍÓÒqÑÁÇ€

İP³QÍÓ11ÑÁàl

%ØİèÀİİèİhÈ %èÉdÈ çıñú»T

İP³KÍÓ11İŞB ÅSÒa

İŞB ÅSÒaÅÖÆÝ Ò Èà»R;ıñİ Jumper Å^ ÈİÚj (Connector) ÅÖÀ Ò~»RÅSÒaÈ` ØèB Ò÷Åq
Ñ_ÅÖÅaĐİ»T

İP³eÍÓ11AWARD BIOS

AWARD BIOS ÅÖÆÝ Ò Èà»RÀf ÈèÒaİnÅŞçaÅÖÑ_Ò,,»R;Y%èÛ_Û İ' Å»ÅÖçèÅ|»T

Ä Û 1A11ÈqÅ'Æ÷ÝUĐÈÒè

ÅéçèÈèÈq[Å ÅÖÅøÍ_È÷ÝU»T

Ä Û 1B 1ÜİÅ ÅXÖóÈèÈ½

çıñİÅ Åf È' ÒèÅ^ È÷ÝUÅÖÒ ÈaÖaÅoĐİ»T

Ä Û 1C Jumper1İnÅŞÄ

ÅiÅİ Jumper ÅÖÅTÄ »T

Óé;öõ»Ã

¿Y%Æ Æ ¿Ö%ü¿f Èq;èÃÖÖé¿öõ»Ã »X



×èÃqñ_
Àa%Q¿UYU%ÁÆóÝ Ö»Ã »T



PÍÁz
%4Ñ ØøÁQÈã»R¿zÉúØãÇaÀ^ÚZÁúÄX»T



×è%¿%ü
ÓŠ¿ ÁØ×uÈ÷ÝUÍ, ¿ÇÃÖ% Ä | »T



ÇÃÇÈ
Î½Ü È' ØøÁQÇÁÜZ»T



Î½ö
Î½ÈÁ%¿ ÁQÈPÌ%ÃÖØøÁQÈÈ“»T

ζòÙ

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Ä Ū A ĒqĀ%Ē÷YŪĐĒðē

Ä Ū B ŪĪĀ ĀXŌóĒĒĒ%½

Ä Ū C JUMPER ÍnĀŠĀ

İP³QÍÓ ÑÀàl

AX6B Plus ƒ QD ƒP dntel 82440BX İÓ% İi ÄÖ ATX çUØ Ä` »BX İÓ% İiƒ È^ ƒE Pentium II CPU İncfÄÖ»R»pİÄ»MÄf ÖðİæÑ†ÄÖİh È »RÄi ÄfAGP ÖéÄeİ†Èİ È »S SDRAM»SUItra DMA/33»SBus master IDE çY»e USB İ†Èİ È İç»TAX6B Plus »D Äð»WAdaptec AIC-7880 SCSI İÓ%»Rçz»pİÄ»W4 È 168 pin ÄÖ DIMM (Dual in-line Memory Module) İ»Öè»RİæÄyççYÄSÖa1GB ÄÖ SDRAM»TÍ, D çUØ Ä` »hÄYÄdÄİ »DÄð L2 cach2»RÄnƒÈİ, »SÖxçnÄİAs Pentium II CPU card (connector SLOT1) »» »W»TÄÖç•»RAX6B Plus ÄéçèÄÖƒ 2M bit Flash ROM»RÄiçYÖ çÖÄiÑ†ÖhçnÜÄÖ»p İÄÉú» »RÑ İ^ ƒ Ä çIÈvÄ »W»T

»çç^ ÄfÄÖ»RAX6B Plus ÜóÄyİä»MÄyĐİç DÄÖçnÜ»X

İJ jumper İncf (Jumper-less) AX6B Plus Ä È »MİŠB Jumper ÄÖİncf»RCPUIÖ,, Ü»»eÜhİ»ççYÄðÈäÈÖİ ÄéçèİƒB »iØy»RÄpİnŠÇaÜçÄ†As»ÄÖ=Ö,,ÄÜÄÖ EEPROM »D»RB È ÄsÄéçè»hÄ çI»Ä` »T

»ÄÖ=ÄéçèÖ,,ÄÜ (Battery-less) AX6B Plus ÄÖ CMOS İnŠSÖ ÈaÜçÄ†As»ÄÖ=Ö,,ÄÜÄÖ EEPROM »D»R»»DaÄfÄÖ»RAX6B Plus ÄÖ RTC ÈaPÜx`Ö (real time clock) »»ÄÖ= ÇÖÖ,,ÄÜ»RÄnÄÖİ, ƒ »Q»ÄyÄİÜ Ä` NÄÄÜÄÖçUØ Ä` »T

Ü D»D"Ø (Suspend to Hard Drive) ÖSçèİ, ĐİçnÜ»RĐ"Ø Èä»ÄRÖ=ÇÈÄüÄÖÖ Ä ÄÖ ÈäD»ÇÄÑ†Ö » Win95»SOffice ÄeÄpçÄİƒB »W»RÄiççYÄ»ÈİÄ İpç ç Ý Ø »ÄÄvÄÖÄƒ ÖR»RÖçyð »ÄQÑ•Äİè»eÜİçèİ' Ä»ÄÖİÜÑ»TxèÄéçèVESA İhÈ ƒÈvÄÖPCI VGA çu (Äf AOpen S3 PV70/PT70)»RSound Blaster ƒÈvÄÖÇİ ÈP çu (Äf AOpen AW35/MP56)»RRockwell ƒÈvÄÖ Modem (AOpen F56/MP56)»RçYxeÄ` İ, ÇiçnÜ ççÜxeÓSÄQ»T

ÄÖ,,ÑxÖaØØØ ÖÖÈÈD"Ø (Zero Voltage Modem Wake Up) ÄbçèATX Soft Power ÄÖçnÜ»RÄİèççYÄsÝ Ø ÄÖÄÄÖR»ƒ »RÖxçèÖaØØØ Èi ÈäÄYÄðÈäÈİBçÖ, Öö»TÍ, ĐİçnÜ ÄÜÈçRÄi çèÄi Öi ÜÜDÄÉ`Ø ÖaİÄÜ Ø »TÄp»ÄÄ»ÄÄÖƒ È"ÄsÄ »RÈ»»WÄÄİèÄÖç•ÈİÄ»Öa ØØØ »Äç•»RÈ' ÜóççYÜ çè»ÖÈİÄ»ÖaØØçu (Internal Modem Card) Äi »pİÄÄÖçnÜ»TÄé çè AX6B Plus Nvƒ F56/MP56 »ÖÈİÄ»ÖaØØçu»RçÇÈäÈöçÖİ] Ö=çèÄ ÝSç•ÄÖÖ,,Ñx»T

Ö Ö ÖÖÈÈD"Ø (LAN Wake up) ÄİÜZÝ Ä »hç' ÄÖÖaØØØ ÖÖÈÈD"Ø »R»ÄÖ]Í, ƒ Öxçè ÈeÈ Ö Ö ÄiÍ Ü Äİè»TÇÈÄéçèÖ Ö ÖÖÈÈD"Ø »RÈ' çİDNU ÜaÄİ»pİÄİ, ĐİçnÜÄÖÖ Ö çu»RÄYÖ=ÄSÖaØRÑ ÄÖÖ Ö Ööİ'İƒB »T

ÑÀàl

ÀSÈàD"Ø (RTC Wake Up Timer) Í, Çì Ìúú:zß È' ÓŠ; ÀSÒ,,À€%QÇì ÈàD»»RÀTN Èà D»%QÀ »RÀÌ ÌèÀ\Ñ'ÀòÈàD"Ø »TE' :z:YÈ_D"Ø ÈàD»ÌnÀSAsÀTÇì% ÀÒÄ %Q%è»RÀèÆ ÀT %èÀÒÄ Çì ÈàÀ »TÀpØ »eÀñ:zDWA È »T

ÀaÀS; ÌÀÀ» CPU ÝÇÙ%*^ð ÀaÀS; ÌÀÀ»ð %QÉ ÀÒÀUÀaÀS; ÌÀÀ»ÁyÁÍÁ ÈÀÀÒÈPì%»T :Y%QÉ ÚÌ;èÀÌÁ;»RÀaÀS; ÌÀÀ»ÀÓ%çÀQÑBÀñÈ_ÒNÁTÀ ÀUÀaÀSÀ»»T

Ó]Ó,,HÀ`BQÓ,,ð (Over Current Protection Circuit) ÀSðÀÌè 3.3V/5V/12V Baby AT Àè ATX ; ÌÀÀ»Ó,,Ñ×ÀèÚÍØØ%r»RÓ]Ó,,HÀ`BQÆ ÀòÈqÌ'ÀÒÓ,,ð »TÓ À ÒaØ Èà%4/3/f %ÀÀÒÌ^ð »RÓ]Ó,,HÀ`BQÀÒ×eÀUÈqÀÌÈP»RÀ Ñ+Q;_P_Øntium II CPU Àè;è%W4/AaÀÒ Ó,,Ú%»RÀpD»ð=ÇÈYÇÙ%ØÈ_5V ÚúÌÀÀ CPU Èð%ÀÓ,,Ú%½(ÀìÀf 2.8V)»RÇ ÀÌÀÒ5V Ó]Ó,, HÀ`BQÀnÁÌÍ|¿ÜÈqÓSÁQ»AX6B Plus ;UØ À`ÀÒÀaÀS; ÌÀÀ»ÝÇÙ%ØÓÌnçf»RÁyÁÍ CPU Èð%ÀÓ,,Ú%Ø]Ó,,HÀ`BQÀÒ;Ìú»RÈÌÀÌ Ç ÀÌÀÒÓ,,Ñ×ÀèÚÍØØÈ_çz'Àè; % À ÀÒÌ^ð À`BQÈú%ç »T

CPU ÒaØ ÌuÇÑÈÈÒaÈÈ AX6B Plus Ì'Àè%MÇÑÈÈÒaÈÈÀÒ;Ìú»R;ç'ÀÓ% CPU Ó]Ö »T Í, D ;UØ À` %rÀÌÁÛÇìÇÑÈÈÈÌÚj »R%QÇì ;z:è'CPU ÇÑÈÈ»RÀÌ ;ç'QÇìÁy;ç:YÍØØ Ìu ÀÒÇÑÈÈÀè;è»P'Ó] %ÁyÌ'À» (ÀìÀf Hardware Monitor Utility)»RÀÌÌèÀSÇÑÈÈYrÈÒÈà»R È_ç'ÀòÈàÌ%çì PÌÁ»T

CPU Ó]Ö À`BQ AX6B Plus ÁyÁÍÈdÈ ÌnçfÀÒÓ]Ö À`BQÓ,,ð »RN CPU NÈBÀñÈÀÀ ÓŠ ; ÀSÒ,,ÀÒÑBÀñÈà»RCPU ÍtÀñÈ_ÀòÈaÇÈÀT»RÁY;SØx;èÚÌ;èÍÈB Í, çì PÌÁ»T

ÀtÌèÓ,,Ú%ÒaÈÈ AX6B Plus %n'Àè%MÓ,,Ú%ÒaÈÈÀtÌè»RÀSÈ'Èì ÈàÀtÌèÀu»RÈ_Ñ'À p Òa ÈÈÀtÌè;%ÁQÓ,,Ú%»RÚaÀuÈ ÀpÀÌÁtÌèÓ,,Ú%ØHÓ]Ø; ÒèÑaÀÒÈ»Àè»RÇ; ÀÍÍ, ÒòÈ»Àè»RÌ_Ñ'Øx;è;%ÁyÌ'À» (ÀìÀf Hardware Monitor Utility) Ó Àè;èÀaè, ;çì PÌÁzÈÈÀ»T

ÀÓØyÀÒ CPU Èð%ÀÓ,,Ú%ç'PÌÁÈú%ç Í, D ;UØ À` ;z:Yç'PÌÁ1.3V À` 3.5V ÀÒ CPU Èð%À Ó,,Ú%»RÓ ;ç'ÒÁì CPU ÀÒÀQÈBÀB»È_Á ;ç'ÌÒt'À»T

PCI ÇÌÈP;uÈÌÚj SB-LINK ÈÌÚj ;z:Y;èÀÌÍt'ÈÌ Creative ÀòÈVÀÒ PCI ÇÌÈP;u»TÀSÒa Í, Òò PCI ÇÌÈP;uÈà»R;ç'ÌDÑ;èÀ ÀÓÈÌÚj %ÈúðèÀ`DOS Ú ÓìÀÒÀòÈÈVÈ=ÝU»T

;ç'ÀÌÁ%ç'ç'ç'ç'ç' BIOS Í, DÌÈ È'È_ÀèÈ'ÀSÌÀS BIOS Ù DÌÈà»R%4ARÀÌÓÒÀ;ç'ÒòYÁ%W»T

FCC DoC Ò'Ýì AX6B Plus ÌBÀÌ È È FCC DoC ÀTÌèÈxØèÑaÓ'Ýì »RÁJ ÀèÀSÌ]Ø Ìu ÀÒBQÀÒR%ç»RÀÌÌ^ %Ñ'DaÈq%YB »TÁY;ç'ÀS;ç'ç'ç'ç'ç' ÇÈ»RÈÌÇO-9001 Ó'Ýì %Á%ç'ç'ç'ç'ç' R À× ÀÌÁ`Ýì »T

Èç'ç'ÀÒÚÌ;èÍÈB %ç'PÌÁ_ÚYÁ ÀÒ AOpen Bonus Pack CD Òa%ÒÁÌÌmYÈç'ç'ÀÒÚÌ;èì' À»RÀÌÌf Norton Antivirus»SADM»SAOchip»SHardware Monitor %ç'Áy»SSuspend to Hard Drive %ç'Áy»R;ç'Y%èBIOS flash %ç'ÁyÌ'À»»T

1.1 ÍhÈ

¿UØ Æ`ÁÆÀ»	ATX
¿UØ Æ`%Ð%¿	305 mm x 244 mm
CPU	Intel Pentium II ÍSÌ`ØØ
¿UÈ`Øeß	SDRAM Æè Registered SDRAM»Rl 68-pin DIMM x4»R Íæ%ÈvÐ,¿zÓW 1GB»T
ÁðÁ È`Øeß	%ÓÁðÁ CPU ¿u%h
ÍÓ% Ìi	Intel 82440BX AGPset
Ûi¿cØè	ISA x3, PCI x4 Óa AGP x1
À ÀTÈ	2 Çi UART 16C550 ÆÈvÁØ RS-232 À ÀTÈ »R¿tÁÍ%Q Çi UART ¿z%pÍÁÆ ¿•×`Øi Ìi»T
¿ÇÀ È	1 Çi ¿z%pÍÁ SPP/ECP/EPP %eØðØeÑaÁÓÁYATÈ
Floppy %ÐÇÈ	1 Çi ÍÉÀ»ØeØeØ ÍtÈÏÛj »R¿zÁé¿è 720 KB»Rl.44MB Æè 2.88MB È À»ÁØ 3.5 ÆeØeØeØ »R%ß60KB»Rl.2MB È À»ÁØ 5.25 ÆeØeØeØ »T
IDE %ÐÇÈ	2 Çi IDE Channel ¿zÍtÈÏ 4 Çi IDE òað~ (ÍŠØeÆe CDROM)»R%pÍÁ PIO mode 4»SBus master»RÆè Ultra DMA/33 Í¿ÐaÛ ÒiÀ»»T
SCSI	%ÓÁð Adaptec AIC-7880 ÍÓ% »T
USB %ÐÇÈ	2 Çi USB ÍtÈÏÛj »RBIOS ¿z¿t USB Bi ÈaÍ`À»¿zØi ÚÚ ÐaÍèÁØ AT Æè PS/2 Ûp×] »T
PS/2 ÑaÓÁ	%ÓÁð Mini-Din PS/2 ÑaÓÁÍtÈÏÛj »T
Ûp×] %ÐÇÈ	%ÓÁð Mini-Din PS/2 Ûp×] ÍtÈÏÛj »T
RTC ÓaÓ,ÀÚ	RTC À Æ Intel PIIX4E chipset %Ó»RÆè¿èCR-2032 Øj Ó,ÀÚ»TÀfÁX%ÁøÈÖÖ,Ñ××`»RÍ] ÐÑÆè¿eÓ,ÀÚ»T
BIOS	AWARD Plug-and-Play, 2M bit Flash ROM BIOS»T%p ÍÁÁyÓÇÁÁÓÁÁ¿Ø»R¿nR%ß%ß%ß»T

ÑÀàl

Ú Ð»Ð"Ø (Suspend to Hard Drive)	¿ëBIOS %PÍÁ»RÚ<À†ÁR¿òÁvÁ0%¿ÁQÁÄÖRÀö0e0eØ %0»F %F%QÀ0Ø"Ø Èä¿ùÁ ¿†¿i Ç %¿ÁQÍvÇÈ»T0÷Áe¿EESA Íh È ÆÖEvÁ0 PCI VGA ¿u»RSound Blaster ÆÖEvÁ0ÇÍÈP ¿u»T
0a06Ø 00ÈÈÐ"Ø (Modem Wake Up)	Í'0]ÉdÈ Á0×^0 Ír¿f»R¿¿¿YÜá¿e¿•ÈÍÁ»Áe%0ÈÍÁ»0à 06Ø (Àf AOpen F56/MP56)»RB Á†ÈAsÁÍ0,,06ÐÁÍÈä À0ÈäÈÏB¿»T
0 0 00ÈÈÐ"Ø (LAN Wake Up)	È†Ái Áe¿e%PÍÁÍ, DÍ¿múÁ00 0 ¿u0a0 00Í€B »RÈ' ¿¿ ¿YÍ'0]ÈeÈ 0 0 Í Û Á†È»T
ÁŠÈäÐ"Ø (RTC Wake Up Timer)	È' ¿¿YÍÁŠ%QÇiÁeÁŠÁ0ÈäÐ»»RB Á†ÈAsÍ, ÇiÈäÐ»à0 ÈäÐ"Ø »T
ÀaÁS¿ ÍÁÁ»YÇÚ%×^0	ÈÄÈÐÍ%ÀaÁS¿ ÍÁÁ»YÇÚ%×^0 (Synchronous Switching Regulator)»T
0]0,,ÆÁ`BQ	ÁyÁÍ CPU Èð%ú0,,Ú%0]0,,ÆÁ`BQÁ0¿mú»RÈ†Ái Ç ÁiÁ0 0,,Ñ×ÁeÚÍ00»R¿¿¿¿Áe¿ %¿ Á Á0Í^0 Á`BQÈú%¿ »T
CPU 0]0 Á`BQ	Ñ CPU ÑBÁñÐh0]0ŠÍñ¿Á0ÈäÇiÍ, ¿i ÐÍÁ»T
CPU ÇÑÈÈ0aÈÈ	Ñ CPU ÇÑÈÈYrÈ0ÈaÍ, ¿i ÐÍÁ»T
Á†Èe0,,Ú%0aÈÈ	Ñ Á†Èe0,,Ú% (5V»S12V»S3.3V 0a 2.8V) %¿ ¿ÈqÈaÍ, ¿i ÐÍÁ»T
SB-LINK ÈÍÚj	¿¿eÁi Í†ÈÍ Creative ÆÖEvÁ0 PCI ÇÍÈP¿u»T

1.2 Ú Ð»Ð"Ø (Suspend to Hard Drive)

»^Ú Ð»Ð"Ø »%À_Æ È_çòÀvÀÓÀÏÈÄÄÖR»RÈ`ØeB ò Èa»RÜYÓ ÌvÇËÚ<À†ÀöÏSÓe%Ø»TÍ^
 ÁúÀÏÈèçÁÓç Úpó,,»RN %f%QÀØÐ"Ø Èa»RÈ' ççYÄ×ÈÏÀoÀ Ç ÁiÀÖ%ÁQÄÖR»R%Ø÷Ìç
 Çi Win95 Ð"Ø ÄH' »R%Ä%Ø÷ÇÄÑ†Èi ÈaÚÍçèÏ' À»»T

ÀfÄXÈ' ÁÍ 16MB È`ØeB »RÄÖÖ %Äö%Ø÷Ç€16MB ÒeØeÄBÐ»Ái Ú<À†ÀÏÈÈÈ`ØeB »T×e
 ÄqÑ_È' çÌBÑÁeçèØa VESA ÍhÈ ÄÈvÄÖPCI VGA çu»RÖa Sound Blaster ÄÈvÄÖÇÍ
 ÈPçu%e APM Driver»TÑ Í^ »RÄöÇaÄöPÍÈ'Ú çèÄö ÄOpen PT70 VGA»RAW35 (ÇÍ
 ÈPçu) Öa MP56 (ÖaØ6Ø +ÇÍÈPçu) çY×eÁ' ÍæÄeÄÖÄÈvÄä»T

ÀÖçnúÄÖÍnÄS% Ä|Àf%f »X

1. Ðz% BIOS setup»RPower Management à Suspend Mode Option»RÜ Øö
 "Suspend to Disk"»T
2. Ðz% BIOS setup»RPNP/PCI Configuration à PnP OS Installed»RÜ Øö "No"»T
 Í, ÁeÈ, BIOS ÁÍØ Ñ`àöÀ Á^ ÁSÄÏÈèØ Ñ»»T
3. Ð"Ø ÈaÚ ØöÐz% DOS Öi À»RÄfÄXÈ'Æ Win95 ÁeçèÄæR×eÄsÐ"Ø çì Ì' "Windows
 95 Starting ..." ÈaÄ %f "F8"»RÜ Øö "Safe Mode Command Prompt Only"»T
4. È_AOZVHDD.EXE Í, Çi %ÄyÍ' À»×aÖ†Ä C ÒeØeÄÖÈçòÚ %f »T
5. % Ä|%Q»XÄeçfile ÈeÖa (ØRçeÄ FAT16 ÚaÈnÄÏÈ)

×eÚ %f%ÄTÄ ç` ÀsÍSÓe%Äöçù%QÇi Ú ÜYÚa»RçeÁi Ú<À†ÀÏÈÄÄÖR%eÈ`ØeB ò Èa»X
 C:>AOZVHDD /c /file

×e×eÄSÈ'Æ ÁpÁÍÄÈ ÄÖÍ†P ÒeØeÄBÐ»È_Ái Ì½çÚ ÜYÚa»TÁi Àf»RÄfÄXÈ'ÄB2MB
 çUÈ`ØeB Öa4MB ÄÖ VGA È`ØeB »RÄÖÖ Í_Ø÷Ç€Äö%ØB6MB (32MB+4MB) ÄÖÍ†
 P ÄBÐ»TÄfÄXAOZVHDD Áp%Ä Í†P ÄBÐ»RÈ' ççYÄeçè DOS ÄÖ DEFRAG Í'
 À»ÄeÆ Win95 ÄÖ`ØeØeÇÄi i' À»»%Äi ØyÍ' ÍSÓe»RçYÍ½çÄÈ ÄÖÍ†P ÄBÐ»»T

% Ä|%X»XÄeç/partiton ÈeÖa (ØRçeÄ FAT16/FAT32 ÚaÈnÄÏÈ)

Äeçè AOZVHDD ÀsÍSÓe%Äöçù%QÇi %ÍÈÈe»RÁi Ú<À†ÀÏÈÄÄÖR%eÈ`ØeB ò Èa»T
 ÇÈÄeçÈÍ, Çi % Ä|%Äv»RçÌBÑç ÀsÍSÓe%ØSÈu%QÐ ÄBÐ»TÄöÇaÄöPÍÈ' ÈöØöçÖÁi È`
 ØeB Úi çcÄÖçzÈúÄÄf»RÓSÈuØ %ÄÖÖeØeÄBÐ»TÁi Àf»XÇj È' çòÀvÁÍ32MB ÄÏÈ
 çUÈ`ØeB Öa4MB ÄÖ VGA È`ØeB »RÁi çÖÁi ÇfÖÖ%eÈBÄ 64MB çUÈ`ØeB »RÄÖÖ
 È' ÍæÄeÄeçè ÒeØe%ÄyÍ' À' (Äf fdisk) ÖSÈu%QÐ 68MB (64MB+4MB) çY%ÄnÄÖçö
 %ÍÈÈeÈ »TÍ^ ÁuÚ %f%ÄTÄ ç` »X

C:>AOZVHDD /c /partition

ÑÁàl

- ÀfÄXË· ÄÖÏŠÖeÄdÄÍ;Ö%ÚÍeÈeÈ »R%a¼¼ÄaË çòÁvÔeÔe%ÄÖÖ ÈàÛ ç¼»R×e%BAéçè
Í, Çí¼ Ä | »T
6. ÇÄÑ†Èi ÈäÄ†Èè (Reboot)»T
 7. ÜáçèÍ]Æ,Á ÜpÀ» (Momentary) Suspend switch»RÄèÆ Û Á Win95 Ð"Ä}çmúÄ
¼¼ÄÖ»^ÖaÈÈ»%RÉ(Ç¼Ä†ÈeDz¼ Suspend to Hard Drive ÖiÀ»»RÍ ^ ÄuÈ_Ö,,Ñ×Ð"Ý
Ý ÍÆ»T
 8. ¼f¼QÀÖÐ"Ø Èä»RÄ†ÈeÍ_Ñ"ÄöÈäAoÁ ç Äv¼¼¼ÄQÍvÇÈ»T



ÐÍÁz: xèÄqÑ_»RIntel Bus Master and Ultra DMA/33 IDE
driver ÄY¼¼ÄÖç Öa Suspend to Hard Drive çmÉuÆÈv»T ÄŠ
ÖaÍ, Çí driver çzÉúÑ"ÄéÄ†ÈeBöÈ, ¼¼¼ÇÁS»RÇjÍ, Ì' Í, ÖöÈ»
Äè»R×eÍÖÈ¼ÄÖ driver»T

ÐÍÁz: SCSI ÍŠÖe¼¼¼pÍÄÄÖçmÉu»T



Î½ö: ¼FÄT VGA çuòxÍ ÖiÆÈ VESA ÆÈv»RÄYçSÖRçèÄ
Suspend to Hard Drive»T

AOpen PV90 (Trident 9680)
AOpen PT60 (S3 Virge/BIOS R1.00-01)
AOpen PV60 (S3 Tiro64V+)
AOpen PT70 (S3 Virge/DX)
ProLink Trident GD-5440
ProLink Cirrus GD-5430
ProLink Cirrus GD-5446
ATI Mach 64 GX
ATI 3D RAGE II
Diamond Stealth64D (S3 868)
Diamond Stealth64V (S3 968)
KuoWei ET-6000
ATI 3D RAGE PRO 2x (AGP)
PLOTECH 3D IMAGE 9850 (AGP)
CARDEX S3 Virge/GX (AGP)



Í½ö: %fÀTÇÌÈP¿uÓxÍ Òi ¿zØR¿èÁ Suspend to Hard Drive»T

- AOpen AW35
- AOpen AW35 Pro
- AOpen MP56
- Creative SB 16 Value PnP
- Creative SB AWE32 PnP
- ESS 1868 PnP

ÀfÄXÈ' ÄÖÇÌÈP ¿uAs Suspend to Hard Drive ÁúÍJÄ|¿ÜÈq%
ÁQ»R×èÄgÖ¿Èi ÆuÒuÁpÆ ÁpÁÍ%pÍÄ APM Bi ÈäÍ' Á»RAYÁS
Òä³Ä»T



×èÄqÑ_: USB ¿mÉú¿òÁvÁY¿Ö¿c%ÜÖa Suspend to Hard Drive È¿Ái Í Òi »TÀfÄXÈ' Í, Í' %ÄÇÄŠÄÖÌ' ØY»R×èDz%
BIOS»RIntegrated Peripherals à USB Legacy Support»T
Ý ÈÖ USB Legacy ¿mÉú»T

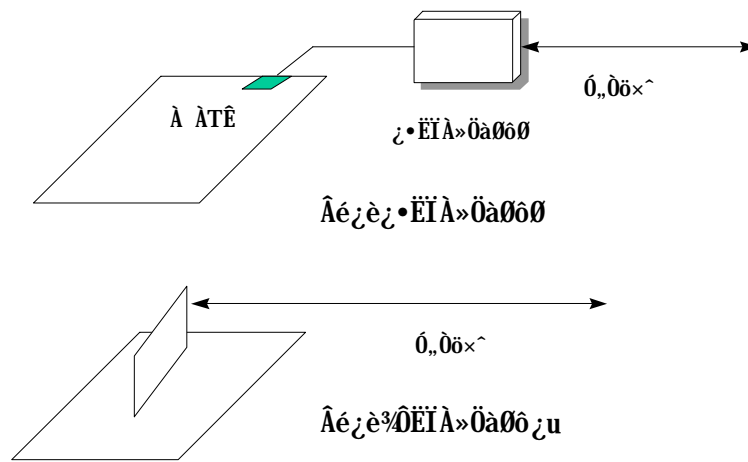
ÑÁàl

1.3 Á\Ó,Ñ×ÒàØòØ ÕÕËËÐ"Ø (Zero Voltage Modem Wake Up)

ÁóÇæÀsÀÓÀi ÇÈÈb×ñÀØ Modem Wake Up çUÇ€Æ ÈŠÓ ÀsÉ^Ó ÁÓÝ Ø ÆØRÀaUp% Ä| ÆÈÚaÆuÓ,Ñ×ÀØÇÑÈÈÆ ÄpÈÈ% ÚúÈa)»RÍ, Ð çUØ Ä`Û Ý^ %m%pÍÁÐaÌèÀØgreen PC suspend mode»RÀ Í, ÁÝ%ÄÀsÁóÇæÈb×ñÀØ×uÍ %»T

Ûáçè ATX soft power On/Off»RÁóÇæççYB ÄÏèÀsÁÓç Ý Ø ÆØÄØR%Æ (çYÁÓÓ,Ñ×Øò Ì`çñú%»ÀØ suspend mode ÄÝ%Æ È`çÛÝ ÍÆÏèÓ,Ñ×) ÀóÈaÈÌBçÓ,Øò»RÀfÁÓË_çz çYÈÌÄ ÝÀÙ Ø ÓaÐaÉ^Ø ÆØçñú»T

Í|×ñÆ %ØÈÌÄ»ÓóÆ ç•ÈÌÄ»ÒaØòØ »RÍ½ççY%pÍÁÓÕËËÐ"Ø ÆØçñú»RÀ Æ Äéçèç•ÈÌÄ» ÓaØòØ ÆØÈaÛZE »RÈ`çÌÐÑB ÓaØòØ ÍSÄ Ð"ÈiÁØÄØR»TOpen ÆØ AX6B Plus ÓaØÈ ÈÌÄ»ÒaØòçuÍ½Æe%WÉdÈ ÁØ×^ð »RÁiçYÈ`Ý|ÐÑÈ Ð`ç À Ó,Ñ×»TÀmÀÓ»RCj È`ÑbÄéçèðÕ ËËÐ"Ø çñúÁØÒò»RÁóÇæÀoPÍÈ`Èpçè AOpen ÆØ%ØÈÌÄ»ÒaØòçu (F56 Æe MP56)»T



Àéçè%ÖËÌÀ»Öà06çüÈä (AOpen MP56)»X

1. **Ð¼** BIOS setup»RPower Management à Modem Wake Up»RÛ Â Enable»T
2. ÀŠÖàË'ÁaË Ç€ÀsÐ"Ø ÈäÈ À ÄÖÛÍçèÏ'À»»RÂYË_Âp0~À »^ÈiÈä»%0†Ìi%¼ÁeÁbçè Suspend to Hard Drive çñú»T
3. çYsoft power switch Ý ÍÁÀÏè»T
4. çY4-pin ÄÖ Modem Ring-On Èä×^»RÍ†ËÏ MP56 ÄÖ RING Í†ËÏÛj Öa AX6B Plus ÄÖ WKUP Í†ËÏÛj »T
5. È_Ö,,06×^Í†ËÏÄ MP56»TÀ€Èi»ZÌ'ÀsË'ççY0i0i Modem Ring-On ÄÖçñú¼»T

Àéçèç•ËÌÀ»Öà060 Èä»X

1. **Ð¼** BIOS setup»RPower Management à Modem Wake Up»RÛ Â Enable»T
2. ÀŠÖàË'ÁaË Ç€ÀsÐ"Ø ÈäÈ À ÄÖÛÍçèÏ'À»»RÂYË_Âp0~À »^ÈiÈä»%0†Ìi%¼ÁeÁbçè Suspend to Hard Drive çñú»T
3. çYsoft power switch Ý ÍÁÀÏè»T
4. È_Öà060 ÄÖRS232 Èä×^Í†ËÏÀ6 COM1 Àè COM2»T
5. È_Ö,,06×^Í†ËÏÄ Öà060 »RÌ^ÁüçÏÐ"Öà060 Ö,,Ñ×»RÌ'Às0i0iÆ:Ár»Z



Í½ö: ç•ËÌÀ»Öà060 ÄÖ wake up Èe00Æ çè COM1 Àè COM2 Ç†ÍuÈ0Ï »WÖËÌÀ»Öà060 ÁyÆ çèÍ†ËÏ RING (Öà060 %h) Öa WKUP (çU0 Ä`%h) ÄÖÈä×^Ç†Íu»T

Í½ö: Suspend to Hard Drive Öa Modem Wake Up ÑwÈ†Áéçè»R Æ ÍÄÜ Ø ÖaÀÆÏ, ÐaË^ÄÖÍaÀe0èÀ^¼ Èñ»T



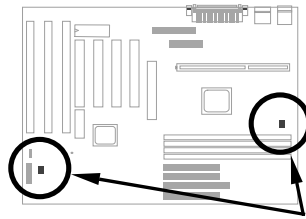
×eÁqÑ : Àéçèç•ËÌÀ»Öà060 ÄÖ06»RÖa060 ÄÖÖ,,Ñ×Ì_çÏÐÑÁ^Á ÀsÐ"ÈiÄÖÄÖR»WÁi Àéçè%ÖËÌÀ»Öà060 ÄÖ06»RÌ_ÁdÁÍÍ, ÖoÇÇ Á %¼»T

ÑÁàl

1.4 Â†ÌèÓ,,Ú¼ÔäËË (System Voltage Monitoring)

AX6B Plus ÁyÁÍ%QÇiÓ,,Ú¼ÔäËË†Ìè»TN È'Ð"ÈiÓ,,Ò¼Áú»R Í, ÇiÔäËË†ÌèÎ_Ñ'Á P Àr ÔäËË†ÌèÄÖ%ÁQÓ,,Ú¼RÚaRúÆ ÁpÁÍ†ÌèÓ,,Ú¼ÐlÓl%Ð¿ ÔeÑaÁÖË»Áe»TÇj ÁÍÍ, ÔoË»Áe»R Î_Ñ'Ôx¿eÚÍ¿eÍ'À» (Áf AOpen Hardware Monitor Utility) Ó Áe¿eÁæÍ, ¿i ÐÍÁZËeËÁ»T Í, ÇiÔäËË†Ìè¿¿YÔäÆ CPU Èð%úÓ,,Ú¼»TÍ, Æ Ôx¿e BIOS Ôa Hardware Monitor Utility (ÚáÀhÚÍY à aohw100.exe»RÁp%¼100 Æ Á ÁÁ¿ÔÖÓxi) Á ¼ ÔWÁÁÔ»RÍJ ÐÑÁé ¿eÁ Áp¿ÁÍŠB »T

1.5 ÇÑÈËÔäËË (Fan Monitoring)

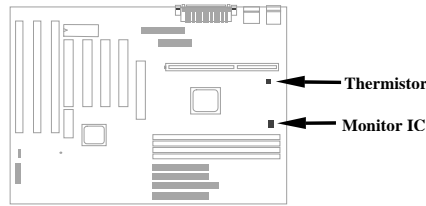


Í, Ð ¿UØ Á`¼ÁÍÁúÇB-pin ÁÖÇÑÈËËÚj CPU_FAN Ôa FAN»R%QÇiÍÓCPU ÇÑÈËËé ¿e»R¿†%QÇi Áy¿¿eÁ Ø Íú¼ÁÁÖÇÑÈË»TÍ, Çi ¿mÉúÆ Ôx¿e BIOS ÔaÚÍ¿eÍ'À» (Áf Hardware Monitor Utility) ÁiÁ ¼ ÔWÁÁÔ»RÍJ ÐÑÁé¿eÁ Áp¿ÁÍŠB »T



×eÁqÑ_ : È' ¿ÐÑÁé¿e 3-pin ÁÖÇÑÈË»RÍ, ÔoÇÑÈË%¼pÍÁ CPU ÇÑÈËÔäËË¿mÉúÁi Ô-ÁÖ SENSE ÈeÓÓ»T

1.6 CPU Ó]Ö Å`BQ (CPU Thermal Protection)



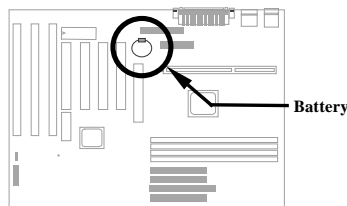
çÖçUØ Ä`ÍmÍÑBÁñÄ`BQ×`ð »RNÑBÁñÈÄÄ ÖŞç ÁŠÖ„ÄÖÖaÇaÈaGRU ÍñÑñÄöÈaÇÈ
 ÁT»RÄYçSÖxçèÜÍçèI`Ä»(Äf Hardware Monitor Utility) Í, çí ÞÍÄzÜÈBe»TÍ, Çí çíñúÈ
 ðxçè BIOS Öa Hardware Monitor Utility ÄiÄ`¼ ÖWÄÄÄÖ»RÍ JÞÑÄéçèÄ ÄpçÄÍŠB »T

1.7 ¼pÎÄÄyÖÇÄ†ÄÖ BIOS (Multi-language BIOS)

ÆÈÍ¼Äè AOpen ÄéçèÄaÍaÄèÄÖ¼pÎÄ»RAOpen Í€B Ð"Í, Í»Ä ðx0]¼ÄpÄÖÖÜÜi»RÍñÄ
 Á[ÄR¼WÄiÄÍÄÖÈ-YU»RÄÄçmÖI, Èzçí Í¼ÄèÄyÖÇÄ;ÄÄçÖ BIOS ÄÖÄöÍ_»T
 È`ççYÈ„ÄöÇaÄÖÖ Èð¼ç ð È`ÄiÇÈÄÖ BIOS ÄÄçÖ (ÓÍÆ ¼¼¼) »RÇÄÑ†Ü_Ü Ä È`ÄÖçUØ
 Ä`¼1»TçYÄüD¼BIOS Setup ÍvÇÈÈa»Rç`ÇÈÄ ¼çF9 Ä Üp»RÍ_ççY¼ÜÍÄÄ¼¼¼¼ÄÖIv
 ÇÈ»RÄRÄ ¼QÄÖ9 Äj çÄoÄ Ço¼¼ÍvÇÈ»T
 ÄöÄYÍ, ÇíÄöÍ_Ö È`ÄsÍmŠBIOS ÐÍçðÈä»RÈ_ÄÍÄiÄfÈ„»T

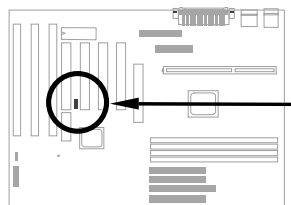
ÑÁàl

1.8 3/4 Ñ÷Áé¿èÓ,,ÀÚ (Battery-less Design)



Æ³WBeÚÍÚ Á` »BOpen Æ AX6B Plus ÈP¿è³W battery-less (3/4 Ñ÷Áé¿èÓ,,ÀÚ) ÆÓIn Çf»T¿ Ç€ ATX Ó,,Ñ××³4Áó%f »RÈ' Á\3/4 Ñ÷ÁRÁé¿èÓ,,ÀÚ'ÁèÓ,,Ñ×ÍÓRTC (real time clock) Óa CMOS Setup»TÍ, ÓòInÇfÁÓÀÉÍSAÁ »R¿¿¿ YÚñÁ\Ó,,ÀÚÁdÓ,,Èä»RÙ ¿CPU ÚhÍ%Óa CMOS Setup Í¿Ò Èà»T³4ÓJ Æ³W/ Á\Áé¿èÁæÁÓÓ÷Ç€»RÁóÇæÁæÍ ^ ÚYÁ ³W/QÚ Øj Ó,,ÀÚ (CR-2032) »RÁi ¿YÁfÁXÈ' ÑbÇ€¿èÓ,,ÀÚÁÓó»R³4¿¿ Y³4¿¿ èÈ_¿ÁÁóÈÓ»T

1.9 SB-LINK ÈÏÚj (SB-LINK connector)



¿ò¿UØ Ä` Î'Áé³W/QÇSB-LINK ÈÏÚj »R¿¿¿PÍÁ Creative ÆÈVÁÓ PCI ÇIÈP¿u»TÀŠÓa Í, Óò PCI ÇIÈP¿uÈà»R¿IÐÑ¿èÁ ÁÓÈÏÚj %ÉúòèÁ^DOS Ú Ói%f ÁÓÆÈVÈ-ÝU»T

İP³XİÓ İŞB ÀŞÒà

çÖÍÓË_çYÍqÁá0;DzÁ0% À»RÖ»À ÀfÀ ÀŞÒàË'ÄÖÄİè»R×è×eÓ ÀaÑíÍ, ÁáÀSà[ÁíÀŞÒa»T



×è%f%ü: ESD (Electrostatic Discharge) ÆËÚcÓ,,Á Ó,,
%ÄÑ_»RçéÁ ÛÈB Ó,,Ò (IC) ÁöÈvÁ Á Á ÚcÓ,,ÑaÚÍÄÖ
Ö%Be»RÄnÄÖÑ"ÄeÑ]ÍSÌ' ØÓ»RÖèÖèØ »RÜi çcu%èÄp
ç]ÄÖÖ†ç Á Á ÐaÈq»TÆ%WÓŠÄÖÚcÓ,,Á Ó,,ÁíÑ}ÝrÍÓ
% »R×èÛ ÀÆfÀTÄÖÖŠÄÖÈÓÆZ»X

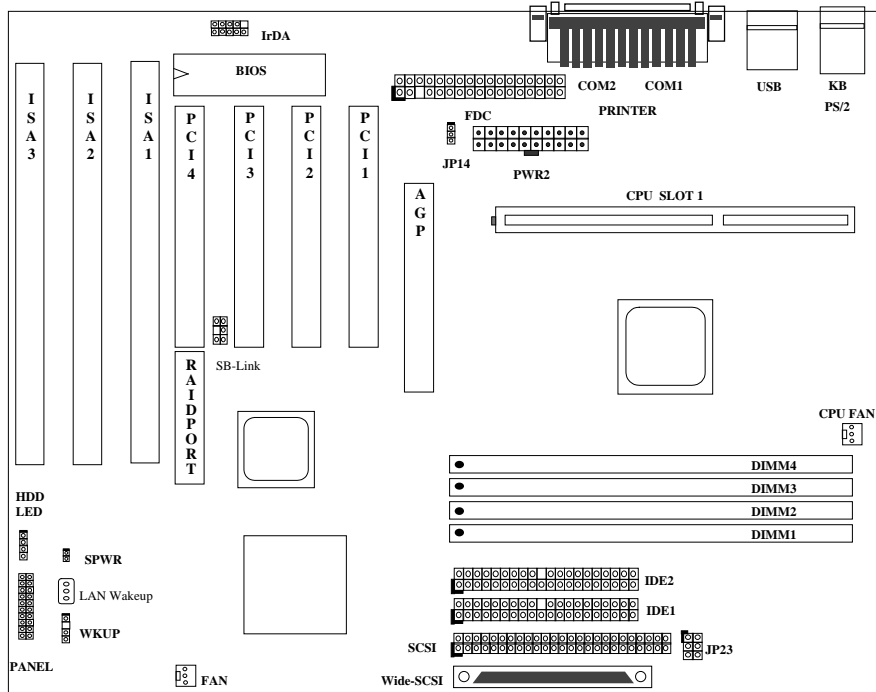
1. È¼ÄUË' %ŠÑÁÍaÀÇÇED"Ä}ÀŞÒaÓ†ç »RÁpÁy%üÖf%½
ÇÈË_Ó†ç ÀöÄÖÚcÓ,,çnÒa%Ä çi»T

2. ÀsË' ÀŞÒaÓ†ç Èã»RæÀÈË%hÁÍËËÄr×^ÄÖ%ü Ü »R
%ÄÍ»ç ÄÖÖ,,%Ø†ç À Í¼ÁÍÀs× »TÀfÄXÄdÁÍ%üÜ »R
×èÄéçèç À çzçYÄØ% ÚcÓ,,ÄÖÍÜç_Á»»RÁíÖaÄİèÓ†
ç %èçUØ Á`ÈİPİ»T

İŞB ÅŞ0à

2.1 Jumper 0aËiÚj ÄÔÀ 0~

¿Y¼Æ Æ¿U0 Ä`¾ Jumper Ä^ ËiÚj (connector) ÄÔË:0~ 0é»X



İŞB ÅŠ0à

Jumpers:

JP14: İ ^ Ê¼CMOS
JP23: AGP Ratio

ËİÚj:

PS2: PS/2 ÑàÓÁËİÚj
KB: PS/2 Ûþ×J ËİÚj
COM1: COM1 ËİÚj
COM2: COM2 ËİÚj
PRINTER: ÀJÄ Ø ËİÚj
PWR2: ATX Ó,Ñ×ËİÚj
USB: USB ËİÚj
FDC: Floppy Í†ËİÚj
IDE1: İP%Qİi IDE Í†ËİÚj
IDE2: İP%Xİi IDE Í†ËİÚj
CPUFAN: CPU ÇÑÈÈËİÚj
FAN: Ø İàÇÑÈÈËİÚj
IrDA: IrDA (Æ ç•×^) Í†ËİÚj
HDD LED: HDD LED Í†ËİÚj
PANEL: Åv¼ ÇÈÀ` Å Ûþ0aÛ` 00Í†ËİÚj
SPWR: ATX Soft-Power Switch ËİÚj
MODEM-WKUP: Modem Wake Up ËİÚj
LAN-WKUP: LAN Wake Up ËİÚj
SB-LINK: Creative PCI ÇİÈPçuËİÚj

İŞB AŞÖà

2.2 Jumpers

İ, D ÇUØ Ä`Æ ÈPçèİ Jumper İncf»RÈù»QÄÖ Jumper Æ İ ^ ÈCMOS»RÄpÇUÇÆ ÇèÄ ÄeÈ`È\`xi ÄÖÈaÇi»T

2.2.1 Ü Â CPU Úhİ%

İ, D ÇUØ Ä` ÇZ YÄöÈaÈÖİ CPU Ó,,Ú»R ÄYÇZ YB È`Às CMOS setup ÇÄİnŞ CPU Úhİ%»RÄiİ]DÑÄèèÄ jumper»Tçç•»RÇZ YÍ`Ó]ÇÄ YÍ`À»RÈ_ÇÜxèÄÖ CPU ò Èe È`Ü Â EEPROM ÇÇTÄfÄÖÇQÄi»RÖfÇ CMOS Ü ÇZ ÇWRE`Î_ÇèÖüÇÇÄİnCPU Ó,, ÚÄçi ÇMEÜ »RÇÇÄÇÄÈÇhÄÖÇİD`Ó,,Ø Ç İuÜaİ CPU ÓöY ÇWTAÍÍ, ÇÆ ÇQÉ ÈP çèİ] jumper İncfÄÖ Pentium ÇUØ Ä` ÇUÇÈÄÖÈ=ÿU»TİnŞ CPU Úhİ%ÄÖÇ Ä|Æ »X

BOIS Setup à Chipset Features Setup à CPU Clock Frequency

(ÇZèüÄÖİnŞÇaÄİ 66»S68.5»S75»S83.3»S100»S103»S112 Öa 133.3 MHz)

BOIS Setup à Chipset Features Setup à CPU Clock Ratio

(ÇZèüÄÖİnŞÇaÄİ 1.5x»S2x»S2.5x»S3x»S3.5x»S4x»S4.5x»S5x»S5.5x»S6x»S6.5x»S7x»S7.5x Öa 8x)

CPU ÇÖİ»Úhİ% = ÇÜÜhÇ x Ç·Úh

INTEL Pentium II	CPU ÇÖİ»Úhİ%	ÇÜÜhÇ	Ç·Úh
Pentium II - 233	233MHz =	3.5x	66MHz
Pentium II - 266	266MHz =	4x	66MHz
Pentium II - 300	300MHz =	4.5x	66MHz
Pentium II - 333	333MHz =	5x	66MHz
Pentium II - 350	350MHz=	3.5x	100MHz
Pentium II - 400	400MHz=	4x	100MHz
Pentium II - 450	450MHz=	4.5x	100MHz
Celeron 266	266MHz	4x	66MHz
Celeron 300	300MHz	4.5x	66MHz
Celeron 300A	300MHz	4.5x	66MHz
Celeron 333	333MHz	5x	66MHz



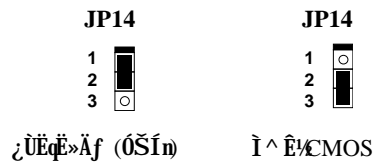
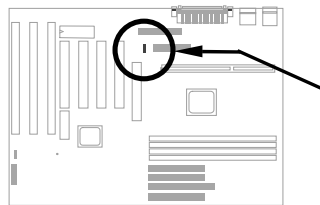
PİAz: INTEL 440BX İÖÇ İiİaÈÄÇZÇpİÄ 100MHz CPU Ç· Úh»R103»S112 Öa 133.3MHz ĐaÄèÇÖİ»İ Öiçè»Tİ, ÄaİnŞÇa ÇŞĐhçi BX İÖÇ İiÄÖİhÈ »R ÇZÈÜN`Ó ÄçİeİÇÄÄÑ)Èq»T

2.2.2 İnAŞ CPU Ó, Ú½

çÖçU0 Ä` %pİİPentium II VID çmú»RçzàöEäEÖİ CPU Èð%úÓ, Ú½»RÄp»uí Às 1.3V Ä 3.5V %ÄD»»T

2.2.3 İ^Ê½CMOS

JP14	İ^Ê½CMOS	ÄfÄXÈ' ÄeÊ` Äi İnAŞÖÀİeÈ\»xi È»RçzàöEäEÖİ È_İ aBE
1-2	çÜÈqè»Äf (ÓŞİn)	İnEİC»D»Rİ^ È^ÄYÇÄİnT^` dİÄÖİnAŞçäÄu»R%»Éú
2-3	İ^Ê½CMOS	ÇÄNİD"Ø »T



İ^Ê½CMOS ÄÖİ' Áá:

1. Ý İÄÄİeÓ, N»»T
2. ÄöEÖ PWR2 %ÄÄÖATX Ó, N»»^»T
3. Äpçi **JP14** Äi ÄsÄÖÄ ò»»RE_D »ÄİfÖnÄ %fÄi»RÄ È^Ä-3 ò"Ä %Ä»T
4. %QÑ"ÄuÈäD»Äu»RÄ %fD »ÄİfÖnçÄNİE^ ÄoÄ 1-2 ò"Ä %Ä»RÄ İpÄÄÇ ÄiÄÖÄÄÖR»T
5. È_ATX Ó, N»»^ÈİÄo PWR2»T
6. ÇÄNİçİD"Ó, ò"Ó, N»»T
7. ÄfÄXN»ÇçİnAŞNİÄÖÄİeÈ\»xi »RçzàöEäEÖİeÈi ÈäEä»RÄ %f [DEL] ÜpDz% BIOS Setup İvÇE%»RARÄ ÄSNIÄÖE\»xi »T



İ½ö: ÄfÄXÈ' ÄÖÄİeÄnÄEĐhÜhÄiN ÈÖÄeİ|Ä|D"Ø »RçzçYİ^Ê½ CMOS»RB ÄİeÄoÄ ÓŞİnÄÖÄÄÖR»T

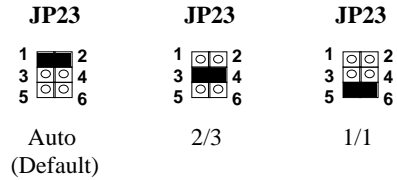
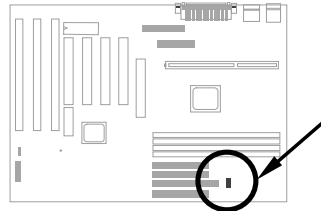
İ½ö: È½ÄÄEçè JP14 %Äç»R È' %nçzçYçè <Home> ÜpÄi İ^Ê½ CMOS»T% Ä|Æ Ä Ä <Home> Üpİ^ÄuĐ"Èi Ó, N»»Y »Rİ, öaÄİeİ_ N"ÄöEäÈ CPU İnÄE Pentium II 233MHz»TEİ%fÄi»RE' çzÄRÄsö ÖöÄE Äf»RDz% BIOS Setup İnAŞ CPU Ühİ%»T

İŞB AŞÖà

2.2.4 AGP Ratio

JP23	AGP Ratio
1-2	Auto (default)
3-4	2/3
5-6	1/1

AX6B Plus İ¼Äe¼M, Çi jumper»Rçz
 È_ AGP İ¼SÄÄÖa CPU 100MHz (ÄaÄöÄ ÄÄ) ç_ÜhÄaÄS»T
 À ÇÉÍ, Ö ÈIÄÖÖ»RÈ' İ¼ÄÇÈÜ È¼Q¼ Ö ÄÄÖ AGP ç_u»TÄ
 Äá AGP ç_uÍ|Ä|ÄiÄ 100MHz ç_Üh»RÄÄÖNÍ(GÄÄhÜhç¼
 Èè»T



CPU öa BX İÖ¼ İ¼ÄD»ÄÍ¼QÈ "66/100" ÈeÖÖx^»RçzçYÄöÈÄÈÖÎ AGP clock»RÓ
 jumperless ÄÖçUØ Ä`ÄiÖ»»RÍ, Ä ÄöÇÄÇÄÖÍçf»TÑ Äéç66MHz ÄÖ Pentium II CPU
 Èä»RçpØ İÖ¼ NÈ_ CPU ç_ÜhÖaAGP bus İ¼ÄÄaÄS»RÄÄÖRçj È_Äi¼ÄhÜhÄ100MHz
 Èä»RAGP ¼ÄNÄÄ 100MHz»T

ÄöÄ 100MHz ÄÖ Pentium II CPU»RçpØ İÖ¼ NÈ_ AGP İ¼Ä 2/3 ÄÖ CPU ç_Üh»R¼n
 İ_Ä Ö»»RÑ CPU Än 100mMHz Èä»RAGP ç_u¼Ä_Ä Ä Äs66MHz»T

È¼ÄM¼Ä Auto ¼Äç_»RÈ'¼çzçYÈ_ÄÖ jumper İ¼Ä 2/3 Äè 1/1»T×èÈèÖ¼Y¼fÄÖÄ È »X

CPU Type	66/100 signal	Bus clock	AGP clock	JP23
66MHz	Low	66MHz	66MHz	1-2
66MHz	Low	100MHz	100MHz	1-2
66MHz	Low	100MHz	66MHz	3-4
100MHz	High	100MHz	66MHz	1-2
100MHz	High	100MHz	66MHz	3-4
100MHz	High	100MHz	100MHz	5-6
100MHz	High	133MHz	88.6MHz	1-2
100MHz	High	133MHz	88.6MHz	3-4
100MHz	High	133MHz	133MHz	5-6



ÞIÄz: Çjç_Üh¼Ä 66MHz»RÄüÍ, Çi jumper İ¼Ä Enabled çz
 ÈüN'Í(GÄÄ¼ÄÈ)Èq»T

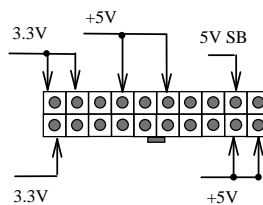
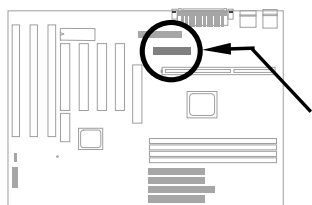
2.3 İ†ËİÚj

2.3.1 Ó,,Ñ×Í†Ëİ×^

ATX Ó,,Ñ×Í†ËİÚj ÅéçèÀf¼Å 20-pin İ†ËİÚj »R×è×eÅŞÈ' İ»¼ÄÖ¼ ÅgÆ çÛ×eÄÖ»T



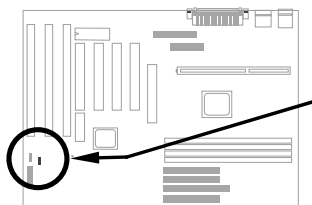
×è¼f¼: ÅsÍ†ËİÄeÄöÈ¼Ó,,Ñ×Í†Ëİ×^¼ÄÄv»R×èç Ý İ†ËİeÓ,,Ñ×»T



PWR2

2.3.2 ATX Soft-Power Switch ËİÚj

ATX soft-power switch İ†ËİÚj Å 2-pin ÄÖ»T×èç È,, ATX Ø İuÄÖÄv¼ ÇÈÀ` ¼hÄpçi Öè çöÅ "power switch" ÄÖ 4-pin İ†Ëİ×^»Rİ^ Äuİ†ËİçUØ Ä` ¼hÄÖ soft-power switch İ†ËİÚj (ÖèçöÅ SPWR)»T

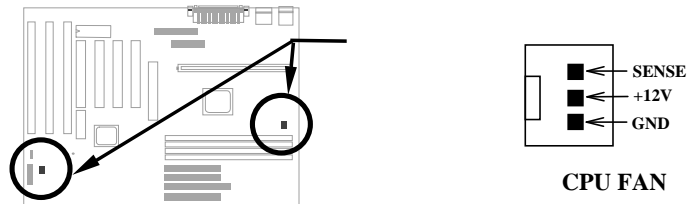


SPWR

İŞB AŞÖà

2.3.3 ÇNÈÈ

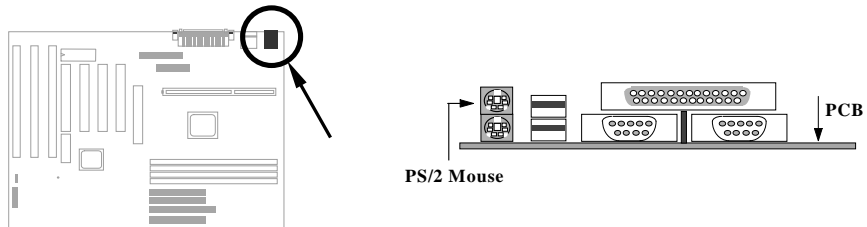
ÀsçUØ Ä` ¼»RÁÍ%ÇÇiÖèCPU FAN Öa%ÇÇiÖèÄEFAN ÄÖÇNÈÈËËÛj »T



Ä İ : Í, ÄüÇiÇNÈÈËËÛj Í¼zçY¼pÍÁİŞB ÖàÈÈçnÈË (hardware monitor)»T

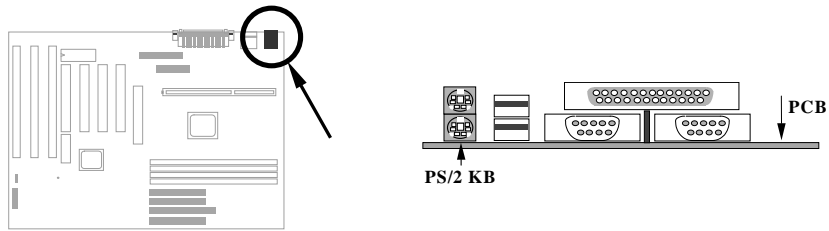
2.3.4 PS/2 NàÓÁ

×éÍ†ÈË PS/2İNàÓÁÁ ÖèçöÆÈİPS2 MSIÄÖÈËËÛj ¼»T



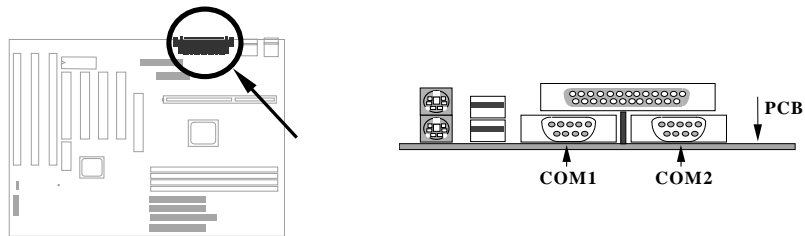
2.3.5 PS/2 Üp×]

×èË PS/2 Üp×] ÈÏÄ Õè¿öÆ KB ÄÔÍ†ÈÏÚj %41»T



2.3.6 À ÀTÊ (COM1/COM2)

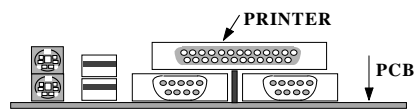
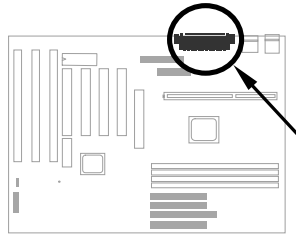
Åü% ÇËÄ` %41ÄÍÄüÇíÔè¿öÆ COM1 Õa COM2 ÄÔ 9-pin D-ÄÆ ÈÏÚj »R¿¿èÄÍ†ÈÏÄ ÀTÊ
ÑàÖÄ (serial mouse) ÄèÆ Õa000 »TÄp%41D%ÇíÄ ÀTÊ ÈÏÚj Õè¿öÆ COM1»WÜP%ÇíÄyöè
¿öÆ COM2»T



İŞB AŞÖà

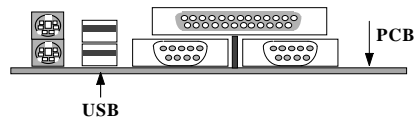
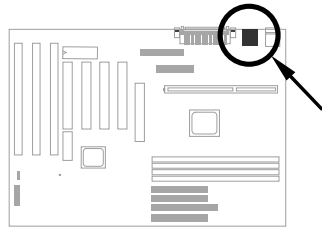
2.3.7 À]Ä Ø

¿UØ Ä` Äü% ÇÄÄ` %h1ÄÍ%QÇi Öê¿öPRINTER ÄÖ 25-pin D-ÄÆËÛj »R¿èÄiÄŠÍ»ÄYÄTÄ»Ä] Ä Ø »T



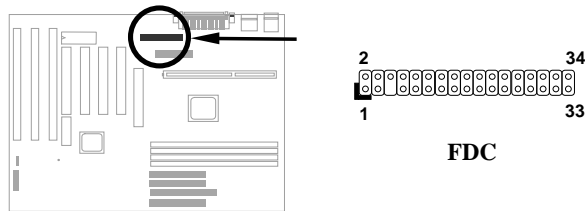
2.3.8 USB ÖàÖ~

Ë`¿æ_ USB ÖàÖ~Í†ËËÄ USB ËËÛj »RÍ,Ð ¿UØ Ä` %h1ÄÍÄüÇÜUSB ËËÛj »RÖè¿öÆ USB»T



2.3.9 İ€0è0

ÀsçU0 Ä` %hÄI%QÇiÖèçöMDC Ä0 34-pin ÈIÜj »RçzeÄIÍ†ÈIÄüç<Í€0è0 »T

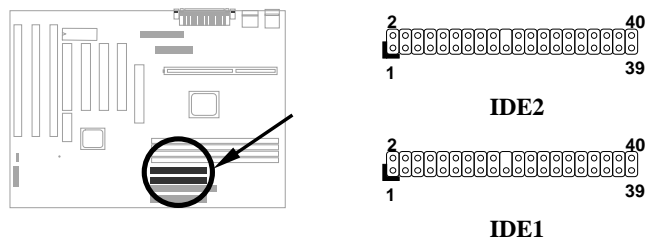


2.3.10 IDE İŞ0è0 0a CDROM

ÀsçU0 Ä` %4»RÈIÄIÄüÇiÖèçöIDE1 Ä^IDE2 Ä0 40-pin ÈaÈŠ»Rçz%0Ä` Í†ÈIÄüÇi IDE 0a0~»RÍaÿçÍ†ÈIçÇi IDE 0a0~»R%QÉIDE1 %d06ÆçUÍ„ÓU(primary channel)»R IDE2 %d06ÆÄ0Í„ÓU (secondary channel)»T

Í†ÈIÄ ç %QÍ„ÓUÄ0İP%Qç<0a0~çİĐNİnÆ master mode»WİP%Kç<0a0~çİĐNİnÆ slave mode»Tç %QÇi0a0~Ä»çzÈIŠ0è0 Äèç 0è0 »T

×èÈ_È'İP%Qç<0a0~İnÆ master mode ÄYÈIÄ IDE1»RİP%Kç<0a0~İnÆ slave mode Äa0aÈIÄ IDE1»TÄfAXE'ÄİİP%ç<%èİPçç«»R<èÄaÄÈIÄÄ IDE2 Ä0 master %è slave mode»T

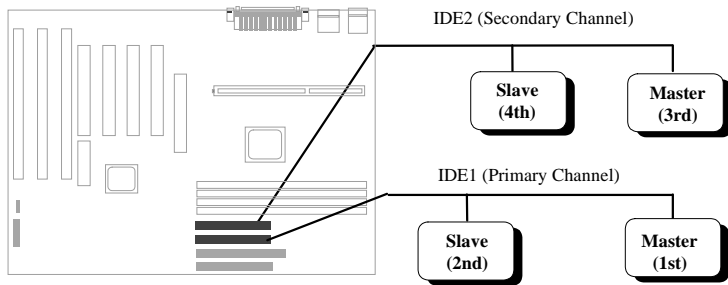


İŞB AŞ0à



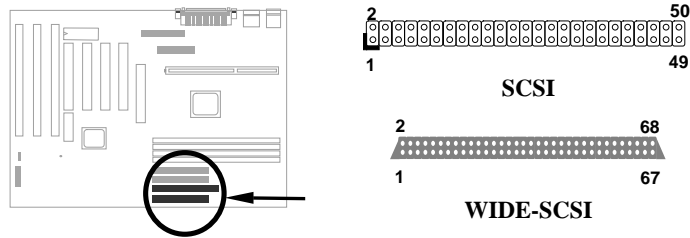
×è³f³ù: IDE İhÈ ÀòPİÈà×^İæÄ ¾¼zDhÓ] 46 ¾×¾Ü (18ÇòÀe)»R;YÁ\Ò ÈàĐáÜ ¾/Ä"»T

×è³f³ù: AEÓWÄ ÍæÀèÄÖAYÖÖÁ•× »RÈà×^İæÖNÖ÷ÄÖ ÖàÖ-İæÆİnÄÄ master mode»RÄYÄæÑi ¾fÖèÄòPİÄÖ ĐĐÄáÄSÖaÑ†ÖàÖ-»T



2.3.11 SCSI Devices

SCSI çzÈİAs 68-pin WIDE-SCSI Äè 50-pin ÄÖ SCSI İ†ÈİÜj ¾h»T



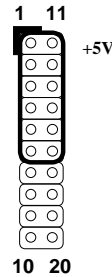
2.3.12 İŞÖê LED Ä çöÜ`

İŞÖê LED Ä çöÜ` ÖèçöÄÄ HDD LED»RÍ, ÇíÈàÈŞ;ç ØR;èÄ ÄfÖò¾¾ÄaÄÖÖ İùRÄfÄXÄiÄSÖàÄÖÖ İùÄv¾ ÇÈ Ä`Ä ÄÍ 4-pin İ†Èİ×^»R×èÄ×Èİİ»¾h»TÄfÄX;^Ä 2-pin ÄÖİ†Èİ×^»R;ç;YÜ ÖòÄŞİ» 1-2 Äè 3-4»RÄ ×èÄqÑ_Ñ; Ää»T

Pin	Description
1	HDD LED
2	GND
3	GND
4	HDD LED

İŞB AŞ0à

ÀfÄX0 İüAvÇEÄ` ç`Ä 12-pin1 ÄÖİ†Eİ
 ×`»RE' çzYÄæz|0éÄ0¼ Ä»ÄiÄŞ0à»R×é×e
 ÄŞİ†Eİ×`Ä0Æ ×`Æ İ†EİÄ 1+5VI Ä0Ä
 0-»T



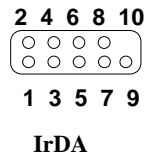
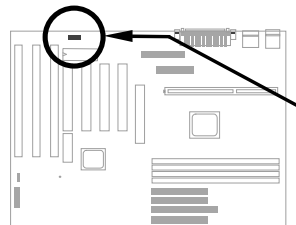
PANEL

2.3.14 Æ ç•×`ĐáÛ Ê (IrDA)

ç0çU0 Ä` ÄÖİP¼XÄ ÄTE (serial port 2) çz¼IÄ IrDA Æ ç•×`Öiİi»TÄiÜñ IrDA
 (Infrared Data Association) Ç çÖÆ çëHP»SCompaq»SBM İçÜfÄi ÄÄüÄ0¼QÇİ
 İiÜİ»RçèÄiEÜÖ†0SçèÆ ç•×`Đá×`0 ÈaÄ0Ä0İ_ÖaÜİçè»TÄüÄiÍEĐ ÄİÖfEi ç¼¼»R
 IrDA Ä`Í¼ÄSÄeÄÄÆ ç•×`ĐáÛ Ä00eNä»Tç`ÇEÈ`Ä00„0¼ÄyİaÆ ç•×`ĐáÛ çmü»RçS
 İBÄi IrDA İñÄŞ»Rİ_ÉüÈ Äs¼QÄŞĐkÜ %0»R¼0¼ÇEİ†Eİ×`ÆEÖİ†Eİ»RÄj ç¼oÈaÖa
 İÄÈ` Ä0„0¼ÄeÇi¼YÖaÄ Äfİ'(PDA) Æ¼Æİ†×`»SĐa00ÜaEñ0 ÈaÄeÈ_¼ç ÈvÄ ¼P
 İÄ IrDA Ä0Ä]Ä 0 ÄTÄ]»Tç0çU0 Ä` çz¼IÄHPSIR (115Kbps, 1 meter)»SASK-IR
 (56Kbps) Öa Fast IR (4Mbps, 2 meters) İçÍhÈ »T

ÄŞ0aÈä»R×eÈ IrDA Æ ç•×`ÖiİiÄŞİ»Ä çU0
 Ä` ¼ÄÖeÄİrDA ÄÖÈaEŞ»TÄŞ0a¼Äü»RE' Üóçİ
 Ö¼Đ"Èi1SZ didv† fİ¼ÖÄ0Æ ç•×` çmü»R¼ çz
 çÜEq¼ÄQ»T

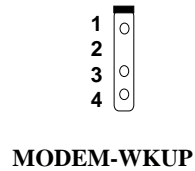
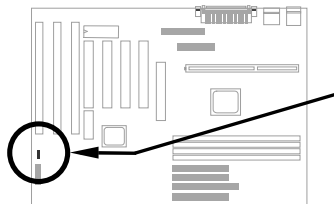
Pin	Description
1	+5V
3	FIRRX (FAST IR)
4	CIRRX
5	IRRX (STANDARD IR)
6	5VSB
7	GND
9	IRTX (STANDARD IR)



2.3.15 Modem Wake-up ÈİÛj

İÖİUØ Ä` %hÄyÁÍÉdÈ ×^ò Íñçf»Rjz#pİÄÖa060 ÖÖÈÈD"Ø
 (Modem Ring-On) İñú»R%ÖÈİÄ» (AOpen MP56) Äèİ•Èİ
 Ä»Öa060 Á»İØRİè»TİèÄ Äèİè%ÖÈİÄ»Öa060İuÄÖ0ö»RİÇÈä
 %Ä=ÈİØ` Ö,,Ñ»RÄİ İYÄöÇæ% ò ÄöPİÈ' Äèİè»TÇj È' ÈPİèÄÖ
 Æ' AOpen MP56»RÄy×èÄèİè4-pin İ†Èİ×»Rİ†Èİ MP56 ÄÖ
RING ÈİÛj ÖaİUØ Ä` %hÄW/KUP ÈİÛj »T

Pin	Description
1	+5V SB
2	NC
3	RING
4	GND

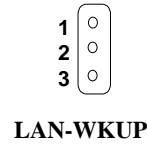
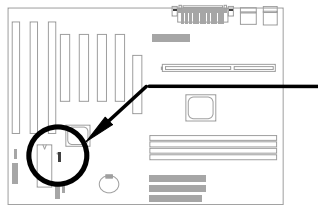


İŞB AŞ0à

2.3.16 LAN Wake-up EİÜj

çÖçU0 Ä`ÄyÀİLAN-WKUP EİÜj »R Ç€ÄéçèLAN Wake-up çİü»RçİDÑÑEİçİİÄÄÖçİüÄÖÖ 0 çu0a0 0öİçB »T

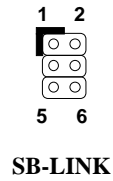
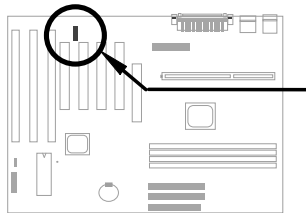
Pin	Description
1	+5V SB
2	GND
3	LID



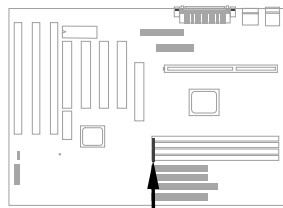
2.3.17 SB-LINK

SB-LINK ççèÄİİ†Eİ Creative ÄÈvÄÖ PCI ÇİEPçü»T Çj È`AŞ0àçM, 0òÇİEPçü»Rİ_çİDÑÄéçèÄ İ, ÇİEİÜj »RççY Ä\ÄsDOS Ü 0içfÉüÈ ÄÈv»T

Pin	Description
1	GNT#
2	GND
3	NC
4	REQ#
5	GND
6	SIRQ#



2.4 ÅŠ0à;UE`0eB



Í, D çU0 Ä`ÁÍ4 È DIMM (Dual-in-line Memory Module) İ»0ë»RçzY³pİÄ SDRAM (Synchronous DRAM) ç Registered SDRAM»R İæÄÈvD,,çz0W Ä 1GB. Ä çÄqÑ_»RSDRAM Ä^ Registered SDRAM Ä çÉúİgİ»Ä0»RÈ' ç`ÉúÅŠ0àÅa%Q0ö DRAM»T



çè³f³ai: ç0çU0 Ä`ÄÝ³/³pİÄ EDO DRAM»T

DIMM 0iİi çzççY³fİi 0ò% Ä»Èé³Ä»X

- I. ç³ ç³f: Í ÇÈÆÈ 1Mx64 (8MB)»S2Mx64 (16MB)»S4Mx64 (32MB)»S8Mx64 (64MB)»S16Mx64 (128MB)»WİÜ ÇÈÆÈ 1Mx64x2 (16MB)»S2Mx64x2 (32MB)»S4Mx64x2 (64MB)»S8Mx64x2 (128MB)»T



İ½ö: ÄÍÇi% Ä|çzçYÜáÆÈ' Ä0 DIMM Ä İ ÇÈÜóÆ Ü ÇÈ -- ÄÆ-DIMM çÄÇÈÄ0 pin 114 0a pin 129»RÀfÄXÄİ Èç0-0,,0 »RÍ, È DIMM çzÉúİ_Æ Ü ÇÈÄ0»WpÄyİ_Æ İ ÇÈÄ0»TçÈÈñi çÇÈÄ00è0è»T



Pin 129 Pin 114



çèÄqÑ_: ÇÈÓWÄ 1GB Ä0È`0eB ÈvD,,»RÍ_çİDNÜ çè 64M bit Ä0 Registered SDRAM»T

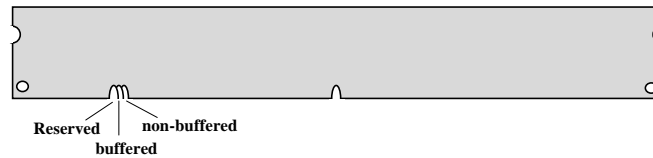
İŞB AŞÖà

II. İtÄn: %QÉ Æ ÖèçöÀf-12 İ, ÖòÄÄ»»Rİ, Ä çöÀtÄ İtÄn (clock cycle time) Æ 12n»RÄi çYÄÖSDRAM İæ%ÄÖ clock Æ 83MHz»TÜöÄİ ç%QÖöÖèçöÆ ÄfÄa67 İ, ÖòÄÄ»»RÄ çöÄaÄSÖ ÈaDaÜ İæÄðçÄ 67MHz»T



×è%f%ü: Äg Äá -10 ÄÖ SDRAM çZÉúÄs 100 MHz CPU ç•Üh%ÜöçççYÖSAQ»RÄ Æ Æ%WÇÄSÄäİöNb»R ÄöÇæÜö Æ ÄöPİ È' Ü çè İBÄi PC 100 İhÈ ÄÖ SDRAM»T

III. Buffered Öa non-buffered: çÖçUØ Ä` %pİÄnon-buffered DIMM»TÈ' ççYÄa06 DIMM %hÇÈÈä%ÄÖÄ Ö~»RÄİÄaÄnon-buffered DIMM Öa buffered DIMM»T×èÈè Nİ%fÖéÄi çö»X



çèÄ Èä%ÄÖÄ Ö~%Äa»Rç^Äİ non-buffered DIMM ççYİ»%ççUØ Ä` %hÄÖDIMM İ»Öè»TÜ İ^ çöÄvçÇÈ%hÈÄ ÄÖDIMM İ½ŞÖxÆ non-buffered ÄÖW»RÄöÇæÄÄö PİÈ' ÄsÜ ÜaÈäİæÄÜöÆ Ö ÄÈÈrÜÈ=İ^ N»»T

IV. 2-clock Öa 4-clock signals: Ü İ^ 2-clock Ä^ 4-clock ÄÖ DIMM İ½ççYçèÄsİ, D çUØ Ä` %h»RÄ Æ%WÄİèYÇÄSÄäİöNb»RÄöÇæÄÄöPİÈ' İæÄÉéç4-clock ÄÖ SDRAM»T



İ½çç: ÇÈÜaÄüÈ' ÄÖ SDRAM Æ 2-clock ÜöÆ 4-clock ÄÖ»RççYÆÆ# pin 79 Öa pin 163»RÄfÄXÄİÈçÖ-Ö, Ö İ_ ÜİÖİÆ 4-clock»WÄpÄyÄ\Æ 2-clock ÄÖ»T

V. ÄaÄ %Ö: %pİÄÖèNÄÄÖ 64 bit wide (İ] parity) Öa 72 bit wide (Äİ parity) ÄÖ DIMM»T

VI. SPD %pİÄ: BIOS NÄöÈäÈÖİ DIMM Æ Äp%pİÄ SPD»RÇj ÄİÄÖö»Rİ_ÄaÄÖİnÄŞ timing»TÄÄİ SPD ÄÖ DIMM Äsİ, D Ä` %h%h%Ä çççÜÈÄéçç»RÄ BIOS Äs POST Èä Nİ½ççöÈ' ÄéçèÄÖ DIMM %h%h%Ä SPD»T

BIOS çççÈäÈÖİ È' ÖèB ÄÖÈvD,,%èÄÄ»»R%ÄÖ=Äéçè Jumper İnÄŞ»Tİæ%ÄÖÈ` ÖèB Èv D,,Æ 1GB»T

İŞB ÅŠÒà

Total Memory Size = Size of DIMM1 + Size of DIMM2 + Size of DIMM3 + Size of DIMM4

»Y%ŒÀT;ÅòPÍÁé;èÄÖ DRAM İiAi »X

DIMM Data chip	ÂTÇË bit Öà;ò	Í Ü ÇË	Chip Öà ;ò	DIMM %Œ %f	Æ ÁpÄòPÍ
1M by 16	1Mx64	x1	4	8MB	Yes
1M by 16	1Mx64	x2	8	16MB	Yes
2M by 8	2Mx64	x1	8	16MB	Yes
2M by 8	2Mx64	x2	16	32MB	Yes
4M by 16	4Mx64	x1	4	32MB	Yes
4M by 16	4Mx64	x2	8	64MB	Yes
8M by 8	8Mx64	x1	8	64MB	Yes
8M by 8	8Mx64	x2	16	128MB	Yes

DIMM Data chip	ÂTÇË bit Öà;ò	Í Ü ÇË	Chip Öà ;ò	DIMM %Œ %f	Æ ÁpÄòPÍ
2M by 32	2Mx64	x1	2	16MB	Yes, but not tested.
2M by 32	2Mx64	x2	4	32MB	Yes, but not tested.

»Y%ŒÀT;ÅòPÍÁé;èÄÖ DRAM İiAi »X

DIMM Data chip	ÂTÇË bit Öà;ò	Í Ü ÇË	Chip Öà ;ò	DIMM %Œ %f	Æ ÁpÄòPÍ
4M by 4	4Mx64	x1	16	32MB	No
4M by 4	4Mx64	x2	32	64MB	No
16M by 4	16Mx64	x1	16	128MB	No

İŞB AŞ0à

Às 100MHz Àè;Y%hÄÖ;•Úh%f »RÇÈ, Â ò ÀÈÄÖÈÈÈuÖaYÇÄŠÄ»RAöÇæAoPIÈ'ÍæÀÛ çè
PC 100 SDRAM»R;Y%f È çòÄOpen Î òiÓJÄÖ PC 100 SDRAM»T

¾¼ ¾f	Ö±Èí	ÄÄÖ	Í Ü ÇÈ	Chip Öaò
32M	Fujitsu	81F16822D-A10-7JF	X2	16
32M	Micron	MT48LC2M8A1-08	x2	16
32M	Hyndai	HY57V168010CTC-10	x1	16
32M	NEC	D4516821AG5-A10-7JF	x1	16
32M	SEC	KM48S2020CT-GH	x1	16
32M	LGS	GM72V661641CT7J	x1	4
64M	Fujitsu	81F64842B-103FN	x2	16
64M	Mitsubishi	M5M4V64S30ATP-10	x1	8
64M	NEC	D4564841G5-A10-9JF	x1	8
64M	SEC	KM48S8030BT-GH	x1	8
64M	Toshiba	TC59S6408FTL-80H	x1	8
64M	LGS	GM72V661641CT7J	x2	8
64M	LGS	GM72V66841CT7J	x1	9
128M	LGS	GM72V66841CT7J	x2	18
128M	Simens	HYS72V16220GU	x2	18

çÖçU0 Ä`È ç Parity check ¾ Ä»ÈÖÎ È`ÖeB Ù Ö»TE' çIDNÄèè72 bit DIMM (64 bit
data + 8 bit parity) Äí¾PIÄ parity check»BIOS çzÄÖÈÄÖÄÖ 72 bit parity DIMM»R
¾Ä-İŞB İmÄ»T

ÀÏ³/eÍÓ Award BIOS

»»†„-N»i'æfƒf †]'wqt;†A-zXi¥Hα¥ AOFIash †o> uαª {f;α § •s¥D “O
“”BIOS;C

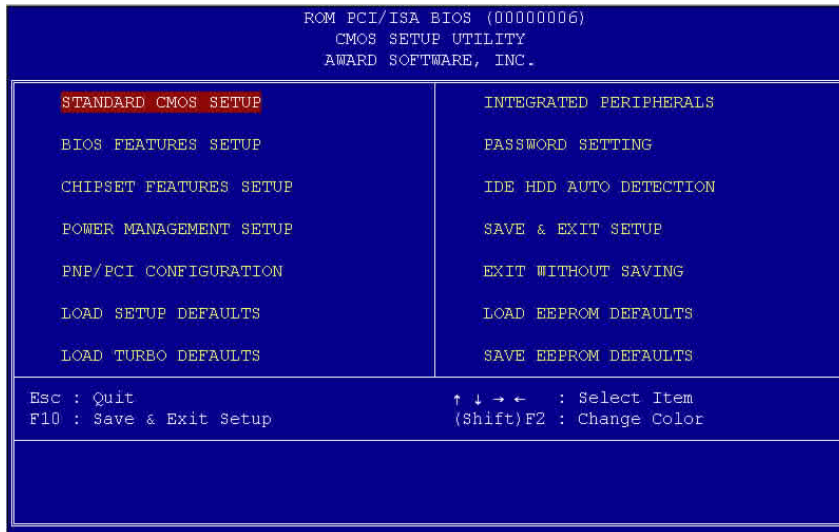


ÇÀÇ€: ÀnÆ BIOS codeÑ“ÚYİöÀ†İèÍ€İŠB ÄÖİnÇfÇnÁ
ÀiÁ Đz»RÄi çYç;í wÈÄÄ BIOS ÄÄçÖçzÉúÑ“ÖaçÖÍÖ%Đİe
ÄÖ%ÖÈv (ÉdÁ`Æ Chipset SetupÈÈÖà) ÁÍÁaÈ†ÌÁ»T

AWARD BIOS

3.1 3.1 BIOS Setup 3.1

BIOS Setup <O@<qfs'æ' Flash ROM "" {f;%;%YiYHY¤ § § ¢tjAa.Î-N
 §fs' 128 byte "" CMOS RAM ;A¤¥"fp>n¶iJ BIOS Setup ;AYu>nfb¶}
 «Æ¢t† f POST (f § ·æ.) ;A« U DEL « ` ;A«K¥i¶iJAWARD BIOS
 Setup ""¥D¿ †;C

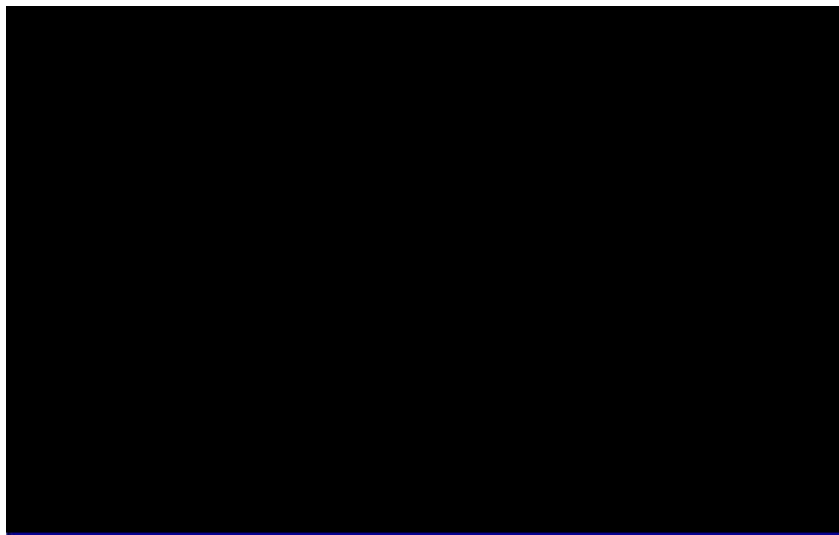


Î½¿öKIÛ Øö "Load Setup Defaults" ¿zØ ¼Ä†IèØS
 ¿ ÍnĀSĀÖÈèÖàÇà»WĀiÛ Øö "Load Turbo Defaults"
 Āy¿zĀè¿èØ ĀðĀÖÈèÖàÇà»RĀ ¿İĐÑÈ Ò ŪİĪ ĀÖĀ†Iè
 İiĀi»T

fb e- Ū† @« ¥Of ;ASi¶D-zpf -NÇæ† ¢ ¿¶ W;Afpf § § †]wA¥H
 fpf -q@> e>-† ¢ ¥t@>;§Q¥fV;¥i-N«G¥æ † ¢ - > § ""¿F¶ W
 [SHIFT] [F2] ¥i§ - ¿^,"""-a¥ f;F- [] ¥i' ¶}†]w ep[[F10] ¥ifb' ¶}«efs;C
 † U>-@f <O,, ' ¿ ¢æ¶ ¥ ¦'†¶
 ¿ ¢æ@> ¶ ¥ ¥¥iYH« U [] ¢ ¿ ¢æ'¶iJU@...h¿C†

3.2 Standard CMOS Setup

Standard CMOS Setup" f] 'w e> iYiYHj d" Y> #w; AfPz000
·`iB "||"- <<"A" #] 'w> ;iCfY YisQ fV` -N t a - > S " "z
¶ W; AfAqY [] ' [] ' < ` f] 'wz ¶ " " iG "



Standard CMOS à Date

fP"G-z·Q>n+] 'w; iYHSQYfV` -N t a **Date** ... f] 'w; AfA< U [] ' [] ' f] 'wY <e" "0;C0·`" " ... f; i;B0'Mf;C

Standard CMOS à Time

fP"G-z·Q>n+] 'w; iYHSQYfV` -N t a **TIME** ... f] 'w; AfA< U [] ' [] ' f] 'wY <e" " ¶Q ¶i" " ... f; i;B 'M<iA<OYH 24 p f; i" Y; iC@Y,; f] 'wfn ¶i' M0;Cf, ><·s¶} <KE> fA><·s¶] 'w

AWARD BIOS

- Standard CMOS à Primary Master à Type
- Standard CMOS à Primary Slave à Type
- Standard CMOS à Secondary Master à Type
- Standard CMOS à Secondary Slave à Type

Type	
Auto	f„ç ¶ ¥i†]!watt' .!\" IDE w\"- ...;Aa fp e¶q
User	(Size) ;B\"<W... (Cylinder) ;B\" Y... (Head) ;B„w, v(pre-
None	compensation) \" _!Cylinder > ;B\" Y¥ § (Landing
1	Zone) \" Cylinder > ;B\" ... (Sector) ¥;C• -z-N ...>\"
2	†] < Auto ;ABIOS • fbatt† f ¶} f § •œ,(POST)
...	;Af »•œ w, \"- \" Type ;Aa^fb Standard BIOS
45	Setup -a¥ ¥Xq C>Y »•œfœ w\"-¶Type '•Qf f †]!w
	...> ;A%†] < User ;Cfp\"Gott†œ... t† wf' - ;A%-
	-NType †] < None ;C
	IDE CDROM †f < Of »•œ¶œ



Î½çö : È' çz Áb çè çU Ìv ÇÈ ÄÖ "IDE HDD Auto
 Detection" Û ĐĪĀi ÆöÈaÈÖĪ Āi ÆŠöaÄÖ IDE ĪŠöeĪh
 È »T

- Standard CMOS à Primary Master à Mode
- Standard CMOS à Primary Slave à Mode
- Standard CMOS à Secondary Master à Mode
- Standard CMOS à Secondary Slave à Mode

Mode	
Auto	œ¥ Logical Block Address (LBA) ... f ;¶ e,œ¥i .!¶W
Normal	„L 528MB \" w\"+C¥ «e¥«†W\" IDE w\"-jfhœa† LBA
LBA	¶ e...f ; ;Aœ xfsœ¶q§ ¶W„L 528MB ;Cfp'Gœ\" w\"-w
Large	†Q f i < LBA On ;A•Nf ¥ LBA Off \" f i œ - ;C

AWARD BIOS

Standard CMOS à Drive A Standard CMOS à Drive B

Drive A
None
360KB 5.25"
1.2MB 5.25"
720KB 3.5"
1.44MB 3.5"
2.88MB 3.5"

f"o> z ¶ ¥i¥¤ z ¶n"-¶A%«¶ fV` f Drive A '
 Drive B ¶B;A« ¶ ' ¶ z »P¶nf;¶ < ¶-" ¶ ...SY
 ¥i;A¤ ¶] 'w> ¶fp¶C

Standard CMOS à Video

Video
EGA/VGA
CGA40
CGA80
Mono

f"z ¶ ¥i¶] 'w' ¶¥" "-a¥ ¥d««"A;A ...w¶] > ¶ <
 VGA/EGA;C¶ ' ¥ «e" > H,q, f¶ - '¶f" ¶W < VGA;A'
 ¥H¶o> z ¶ ·X¥G¤Sf¶ »;C¶B

Standard CMOS à Halt On

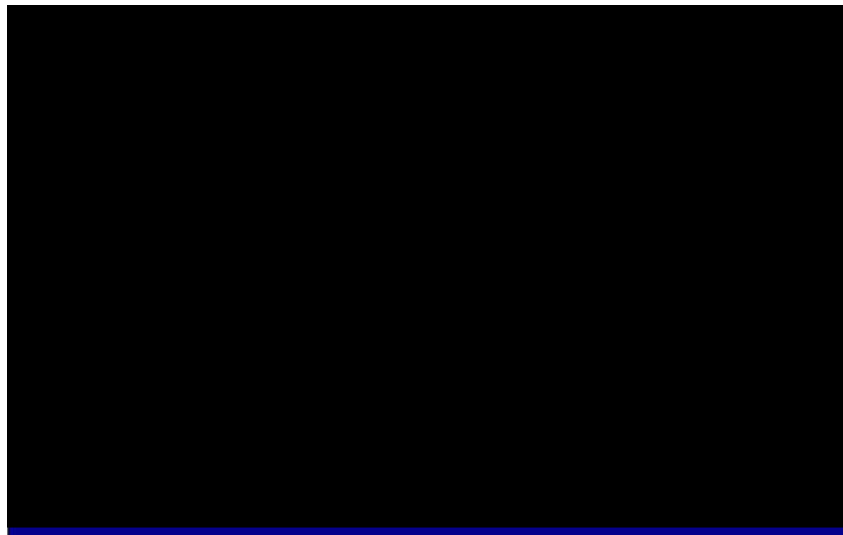
Halt On
No Errors
All Errors
All, But Keyboard
All, But Diskette
All, But Disk/Key

f"z ¶ ¥i--¤ ¶t¶ f f § ·œ(POST) ¶A fp »·œ¤ zø»~
 <O§_>n - „B§@ ...w¶] > ¶ < All Errors;A" ¥ ¶t¶¥u>n »
 ·œ¤ zø»~" <¶A§Y· | - „B§@

AWARD BIOS

3.3 BIOS Features Setup

Press **F2** to enter "BIOS Features Setup" screen.



BIOS Features à Virus Warning

Virus Warning	Press F2 to enter "BIOS Features Setup" screen.
Enabled	Press F2 to enter "BIOS Features Setup" screen.
Disabled	Press F2 to enter "BIOS Features Setup" screen.

! WARNING !
Disk Boot Sector is to be modified
Type "Y" to accept write, or "N" to abort write
Award Software, Inc.

AWARD BIOS

BIOS Features à External Cache

External Cache	<i>f,,z ¶ ¥i+] 'watt+¶¥+~G¶¥\$ ¶œ O - (¶ «e<PBRAM</i>
Enabled	<i>\$ ¶œ O -)¶¥H¥[\$ „q,f" f i~¶œ+] 'w• • "Catt</i>
Disabled	<i>" "†t<;A' ¥H< ~†-z-N ...†] Enable;A f<<Datt o¥f^></i> <i>'w" " " < "p~†] < Disable;C</i>

BIOS Features à CPU L2 Cache ECC Checking

CPU L2 Cache ECC Checking	<i>†o> ¶ ¥ ¥i - z†] 'w<O\$ _L2;Cache ECC Checking;C</i>
Enabled	
Disabled	

BIOS Features à Quick Power On Self Test

Quick Power on Self test	<i>f,,¶ ¥ ¥i+] 'watt+, „L<Y¶.œ, ¶ ¥ ;A¥H¥[\$ f \$.œ,</i>
Enable	<i>(POST) " " {§C ...„w†] > " < Enabled;C</i>
Disabled	

BIOS Features à Boot Sequence

Boot Sequence	<i>f,,z ¶ ¥i< 'watt+¶} " " • j;¶¶¶S" ^ sO%X (ID) fp</i>
A,C,SCSI	<i>U' ¥;G</i>
C,A,SCSI	<i>C: Primary master</i>
C,CDROM,A	<i>D: Primary slave</i>
CDROM,C,A	<i>E: Secondary master</i>
D,A,SCSI	<i>F: Secondary slave</i>
E,A,SCSI	<i>LS: LS120</i>
F,A,SCSI	<i>Zip: IOMEGA ZIP Drive</i>
SCSI,A,C	
SCSI,C,A	
C only	
LS/ZIP,C	

BIOS Features à Swap Floppy Drive

Swap Floppy Drive	<i>f,,z ¶ ¥i¥H` .«†nf; " " - " ¶¶ ,¶¶ ¶Yf†¶ ¥x†nf; "</i>
Enabled	<i>" - (A;B) ;A-z¥i¥H< 'w†~@¥x< " " - B;A†~G¥x< "</i>
Disabled	<i>" - A;C</i>

AWARD BIOS

BIOS Features à Boot Up NumLock Status

Boot Up NumLock Status	<i>f,,z ¶ ¥i†] 'w` %L W`" ..fr` ¥x0%... f; ;C-N ...> ¶]<</i>
On	<i>On; A' ¥ ¥i- ...fr` ¥x†B' ...fr; Q; P; s *Q†]< Off; A«h</i>
Off	<i>fb¶ } «Æ¶" □¥· ...--q C¥ \ ...„w†]>”< On; C</i>

BIOS Features à Boot Up System Speed

Boot Up System Speed	<i>-z¥i¥H; □t†" f †t« High ' Low; C ...„w†]>”</i>
High	<i>< High; C</i>
Low	

BIOS Features à Typematic Rate Setting

Typematic Rate Setting	<i>f,,z ¶ ¥i†] 'w'□æ ` %L.. >«%”V »”¥\ ;C-Nf,, ...</i>
Enabled	<i>†]< Enabled; A†t†S¥¥i- □ ` %L.. †Q>«%”V »”</i>
Disabled	<i>§@; C</i>

BIOS Features à Typematic Rate (Chars/Sec)

Typematic Rate	<i>f,,z ¶ ¥i†] 'w>«%”V »` %L" [A†t«.>”< 6; B; B0; B</i>
6	<i>12; B15; B20; B24; B30 ¥; C†t†„w†]>”< □C 30 > fr; C</i>
8	
10	
12	
15	
20	
24	
30	

BIOS Features à Typematic Delay (Msec)

Typematic Delay	<i>f,,z ¶ ¥i¥□ --□ • ` %L†Q>«%”V »”a¥ fb; ^,,</i>
250	<i>W”” ¶ ; i†A†f† 250; A250; A500; A750 'M1000 ms; C„w</i>
500	<i>†]>”< 250 ms ; C</i>
750	
1000	

AWARD BIOS

BIOS Features à Security Option

Security Option	f,,¶ ¥ ¥i†]'watt†-K%XS¥LH¥... ,g-¡Bf f ¶¥-z
Setup	"",q,¶C
System	fP"G†]<System;A" ¥ ¶Cf,>«•s- ¶¶¶£• >n¶D¿Ø J-K%XC¥t¥†A¶iJBIOS Setup {f;A}• >n¶D¿ØJ-K %X;C fP"G†]<Setup;A¶"• fb¶iBIOS Setup {f;A>n¶D¿Ø J J-K%X;CfP>n¶æ -K%X¥\ ;A%-¿ ¥D¿ † ""Password Setting" ¶ ¥ ;A£>n¿ØJ¥ f;A¶ U <Enter> ` §Y¥iC

BIOS Features à PCI/VGA Palette Snoop

PCI/VGA Palette Snoop	f,,¿ ¶ ¥i>n¶PCI VGA ¥d""% f % (Palette Snooping) †Q
Enabled	fS¶æ «O«øfw R;A £f^ †¥ f «H,,¥H` §K< %~iC fP"G-zfb
Disabled	PCI `X¥R.. Wfw. ;¶ øfP...% f %Lf §}"`X¥R¥d (¶ fP;G MPEG ¥d!..v,,† •¥d)iA¥i-N¶ ¶ †]< Enabled;A,w¶ `X ¥R¥d< %iC

BIOS Features à OS Select for DRAM > 64MB

OS Select for DRAM > 64MB	fP"G-z¶ OS/2 §@•~att;A¶^¥B O -¿Ø e¶q¶W,,L
OS/2	64MB;A¥i-N ...> "†]<OS/2;A§_«h%-¿Non-OS/2;C
Non-OS/2	

AWARD BIOS

BIOS Features à Video BIOS Shadow

Video BIOS Shadow	' ¿ VGA BIOS Shadow <O-N^a¥ ¥d^BIOS ,E%»s
Enabled	f bDRAM 0 -¿i¥[s dt+" f ;?i]× DRAM
Disabled	" "fsætt« æROM ` s ;C ...+] 'w>" <Enabled;C

- BIOS Features à C800-CBFF Shadow
- BIOS Features à CC00-CFFF Shadow
- BIOS Features à D000-D3FF Shadow
- BIOS Features à D400-D7FF Shadow
- BIOS Features à D800-DBFF Shadow
- BIOS Features à DC00-DFFF Shadow

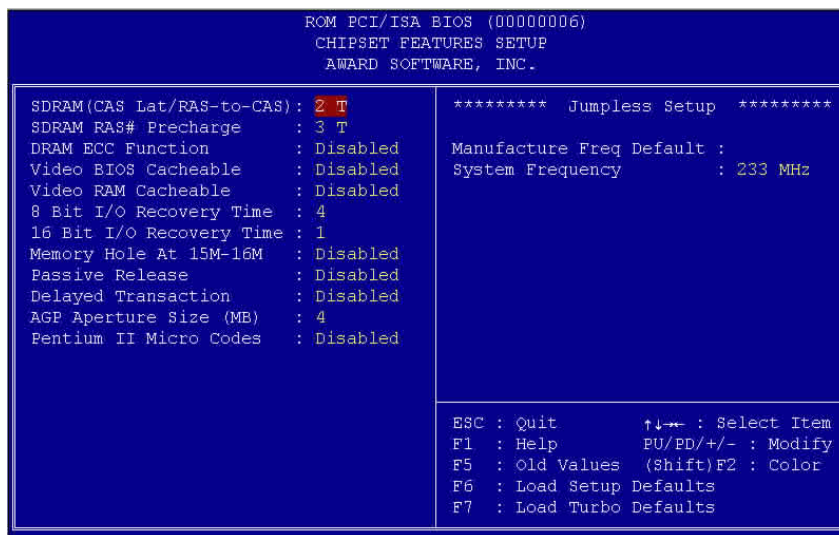
C800-CBFF Shadow	†oaf s } <O 0 -^Ø<O d „ ^X¥R¥dæ¥" ;Cfp"G¿
Enabled	Enabled;A dt+• -N^X¥R¥d^ROM Code %»s@¥ □ ¥D
Disabled	0 -Ø(DRAM) ;Af „ s †N" < Shadow;¥i „ F□ ,ß s " "†B†z†t†C>Y-zftM• ;^X¥R¥ROM Code " " f s } ,E ;¥i-N ...†f†] <Enabled;A æ,ß «O^ „ <M□ ' f†" " ROM Code;Af ¶O 0 -¿Ø



ÄqÑ_: F000 Ä^ E000 Ä Á"Æ ÈÄ Ä`ÉuİÓ BIOS
Code Ó Æ%Ä¿è»T

3.4 Chipset Features Setup

"Chipset Features Setup" < ¥D "O""·„ø† < ^ ¥\ ; ¼øW¥\ †q-`»P„q,£
"" f ~ f†ŸC



ÄqÑ: ÄsÁ Böz Ä ÍnÄŠÄv»R×ê×eÄŠÈ'Æ ÄpÓ Í, Äá
ĐĪ¿øÄÖÍnÄŠÄÓ¿ ÜRÖê»RÄpÄy¾ÄÄÖRÑ ÄÖÍnÄŠÈÄÄ]Ä|
Î½aİeÄÖÈ Ä Í†Äñ»R¼eÇaN"Ö%BeÄ†İeÄÖYÇÄŠÄ»T

AWARD BIOS

Chipset Features à SDRAM(CAS Lat/RAS-to-CAS)

SDRAM(CAS Lat/RAS-to-CAS)
 2/2
 3/3
 Auto

f,,z ¶ ¥i+] SDRAM CAS Latency 'MRAS ,, CAS
 "" ¥« ¶¶o¶+] 'w>""¥i...v~TSDRAM "" f f¶A
 „w¶+]>"" Auto ;AfP"Gfw, ,«Æ;ASDRAM f¶ f< e""-;
 §;A%-§ < 3/3;C



Î½øKICj È_ÀÓÛ DĪĪnÆ Auto»RĀy BIOS N̄“ÀöÊäÁa
 ŪpÈ' Áe_zèÀŌ SDRAM Æ Áp¶¶ĪĀ SPD»TÀfĀXÆ »RĪ_
 ÁæŌŌ SDRAM ¶hĀŌŌaŌŌÁĪ ĪnĀSĪ, Çi Ū DĪ»WÇj ¶½
 Æ »RĀyN̄“ĪnÆ 3/3»TĀŌpĪÈ' ŌãD„ Ū çè¶¶ĪĀ SPD ĀŌ
 SDRAM»RĪ, ŌãĀsĪnĀS¶hN̄“¼ Ō ¶¼ Ā\»T

Chipset Features à SDRAM RAS# Precharge

SDRAM RAS# Precharge
 2T
 3T
 Auto

¥α ¶+] 'w SDRAM RAS T, "" Precharge ¶; (¥H
 clock >p...);CPrecharge < RAS ~"...g §@<e""•¶
 ¶; ;A„w¶+] >"" <Auto;C

Chipset Features à DRAM ECC Function

DRAM ECC Function
 Auto
 Disabled

¥α - ¥'αæ DRAM ECC ¥\ ;C

Chipset Features à Video BIOS Cacheable

Video BIOS Cacheable
 Enabled
 Disabled

f,,z ¶ ¥i~ ...vBĪOS "%- -q§ αæ Ō -~Ō_A¥[ttat
 †"" f †t¶C

AWARD BIOS

Chipset Features à Video RAM Cacheable

Video RAM Cacheable	to> ç ¶ ¥i¥¤ § dVideo RAM A000 »P B000;C
Enabled	
Disabled	

Chipset Features à 8 Bit I/O Recovery Time

8 Bit I/O Recovery Time	f†@,ß´´" I/O .„qAfb f \$„@> I/O 'R¥O«FA»
1	>n@> .-> ¶A~ fA~~ f U@> I/O 'R¥Q C¥
2	' ¥ «e" CPU 'M.„ø† †£-N/O ""†B†z†t« ¥[SA¤
3	-o†o¤´´««"1ISA †]†» >n¥[S .-> ;¶i;ç ¶ ¥i-
4	-z« 'w•ISA ¥d< 8-bit ;A/O 'R¥Of^ † „ø††' » >n
5	""·-> ¶Cfp"G-z ot{ 'fw, ,16-bit ISA ¥df†£<
6	e""-;¥i, ' " I/O ·-> ¶i;C BIOS „w†]»<4
7	ISA clocks;Cfp"G-N ...>†]NA ;A.„ø† · f
8	çØJ 3.5 ""ø††;C
NA	

Chipset Features à 16 Bit I/O Recovery Time

16 Bit I/O Recovery Time	f„ç ¶ ¥i†]16A ¥d< 16-bit ;A/O 'R¥Of^ † „ø††
1	' » >n""·-> ;Cfp"G-z ot{ 'fw, ,16-bit ISA ¥d
2	f†£< e""-;¥i, ' " I/O ·-> ¶i;C BIOS „w
3	†]»< 1 ISA clocks;Cfp"G-N ...>†]NA ;A.„ø
4	† · f çØJ 3.5 ""ø††;C
NA	

Chipset Features à Memory Hole At 15M-16M

Memory Hole At 15M-16M	f„ç ¶ ¥i«Oøat O -Ø „« 'ISA ¥dø¥H`
Enabled	SK O -Ø< %C.„ø† ¥i-qf„ - f<"%- ISA bus
Disabled	¶çØ„L¤ "" ,E'M;C¶-†A f„ <O«Od „ I/O ¥d
	„ <MøC

AWARD BIOS

Chipset Features à Passive Release

Passive Release	<i>f,,z ¶ ¥i- -z--PIIX4 .,,ø† (Intel PCI - ISA) ""</i>
Enabled	<i>‡Q ¶»...¥\Cf,,¥\ <O‡Qø¥fbø††,,Jø ISA ¥D¶ -</i>
Disabled	<i><y†f¥' ‡CfP"G-z""ISA ¥df†f< e""†¥¥i¥H,,` , ¥h†]'w'pø</i>

Chipset Features à Delayed Transaction

Delayed Transaction	<i>f,,z ¶ ¥i- -z--ø PIIIX4 .,, ø† (Intel PCI to ISA</i>
Enabled	<i>bridge) ""' z ¥ ' ¥\C¥ ' PCI"" ,ø¶zø• æ ISA</i>
Disabled	<i>bus § ;Afp"G-z""ISA ¥d• f†f< e""†¥¥i¥H†]'w f,,¥\ - PCI""¶zø,ø' zC</i>

Chipset Features à AGP Aperture Size (MB)

AGP Aperture Size (MB)	<i>‡o> ¶ ¥ ¥ø øM'AGP ,ø¶»... ""†øp</i>
4	
8	
16	
32	
64	
128	
256	

Chipset Features à Pentium II Micro Codes

Pentium II Micro Codes	<i>f,, microcode <O¥ø > ¥zCPU "" bug;A-jP« ~†-z¿</i>
Enabled	<i>Enabled;Cf,,L>Y-z§- f†,§ ""†¥¥i¥Hf ...{z</i>
Disabled	<i>Disabled;C</i>

Chipset Features à Manufacture Frequency Default

Manufacture Frequency Default	<i>‡o> ¶ ¥ ¥u<O¥ø øCPU ""u¥z W†ø• ¶} «</i>
Depends on the CPU type	<i>U"Home" ` ;A.N• f^._ø ø ‡o> ;ø†,w†]>"<233 Mhz;A>n§ - ‡o> >" ;A¥i¥Hø flash.exe ‡o> uª { f;iC</i>

AWARD BIOS

Chipset Features à System Frequency

<u>System Frequency</u>	
233 Mhz	f,,¶ ¥ ¥¤ †] 'wCPU "†; W†vA>Y-z•Q†] < §O"†A" ¥i¥H; "Manual";A M«Æ §O†] 'wH U" "CPU Clock Frequency" "CPU Clock Ratio" ;iC
266 Mhz	
300 Mhz	
333 Mhz	
350 Mhz	
400 Mhz	
450 Mhz	
Manual	

Chipset Features à CPU Clock Frequency

<u>CPU Clock Frequency</u>	
66.8 Mhz	†o> ¿ ¶ ¥¤ †] 'w~(bus clock);Æ «e¥«>-Ww<y †q" "Klamath CPU †f<O¥ 66.8 Mhz;¶H«Æ•s" CPU †] 'w "k†f £" < fP;A' ¥I£- \-z" CPU »;i'æ ;iC
68.5 Mhz	
75.0 Mhz	
83.3 Mhz	
100 Mhz	
103 Mhz	
112 Mhz	
133.3 Mhz	

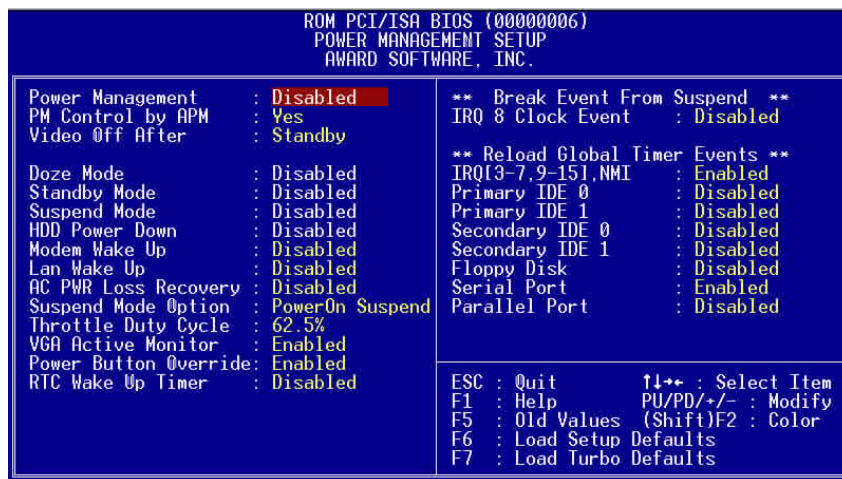
Chipset Features à CPU Clock Ratio

<u>CPU Clock Ratio</u>	
1.5	Intel Pentium II " "†; ()»P¥~† (Bus) W†v¤^£< fP;A†o> ¿ ¶ ¥i¥¤ « Core/Bus "æ¤;C,,w†] >"<O 3.5x;iC
2.0	
2.5	
3.0	
3.5	
4.0	
4.5	
5.0	
5.5	
6.0	
6.5	
7.0	
7.5	
8.0	

AWARD BIOS

3.5 Power Management Setup

Power Management Setup
 "Power Management Setup"



Power Management à Power Management

Power Management
Max Saving
Mix Saving
User Define
Disabled

f,,z ¶ ¥i- -z+] 'w< „q" †z" fU¶p"G>nœ<
 „q" †z¶\ ;%—N …>”†] < Disable ;Cfp"G†] User
 Defined ¥i¥Hf f z < „q" †z" ;C.>”

Mode	Doze	Standby	Suspend	HDD Power Down
Min Saving	1 hour	1 hour	1 hour	15 min
Max Saving	1 min	1 min	1 min	1 min

AWARD BIOS

Power Management à PM Controlled by APM

PM Controlled by APM	fP"Gz "Max Saving";A-N¥i¥H- ¢tt ·'¶i¶¥„q•%”
Yes	tz(APM)¥\;¥[-j< „q” tz¥¢ fP G- CPU ”
No	‡i „B§@

Power Management à Video Off After

Video Off After	f„z ¶ ¥i‡]'w-a¥ „• fbf ” < „q... fiUíC ‡<¿^”
N/A	
Doze	
Standby	
Suspend	

Power Management à Doze Mode

Doze Mode	f„z ¶ ¥i- -z‡]'w¢tt¶iJ”v... fiQbf¶; fi
Disabled	U;ACPU ”„B§@ • ”C;C¥ ' ¢tt<Ofbtq„q”<”A
1 Min	¡A' ¥H »•œ¤ ¥ f §¥• ¥ §Y« •_¤ ¥¿-`”<”A
2 Min	U;C¢tt<O´¥ ” IRQ T„¤ »•œ<ijC
4 Min	
8 Min	
12 Min	
20 Min	
30 Min	
40 Min	
1 Hour	

AWARD BIOS

Power Management à Standby Mode

<u>Standby Mode</u>
Disabled
1 Min
2 Min
4 Min
8 Min
12 Min
20 Min
30 Min
40 Min
1 Hour

f,,z ¶ ¥i- -z+]watt¶iJ ¥< ... fiCbf¶i. fi
 U¶|- CPU "" „BS@ - ;ACw"-... -"œiS@
 z^" ^ †< ¥¥\C¥ ' att<Ofb†q„q\A"AYH »·œ
 ¶ ¥ f S@A¶£•|¥SY« ·_¶ ¥z-"<†att<O`
 ¥ " IRQ T,„¶ »·œ<ijC

Power Management à Suspend Mode

<u>Suspend Mode</u>
Disabled
1 Min
2 Min
4 Min
8 Min
12 Min
20 Min
30 Min
40 Min
1 Hour

f,,z ¶ ¥i- -z+]watt¶iJ... -... fiC." ¶i. fi
 ""+]w† Power On Suspend ' Suspend to Hard
 Drive ¥... fiC

Power Management à HDD Power Down

<u>HDD Power Down</u>
Disabled
1 Min
.....
15 Min

• att¶iJ< „q"¶i"z ¶ ¥i- -z« 'w IDE w"- -
 „BS@" " ¶i»z ¶ > tfX¶ ¥f ¥< ... fi'M..." -... fi
 ""+]w>"f †@

AWARD BIOS

Power Management à Modem Wake Up

Modem Wake Up	tz,,LAPopen "S %u, t]pApoto¶ ¥D "O¥i¥Hf »
Enabled	·æ.. " " T,,iAp^¥i,9¥ ATX Soft Power f ¶ }
Disabled	¡toto¶ ¥\ «D-` AfX¥¤ ¶u »P "¿ ;C»P¶

† Green PC suspend mode fP" " <PAot†¥i¥Hu¥¿
 ^ (SP_ f¡<O ,<d,,q•%¤ † „" " >• <OS_ - "B
 ´) ;C¥~- f¡' " - f¡... ¥d(AOpen MP56/F56) †f¥i
 ¥H · ' modem ring-on " "¥\ ;A f,,L§ > « ~†-z ¤¥i
 -~¥ MP56/F56 ;Af] < MP56/F56 f†S " " „q, †] > p¥i
 ¥H > P¥D "OS „ < " " • pA,tot†^ ;A f» > n f f § ¥ ^ B
 ¥~ " " „q•%C

Power Management à LAN Wake Up

LAN Wake Up	t+o> ¶ ¥ ¥¤ †] 'w<OS_- LAN Wake Up ¥\ ;C
Enabled	
Disabled	

Power Management à AC PWR Loss Recovery

AC PWR Loss Recovery	-Nf „¿ ¶ †] Enabled ;A•h• - „q«E „q•%fAf^Aq†
Enabled	†• f f^¤ > ¥ " " < " (power on ' power off) ;C†o
Disabled	> ¥\ „ " , f"A „qA×p<D-` „E¥j^C

Power Management à Suspend Mode Option

Suspend Modem Option	¥»ot†•f¤ UfCG" suspend... f¡A -z f < „q" †z
PowerOn Suspend	¥\ ;C• ot†¶iJ Power On Suspend < „q... f¡iA
Suspend to Disk	¶†" " Green PC • - f¤... " "¥A CPU -

„B´ ;A' f†" "†]†]†f^ †< ;C E„L††• |«O«†q„q" <
 "A ;A¥H«K... ;B` %L ;B' • „ « »•æ¤ < ;A¥i¥H ¤
 W« •_¤ ¥¿-`... ;C¥¥†A' IRQ " " < ;]¥i¥H-
 ot†... " -... f¡" f^¥¿-` ;C, **Suspend to Disk** ¥i¥Hfb
 ... ^ «A-Not† " < "A O -^Ø'M¿^ „„¥ ¥ xfsfb
 w"†A• „q•%>«s¶ }-†ot†•N• | " f^¤ -z > ¥ u
 §@ " " < "A ;C-z¥†» ¤¥ AOZVHDD §¥ {f¡¤ <O d
 w" - " ¶ i ;C

AWARD BIOS

Power Management à VGA Active Monitor

VGA Active Monitor	f,,z ¶ ¥i » VGA -a¥ ,,fb< „q"<"A "¶CS@
Enabled	
Disabled	

Power Management à Power Button Override

Power Button Override	†o<ACPI "†W \$@ z Enabled ;Ake>- "OW" soft power switch ¥i¥H¥α --α ot†¶;B.. - (Suspend) '^ ;Cfb¶} ""<"AY« U switch;A ¥B ¶; ' 4 < ;Aot†«K• ¶iJ Suspend ... f¶F>Y« U" ¶;¶W„I4 < ;Aot†«K• ^ ;C„w†]""<"AY Disabled ;Asoft power switch ¥u ¥α ¶ ^ ;AoSf† Suspend;A' ¥H]αSf# < ~`"">>qC
Enabled	
Disabled	

Power Management à RTC Wake Up Timer

RTC Wake Up Timer	RTC WakeUp Timer ¥i¥H^- -z« 'w@> S'w""Ø·/` ¶;Aot†• fb†o> ¶;f ¶;¶o> Ø·` / ¶; <Ofb WakeUp Date (of Month) "WakeUp Time (hh:mm:ss)" z ¶ « 'w†C
Enabled	
Disabled	

Power Management à WakeUp Date (of Month)

WakeUp Date (of Month)	t fX"RTC WakeUp Timer" z ¶ α¥αα < 'wαC> ° ""<Y@Cα fp 15·N¥N" αC> °""15 „;A†] < 0 «h¥N" αC@C
0	
1	
.....	
31	



×eÄqÑ: È_ÀÓDÍçòÍnÆE 0»RçzÁéÁ†ÌèÁT%èÀsÀa%Q
ÇíÈäD» (çzçè WakeUp Time Á ÁŠ) ÀõÈäD"Ø »T

AWARD BIOS

Power Management à WakeUp Time (hh:mm:ss)

WakeUp Time (hh:mm:ss) hh:mm:ss	tfX"RTC WakeUp Timer" è ¶ ò¶¶ò « 'wf ¶ } " ¶ ijò f< hh:mm:ss; ¶-''·¶ p "" ¶ f i < 1 ò 24 i C
---	--

Power Management à IRQ 8 Clock Event

IRQ 8 Clock Event Enabled Disabled	f,, è ¶ ¶ i ¶] 'w ò t f b < „q,; A ¶ i U IRQ8 (RTC) "" < i j C OS2 • „ IRQ8 (RTC) i ¶ w · ` i ¶ p "G ¶] 'w IRQ8 "" ¶ \ ; A f b OS2 § @ • ~ ò t ¶ ¶ i L "k ¶ i J w ¶] 'w f n "" < „q... f i C
---	--

Power Management à IRQ [3-7,9-15],NMI

IRQ [3-7,9-15],NMI Enabled Disabled	ò t f b < „q... f ¶ i ' ¶ f ,, è ¶ » · ò IRQ ¶ q, 'NMI `_ < O § _ f ¶ ¶ f ¶ ¶ N f ,, è ¶ ¶] < Enabled ; A f p - q ¶ q , » · ò ¶ ¶ f ¶ ¶ Y • ¶ ¶ ¶ ò ¶ ¶ ^ ~ ò t ¶ " f ^ ¶ ¶ è - ` ... f i C
--	---

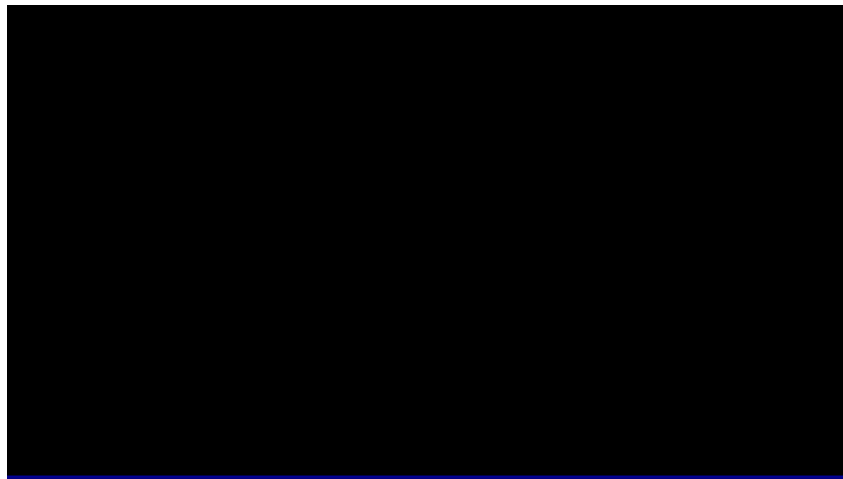
- Power Management à Primary IDE 0
- Power Management à Primary IDE 1
- Power Management à Secondary IDE 0
- Power Management à Secondary IDE 1
- Power Management à Floppy Disk
- Power Management à Serial Port
- Power Management à Parallel Port

Primary IDE 0 Enabled Disabled	† ò ò è ¶ ¶ ¶ i ¶] 'w ' ò ò t f b » ¶ ¶ ¶ ò t f b IDE ¶] ¶ ¶ n " - i ¶ ^ f C i ¶ f C f b < „q " < "AU" " < i j C - i § f p "G ¶ ò ¶ ¶] ¶ ¶ Q » · ò ò f ¶ t . ¶ y ' I / O ¶ ¶ è ¶ ¶ " § @ N • - q ... " - ... f i " f ^ ¶ ¶ è - ` .. i C f i
---	---

AWARD BIOS

3.6 PNP/PCI Configuration Setup

PNP/PCI Configuration Setup is a utility that allows you to configure the PNP/PCI settings of the BIOS. It is located in the BIOS Setup Utility, and is accessed by pressing the **F7** key during the POST. The PNP/PCI Configuration Setup screen displays the following information:



PNP/PCI Configuration à PnP OS Installed

PnP OS Installed	PNP OS Installed
Yes	PNP OS Installed
No	PNP OS Installed

PNP/PCI Configuration à Resources Controlled By

Resources Controlled by	Resources Controlled by
Auto	Resources Controlled by
Manual	Resources Controlled by

AWARD BIOS

PNP/PCI Configuration à Reset Configuration Data

Reset Configuration Data	<i>fP"G' « 'w" IRQ 'at>«•s<["c«Æ o%< %~" "-i</i>
Enabled	<i>S;A·N%ì-Nf,,z ¶ ¶] Enabled;A- at>«•s<["cα^</i>
Disabled	<i>'w•s" IRQ;C</i>

- PNP/PCI Configuration à IRQ3 (COM2)
- PNP/PCI Configuration à IRQ4 (COM1)
- PNP/PCI Configuration à IRQ5 (Network/Sound or Others)
- PNP/PCI Configuration à IRQ7 (Printer or Others)
- PNP/PCI Configuration à IRQ9 (Video or Others)
- PNP/PCI Configuration à IRQ10 (SCSI or Others)
- PNP/PCI Configuration à IRQ11 (SCSI or Others)
- PNP/PCI Configuration à IRQ12 (PS/2 Mouse)
- PNP/PCI Configuration à IRQ14 (IDE1)
- PNP/PCI Configuration à IRQ15 (IDE2)

IRQ 3	<i>fP"G-z' fw,, ISA %dαsf PnP %\;AN» >n« 'wRQ</i>
Legacy ISA	<i>α · '%f" %\ ;Ctαz ¶ %i- -zα S« 'w IRQ "</i>
PCI/ISA PnP	<i>Legacy ISA %d' MPCI/ISA PnP %dα%C• -z« 'w<Y@</i>
	<i>IRQ < Legacy ISA , at+• q" PnP BIOS -N' ¶]</i>
	<i>"IRQ «O d ,, wfw, ," ISA %dα%C ...w¶]>"<</i>
	<i>PCI/ISA PnP; &-""·Nf+αPCI %d(,β´'«<"PCI% d ε%)-</i>
	<i>α^ε» >nα% IRQ;A·N%ì«Od ,ISA %dα%C</i>

- PNP/PCI Configuration à DMA 0
- PNP/PCI Configuration à DMA 1
- PNP/PCI Configuration à DMA 3
- PNP/PCI Configuration à DMA 5
- PNP/PCI Configuration à DMA 6
- PNP/PCI Configuration à DMA 7

DMA 0	<i>fP"G-z' fw,, ISA %dαsf PnP %\;A·N%t¶« 'wDMA tq</i>
Legacy ISA	<i>„Dα · '%f" %\ ;Ctαz ¶ %i- -zα S« 'w DMA tq„D "</i>
PCI/ISA PnP	<i>Legacy ISA %d' MPCI/ISA PnP %dα%C -z« 'w<Y@ DMA tq</i>
	<i>„D< Legacy ISA ;Aat+• q" PnP BIOS -Nf„DMA tq„D</i>
	<i>«Od „ ' fw, ," ISA %dα%C ...w¶]>"< PCI/ISA PnP; &-</i>
	<i>""·Nf+αPCI %dα^ε» >nα% DMA tq„D·N%ì«Od ,ISA %d</i>
	<i>α% C</i>

AWARD BIOS

PNP/PCI Configuration à PCI IDE IRQ Map To

PCI IDE IRQ Map To	f, @, B, ^, ~, " PCI IDE 'X, Y, R, Y, d, f . ' PnP \ ; C, t, o, a
ISA	Y, d, > n, f, w, , f, b, < ' w, " " X, Y, R, Y, d, f . ' BIOS > < . s < [" c
PCI-Slot1	PnP , @, ., C, f, " Y, \ , " , t, \ - z, i PCI . i, ... , PCI IDE ' X
PCI-Slot2	Y, R, Y, d, q, Y,] ' w Auto . f < [" c, w, f, w, , " PCI-IDE I ' X
PCI-Slot3	Y, R, Y, d, C
PCI-Slot4	
PCI-Auto	

PNP/PCI Configuration à Primary IDE INT# PNP/PCI Configuration à Secondary IDE INT#

Primary IDE INT#	t, o, a > i, q, " Y, t, q, " " PCI IDE IRQ Map To" q, Y, p, q, C
A	- z, Y, i, Y, H, < ' IDE ' _ , PCI IDE ' X, Y, R, Y, d, " primary ' M
B	secondary t, q, " D, q, Y, C, @ > PCI . i, ... t, f, t, Y, PCI
C	' _ Y, i, t, Q, < ' w, C - z, Y, t, q, . Y, t,] " PCI IDE IRQ Map To" ' i
D	q, Y, " . i, ... i, A, f, A, f, " < ' w PCI ' _ " ' f, w, , " " X, Y, R, Y, d, C

PCI Slot	Location 1 (pin A6)	Location 2 (pin B7)	Location 3 (pin A7)	Location 4 (pin B8)
Slot 1	INTA	INTB	INTC	INTD
Slot 2	INTB	INTC	INTD	INTA
Slot 3	INTC	INTD	INTA	INTB
Slot 4	INTD	INTA	INTB	INTC
Slot 5 (if any)	INTD	INTA	INTB	INTC

PNP/PCI Configuration à Onboard SCSI BIOS

Onboard SCSI BIOS	f, " i, q, t,] < Disabled " , , i, A, f] < < Y, h, F, O, S, T SCSI
Disabled	BIOS , J " " Y, H, Y, i, Y, [t, t, a, t, t, q,] q, t, o, > @, q, A
Enabled	- z . N L " k, D, O, S U, q, i, J SCSI BIOS t,] ' w t " A, E

AWARD BIOS

PNP/PCI Configuration à Used MEM Base Addr

Used MEM base addr	
N/A	f"z' ¶ ¥t¶• t}Used MEM Length" ¶ ¥ ¢Cfp"G
C800	-z' fw, "ISA ¥d¢Sf#PnP ¥\ ;A·N¥t¶•« 'w O -Ø
CC00	e¶q¢ ·'¥f"¥¥f"z ¶ «h¥i« 'w†Q«Od"" O -
D000	-Ø"¶i¶}'lf §Q
D400	
D800	
DC00	

PNP/PCI Configuration à Used MEM Length

Used MEM Length	
8K	fp"G-z' fw, "ISA ¥d¢Sf#PnP ¥\ ;A·N¥t¶•« 'w O
16K	-Øe¶q¢ ·'¥f"¥¥f"z ¶ ¥i†]'w' > >n O -Ø
32K	e¶q;A^†q" PnP BIOS -N O -Ø"¶i<O d " 'fw, ,
64K	ISA ¥d¢¶C

PNP/PCI Configuration à PCI Slot1 IRQ (Right) PNP/PCI Configuration à PCI Slot2 IRQ PNP/PCI Configuration à PCI Slot3 IRQ PNP/PCI Configuration à PCI Slot4 IRQ (Left)

PCI Slot1 IRQ	
3	†o> ¶ ¥ ¥i¥H -z¥H "" f;†]'w¢C @¶ PCI " ¥[
4	¥d" "IRQ >" ;C>Y¿ Auto;A¢t†·N· f <f¥i¥""
5	>" ;C
7	>Y¢Sf†S§O"" > ;A¢ ~†-z† fn¢¥,w†]>"Auto ;C
9	
10	
11	
12	
14	
15	
Auto	

AWARD BIOS

3.7 Load Setup Defaults

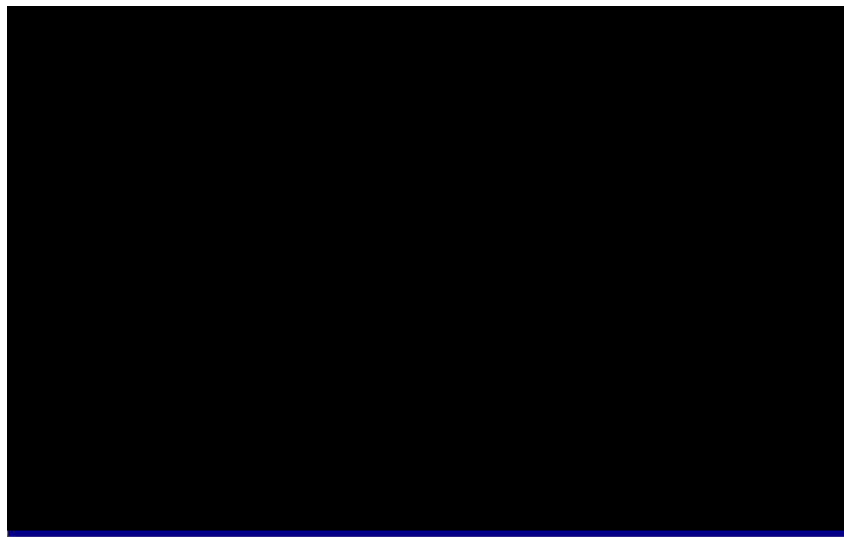
BIOS ROM , J fX A " "] ' m @ ° ° ¥ " % - S Q ¥ f " , J , w
] > ; A t o a , w] > " • | ... v - T ' f i Setup ¥ ; A f F ... - • " CMOS] ' w C f p " G - z
" a t f w , , B " e l q " O - 0 ; A f ' X ¥ R .] • ' i ' X ¥ R d ; A S > « ~ t z ¥ , ¥]
' w i C
f " , z ¥ i ^ f e - o < Q f w ¥ " " a t #] ' w i A ' ¥ H > ¥ t t e f ^ > w " " " p i A - z i ¥ H
" " f i f b " BIOS Features Setup " ' M " Chipset Features Setup " z ¥ i A t] ' w
, B " C ' M , B ^ > ' w " " t j C w

3.8 Load Turbo Defaults

f " , z ¥ i , J f s ' BIOS ROM , B " ~ t v " " , w t j Q b o a , U " , w t j > " • | ... v - T '
f t " " Setup ¥ ; A f F ... - • " CMOS] ' w ¥ H ¥ t C " ~ t v " "] ' w > " , a t t f a ¥ ¥ i
e O t a " t] ' w ; A f p G - z " " O - 0 e l q f j j A p S f t f w , « f i X ¥ R d ; A . N ¥ i
¥ H f ... { f , ¥] t j i O
- z] ¥ i ¥ H ¥ " " f i f b " BIOS Features Setup " ' M " Chipset Features Setup " z
¥ i t] ' w . ; A a t t - o S f n " " f i C # y L - z > n ¥ ` A , ' f t " " z ¥ i A ¥ \
f] < " t t t] ' w > " a • , o t ' M t ¥ % d A ¥ f f p " 3% a 10% " " f ~ t t C

3.9 Integrated Peripherals

Press **F10** to enter the "Integrated Peripherals" menu. Press **U** to enter the **IDE HDD Block Mode** option.



Integrated Peripherals à IDE HDD Block Mode

IDE HDD Block Mode	Press F10 to enter the "Integrated Peripherals" menu.
Mode	> " " " " ' _ + B + Z + C + J + I + Y " " IDE w " - + f + i . ' f "
Enabled	Press U to enter the IDE HDD Block Mode option.
Disabled	Press C to exit the menu.

AWARD BIOS

- Integrated Peripherals à IDE Primary Master UDMA
- Integrated Peripherals à IDE Primary Slave UDMA
- Integrated Peripherals à IDE Secondary Master UDMA
- Integrated Peripherals à IDE Secondary Slave UDMA

IDE Primary Master UDMA Auto Disabled	IDE primary master UDMA mode Auto: Ultra DMA/33 Disabled: Disabled
--	--

- Integrated Peripherals à On-Chip Primary PCI IDE
- Integrated Peripherals à On-Chip Secondary PCI IDE

On-Chip Primary PCI IDE Enabled Disabled	On-chip primary IDE controller Enabled: Enabled Disabled: Disabled
---	--

Integrated Peripherals à USB Legacy Support

USB Legacy Support Enabled Disabled	USB legacy support Enabled: Enabled Disabled: Disabled
--	--



Warning: USB Legacy Support is disabled. This may affect the ability to use USB devices during the boot process.

Integrated Peripherals à USB IRQ Released

USB IRQ Released Yes No	USB IRQ released Yes: Enabled No: Disabled
--------------------------------------	--



Warning: USB IRQ Released is disabled. This may affect the ability to use USB devices during the boot process.

AWARD BIOS

Integrated Peripherals à Onboard FDC Controller

Onboard FDC Controller
Enabled
Disabled

f,,z ¶ ¥i!]'w+nfi;"- \Cp"G•Qo¥¥~- fi""
 --□ ¥d;A&--Nz ¶ !]< Disabled ;C ...„w+]>"<
 Enabled;A¥i- ""- ¥¿-`„B\$@

Integrated Peripherals à Onboard Serial Port 1 Integrated Peripherals à Onboard Serial Port 2

Onboard Serial Port 1
Auto
3F8/IRQ4
2F8/IRQ3
3E8/IRQ4
2E8/IRQ3
Disabled

f,,z ¶ ¥i« 'w¥D "OW" fEfc+s- „fi;wM' _
 >"< **Auto**;C



×eÄqÑ: ÄfÄXÈ'ÄÍÄé¿èÖ ò ¿u»R×é×eÄS%ÄpÄ Ä"
 ÖaÄp¿ÄÄÖÍñÄÄÄ¥%ÄÄ×P»T

Integrated Peripherals à Onboard Parallel Port

Onboard Parallel Port
3BC/IRQ7
378/IRQ7
278/IRQ5
Disabled

±o> ¿ ¶ ¥i¥H--□ "« ¥>f ""f \$¿»P' _

AWARD BIOS

Integrated Peripherals à Parallel Port Mode

Parallel Port Mode	f,,¶ ¥ ¥i~ -z« 'w^fC ¥Hf " §@.. f;¶ ¶¿Ø,Æ
SPP	¡C ...w¶] > " < Normal,]·N<SPP (Stand Parallel
EPP	Port) ... f;¶A< IBM AT 'MPS/2 < e... f;¶A¥i,,¶\^fC
ECP	f b¶ fV... f; U¥HY¿-`¶t« §@¡CEPP (Enhanced
ECP + EPP	Parallel Port) ... f;¶A „¶\^fC f b´øfV... f;U¥H¶ j¶t
	« §@ECP (Extended Parallel Port) ... f;¶A æ¶ `¶t
	« § § " "´øfV^fC §@¡A<O¥H DMA 'M RLE (Run
	Length Encoded) £`Y'M, £`Y" f;¶ ¶¿Ø,Æ

Integrated Peripherals à Onboard IR Controller

Onboard IR Controller	- ¥' ^ ¶ < IR (<¥~%u) --¶ ¡C
Enable	
Disable	

Integrated Peripherals à IR Address Selection

IR Address Selection	¥¶ ¿ IR --¶ „" f §¶
2E0H	
2E8H	
2F8H	
3E0H	
3E8H	
3F8H	

Integrated Peripherals à IR Mode

IR Mode	f,,¿ ¶ ¥i¥H« 'w<¥~%u¶¿Ø"¶C f;¶
ASKIR	
IrDA	

- IrDA - ¶] 'wserial port 2 " §@< ¥¿-`... ¡A¶ „¿ ¶] <O ...w¶]D" "
- ASKIR - fp"Gfb,,q,£"rDA ¶s- „Wwfw,<¥P%u- f< „ (IrDA)¡A·N¥i ¥H¶¥f,,¿ ¶Ato" ¶] 'w¥i¶ ef¶fC TS„F19.2K "i§C

AWARD BIOS

Integrated Peripherals à IR IRQ Select

IR IRQ Selection	IR IRQ Select
IRQ3	
IRQ4	
IRQ10	
IRQ11	



×éÁqÑ: Cj È' ÁÍÁŠÒáÔ Ò ¿u»R×è×e0' IRQ ÁdÁÍÆ
×pÆ »T

3.10 Password Setting

-K%X]'w¥i' \$K¥LH¥... ,G'v¡Bf f o¥-z' " ,q, f¡Cfp"G]'wfn-K%X;Aott• |
fbCf,¶} '¶i J BIOS %¥ {f¡ ;A¥Xt{ @ •f¥ t-, „iA>nD-z¿Ø J¥ %T" "-K
%X¡C

‡]'w-K%XG

1. fb•f¥ t-, iB J‡ fh 8 > fr, " "-K%X-z' ` J" fr, fb¿^ „W¥u• |¥X
t{ <P, iC
2. ` J-K%X<Æ% -« Enter ` ¡C
3. - S• |¥Xt{ •f¥ t¡ fA` J@f, -K%XT{-K%X" "¥¿%C' Enter
` ¡AS¥• |"%- f^D ¥D -

fp"G>nDv‡]'w" -K%X-fb¥Xt{¿ØJ-K%X" •f¥ t¡A"%Æ « Enter ` ¡A
e>-W•N• |¥Xt{ @> TSSi¶D-z-K%Xw¡QDæ

3.11 IDE HDD Auto Detection

f,¥\ ¥i¥Hf »•æ IDE w"- " <<"A¡A¿^N »•æ " " ...> "¶a "Standard
CMOS Setup" " "Hard Disk" ¿ ¶¡Cf‡D IDE w"-¥i •'fh t " " ...‡ÁfD
"G-z" w"- É A¥‡Df »•æ " " ...> "¡A%-¿ N É>nD¥f> ¡A¿¶i J
"Standard CMOS Setup" ¿ ¶ ` J¥¿%T" " ¡C> "

AWARD BIOS

3.12 Save & Exit Setup

Setup % {f; Af xfs' f+"CMOS >"iC

3.13 Load EEPROM Default

EF "Load Setup Default" »P"Load Turbo Default" S¥-A-z]¥i¥H-Nf v""+'w
>"fsJ EEPROM ;A^SQ¥f,,z ¶ ><•qC J

3.14 Save EEPROM Default

f,,z ¶ ¥i-N-zf v""+'w>"fEEPROM ;A Ø«E>CMOS ,Ez ¥ç'-zS -...
fpf > ¥ +'¥¥i'¥ W>z"" "Load EEPROM DEFAULT" ><•s, jC

3.15 Exit without Saving

' ¶}Setup % {f;Af E•| xfsS S „L" CMOS >"iCfp"G-z>n xfs•s""+'w
>"iA%-E>nQ¥f,,z i¶

3.16 BIOS Û_Û ¼Âý

¥H' „""¥D "OfE<BIOS 'æfb@" " < EPROM "" O -i¶ » >nS •s BIOS
"" >iA¥+¶•Q¥Q EPROM zNz ;A' ¥H@°Q¥" Q"KJ f S •s BIOS iC
f •s@¥N""¥D "Owiv"¥S Flash ROM Q xfsBIOS;A u'Ifb' Q¥" ¥u>n
tz„LuQª {fAK¥if f S •s•s"' BIOS;A D-'«¶CS •s BIOS ""¥ ""fb' •E
Q •s""¥\A' •'@Q¥<>-W•s- ¥X"¶CwzØia¥ BIOS zNz {fiQ S •s
BIOS ""¥¥A>nQe-ot •sBIOS »PzNz {fz-<ç, -z"" ,g QA' "]
¥i¥H¶iJ S > "" , (<http://www.aopen.com.tw>) U, (download) iCzNz «e%-
%T'w-z' Qe-oBIOS <O¥z%T""¶iX®°Q »A-z¥i¥H-q fW" Q S E' _
Q fH G>Y fW<AP5TR110.BIN;A•N¥N" †o<AP5T ¥D "O" BIOS;AQ ""¥><O
1.10;C

S > f@•E Q FQ > uQª {fi;G CHECKSUM.EXE AOpen zNz {fi
AOFLASH.EXE;CQ¥"k%- f ¥HU"iQBJ

AWARD BIOS

[CHECKSUM.EXE]

↑o> uα^a {f_i¥i¥H -z%T} {U, BIOS checksum <O§_¥¿%IC

1. %— f CHECKSUM Biosfile.bin
Biosfile.bin « ""<BIOS ""fW" (fpAP5TR110.BIN);C
2. {f_i•|~^a¥Checksum is ssss";C
3. %—æ,β> %§GfbAOpen " , ' BBS W""checksum "sss";A< < <O§_¥¿%IC
fp"G£¥¿%T"";A%— f ¿N¿ ""iBQ^>«•sU, @C,

[AOFLASH.EXE]

f CHECKSUM L>~«FAK¥i¥Hα¥ AOFLASH.EXE α ¿N¿ •s"" BIOS F;C
↑o> ¿N¿ {f_i•|¥ , <d¥D Super/Ultra I/O IC ""«<, iA¥H%T«BIOS <O
¥¿%T""¿C%—""•NG¿N¿ §,,fαfA> BIOS -N•|†Q'→\†C

1. ¥ A ""—¥H DOS ¶} ø¶} ;Aα^¥B%— ~ f fU" O -Ø" †z {f_i (fp HIMEM;BEMM386;EQEMM386, ...);C
2. %— f AOFLASH Biosfile.bin
Biosfile.bin « ""<BIOS ""fW" (fpAP5TR110.BIN);C
3. fb, J•s""BIOS «FA {f_i•| -z<O§_>n-N'~"" BIOS fsα ""~%— ¿ "Y" -Nα fs<"BIOS.OLD";C
4. xfs'~""BIOS §,,†f«FA%—« U"Y" ¶} 'l¶if ¿N¿C
5. ÀSÛ Û Ó]Ï' %RUYÓ Ñ"By¿ô%QÀ "FLASHING" ÄÖEeÈÄ (Û_Û %R); ÄÄÖEä%uòf%½ ¿ZÝ Ø ;C
6. fb"FLASHING" T§¥çqA%—^ †<,,q•%α^>«•s¶}C
7. ¶} «E« U"DEL" ` ¶iJ BIOS Setup e>†C
8. >«•s¿ 'wBIOS SETUP DEFAULT" ¿ ¶iA-Nαt††] 'wfb† ^>'w""iR"A "" -z]¥i¥H% afa> ¥ ""†;Cw>"
9. ¿ "Save & Exit";iAα f,,·Nj¥\§ifα ;I



ĐÍ Áz: xê %ç€Às Û_Û Ó]Ï' %R (Û_Æ Ñ ÛYÓ %RBy¿ö "FLASHING" Èä) Ý Ø »T AfAXAdAÍ_Û_Û ÁÓÁÁÍ_Ý Ø »RÁ† ÌèÈ_Ï]Ä|ÇÄÑ†Èi Èä»RE' Î_¿ÌĐÑÁ ÎÄ BIOS Flash ROM %WT



Î½¿ö: È' %n¿z¿YÄæÎqÀa Öä ÄÖÏ' Áá»RE_ÜÜÄÄÖ BIOS "BIOS.OLD" ÖxÀo»T

Ä Ü A ÈqÂ¼Ë÷ÝUDÊÒë

Q: ÀfÀ Èé%Ú1BIOS1ÁÁ¿ÖP

A: AOpen ¥D "O" BIOS "'¥»•|~ª¥ fPOST (Power-On Self Test) e>-"¥"¥W
¤;C@°¤ ¥À<O¥H R ¶} ¥A«e>-<O¥D "O«fÀ¥Æ>-«h<OØ;C¤ fP G

AP53/AX53 R3.80 Oct.22.1996

↖ BIOS revision

Q:1ÀfÀ ¿ë1PCB wÈé%Ú;UØ Ä`ÄÖÄDÖÖaÁÁ¿ÖP

A: AOpen ¥D "O" "¥», ,%X•|...-¥ fb PCB W;A¤ f;¡< REV:X.X;Ctq-'<Ofb
AOpen ...->x»P¥D "O«<, ,, "iCæ fPAX6L REV:1.2;G



Q: %ÊÖ Æ MMX»Y

A: MMX <O•s@¥N Intel Pentium PP/MT (P55C) Pentium II CPU"~† f fh« ¥O
(single line multiple instruction) § †NMMX §« ¥Ofbfh·C~Ø †¥WSSOf†~(fp
3D ¥ ~Ø...v,,#E3D > ~iB T•|~†, ,, m)iCfh·C~Ø †¥ {f;Y¤¥MMX « ¥O-N
¥ij·T§ %u§@C« Ø AOpen ¥D "OWf†f ´ø,,q•% ·'P55C;A¤^f»
>nS §·,,ø¤ ·' MMX CPU §Y¥i¤¥f,,@¥C

Q: Pentium II ÀsÊ À ÈÉú%hÁÏ¥%ÄÖÁ Ì×P

A: § > -N•s@¥N"CPU †¤ Fæ;¥¥HU""·æ, †"G·£¤ -z <iC f
DRAM: 64MB EDO ' SDRAM
HDD : Quantum Fireball 1280AT
VGA : Matrox Millennium VGA, 4MB, 1024x768 24bit, 85Hz.
OS : Windows 95 4.00.950

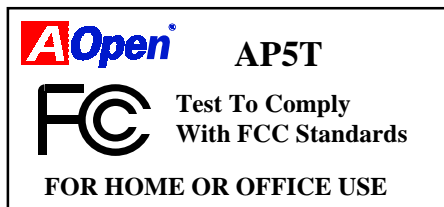
ÄqÄ½Ë÷ÝÜÊËö

CPU	MB	Chipset	Winstone97 Business	Winstone97 High-End
PP/MT-200	AP5T/AX5T	Intel 430TX	48.3	21.9
PP/MT-233	AP5T/AX5T	Intel 430TX	50.5	23.6
Pentium II 200	AX6F	Intel 440FX	45.3	24.1
Pentium II 233	AX6F	Intel 440FX	48.4	26.5
Pentium II 266	AX6F	Intel 440FX	50.8	28.2
Pentium II 266	AX6L	Intel 440LX	54.5	30.8

¥ ¥H W"" § > ¥i¥H ot{;Afb Business Winstone97 ""·æ, †"G jA Klamath-233 æ_PP/MT-233 □^□Sf†fh t§Of fb High-End Winstone97 «o f†,ß ¢§"" †{C†¥i <Ofk Klamath B'I„B "" Qßf†A fX¥ „ §†B†zC

Q: ÆË Æ FCC DoC (Declaration of Conformity)?

A: DoC <O@" •s" FCC †W »{^ ...†A•s...-†WDMY„st ¥ (fp¥D "Dfb¥ %- DoC ...-†„(Logo) jA¥†•† £ · ""„jA□ ·æ, †W% d47CFR 15.3jC¥D "O§DoC ·æ, æ¶FCC ·æ, § ¥[§xAp"G¥D "O†qDbC ·æ, jA·N¥N" ¥D "O"EMI ç g«D-'§@' ¥H-z¥i¥H¥¥ f § §"" (§Y«K<O¥"§@"" · ¥ ¥i);Ck ø AOpen•s@¥N""¥D "O†fw†DqL ·æ, jA U„SY<DoC ...-†„„„, ¥»G



Q: ÆË Æ Bus Master IDE (DMA mode)?

A: ¶†"" PIO (Programmable I/O) IDE » >nCPU □ §Uf†^ IDE ¥ †i"" f §@jA¥] <A ¥« -æ §@""§Cf□· CPU ""u§@>t jA Bus Master IDE ,,m ¥i"%- fs□æ O -∅,¶A E» ,g¥CPU;¥B¥i□ CPU „B§@'M O -∅IDE ,,m ¶j"" ,¶¶ efp ¶pGE„L Bus Master IDE » >rBus Master IDE -X {fj'M Bus Master IDE w"-□ ·'¥i¥ç-¥CS@

ĖqĀ1Ė÷YUDĖ0ë

Q: ĖĖ0 Ė Ultra DMA/33?

A: Ĥo<O@" •s" ĤĖĖ " Ĥfbš % IDE w"-" ĤĖ0ĤĖĤš PIO Mode ĤĖ" ĤĖ IDE --Ĥ ĤH, „šW%t (Rising edge) Ĥ ĤĖ0, ĤĖ DMA/33 ĤhĖifP ĤĖW%t'M U%Ĥ(Falling edge) ĤAf]f„, Ĥ TĤĖ0Ĥv ĤPIO Mode 4 ' DMA Mode 2 " Ĥ >ĤA (16.6MB/S x 2 = 33MB/S) ĤC

U" ĤCĖĤ IDE PIO 'M DMA Mode šĤĖ0Ĥv Af] IDE Ĥ <Y- <016 bit ĤA <GĤCf, Ĥ ĤĖ<02 byte ĤC

Mode	Clock per 33MHz PCI	Clock count	Cycle time	Data Transfer rate
PIO mode 0	30ns	20	600ns	(1/600ns) x 2byte = 3.3MB/s
PIO mode 1	30ns	13	383ns	(1/383ns) x 2byte = 5.2MB/s
PIO mode 2	30ns	8	240ns	(1/240ns) x 2byte = 8.3MB/s
PIO mode 3	30ns	6	180ns	(1/180ns) x 2byte = 11.1MB/s
PIO mode 4	30ns	4	120ns	(1/120ns) x 2byte = 16.6MB/s
DMA mode 0	30ns	16	480ns	(1/480ns) x 2byte = 4.16MB/s
DMA mode 1	30ns	5	150ns	(1/150ns) x 2byte = 13.3MB/s
DMA mode 2	30ns	4	120ns	(1/120ns) x 2byte = 16.6MB/s
DMA/33	30ns	4	120ns	(1/120ns) x 2byte x2 = 33MB/s

Q: ĖĖ0 Ė ACPI (Advanced Configuration & Power Interface) Ā^ OnNow»Y

A: ACPI <01997(PC97) " @" •s" „q•%“ ĤzĤW ĤA Ĥ Ĥ" Ĥfbš ĤiĤB, Ĥš §0•šĤ Ĥ (OS) Ĥ, ' < „q•%f Ĥz„ ĤĤ Ĥ Green PC BIOS ĤA < „P Ĥf„ Ĥ" Ĥ Chipset ' Ultra I/O „ Ĥ Ĥ •Ĥ Ĥ... Ĥ Ĥ>- (Standard Register Interface) „š0•~ĤĤ (fP Win98) ĤA ĤĤI 'š0•~ĤĤ ĤW Ĥ <'MĤ •_ ĤfP• „ 0„q•% šĤ\ ĤĤ0•Q" Ĥk<Of ĤĤI „ Ĥ H• ĤšYĤPnP šĤ>- (Register Interface) ĤC

ACPI ĤW'wĤ ĤL <qf Ĥ„q•%« Ĥ (Momentary Soft Power Switch) Ĥ --Ĥ Ĥ„q•% " <"pš ĤĤ Af]f„ ĤĤĤ•fb ATX Form Factor Ĥf ĤMomentary Soft Power Switch " " ĤĤU~ „ Ĥ Ĥ ĤĤG ACPI Ĥ šl @ „q, ĤĤĤ" " ĤĤĤ ĤĤi <OĤ ĤĤi Ĥf Ĥ„q, Ĥ (Notebook) ' ' f0f Ĥ " " ĤĤ OnNow" ĤĤ0Ĥ\ „ Ĥ\šA ĤWf^ Ĥ W0f, ^ «e" uš0 ep Ĥf ĤĤĤĤ" Ĥ Ĥ ĤĤ -qĤ} - (Bootup) ĤĤiJ Win95 ĤA M<Ĥ ĤĤiJ Winword ĤC ĤĤ TX • „0š < 0 AOpen AX5T ĤAšY • ' ACPI Ĥ\ ĤC

ÄqÄ½Ë÷ÝÜÊËö

Q: » <O ATX Soft Power On/Off 'M Momentary Switch?

A: ATX Soft Power On (Soft Power On) <O> sD, q, % ^ tA . f @ > « 'R, q <y (Standby Current) ' @ S f ^, ; A % ' % < tE z „ q, f S @ % (Wake Up Event); C æ f p » ; < % ~ % u t Q B. tE z ' ` n > tE z @ ° @ » ; tA ' t t " t B t z < O . f @ % > „ q < y ' „ q . % ` . « f f] f „ L < q f i „ q . % ¶ } ^ (Momentary Switch) % i % „ q . % t „ t n ^ 0 „ q . % - a " (Soft Power Control Pin) ¶ } - ' ^ t < % D n „ q . % ; C < o A Open s A T X % D " O S ; . Momentary ¶ } ^ % A X 5 T / A X 5 8 / A X 6 L < t > > p f t ... tE z S % \ (Modem Wake Up); C t n ^ 0 ^ (Soft Power Off) « h < O « % t n ^ 0 ^ t < ; A / W i n 9 5 " " ... " (Shutdown) % \ % i % H % a , . æ - z " % D " O < O S _ . ' t n ^ 0 ^ % \ ; C < o A Open % D " O s A X 5 T / A X 5 T C / A X 6 F / A X 6 L / A X 6 L C / A X 6 B / A X 6 B C / A X 6 B Plus < . ' t n ^ 0 ^ S % \

Q: » <O RTC Wake Up Timer?

A: RTC (Real Time Clock) <O> ^ f „ q l z " „ ; A % i % H H S . s o t t " " Ø . ` » P ¶ } ; C Wake Up Timer . N „ t < O x „ a @ ; A t] ' w " ¶ } ; @ A o t t . N f ¶ } ; C t o > ¶ } ; % i % H t] ' w < a C @ " " f A > % i % H < O a C > ° " " < Y > ; C % % i % BIOS setup -> Power Management -> RTC Wake Up Timer; Enable; cRTC < O a ¶ } % D " O " " ... -> „ „ m j A f Wake Up Timer « a ^ f < O @ " " ... -> " t] > p ; C A Open A X 5 T / A X 5 T C / A X 6 F / A X 6 L / A X 6 L C / A X 6 B / A X 6 B C / A X 6 B Plus t f t . ' RTC Wake Up Timer ; C

Q: » <O Lan Wake Up?

A: Lan Wake Up <O> " % i % H ^ - z - q » . " - - a " „ P O (S Y a < O t B ' ^ " " <) " A " " f i ; C . client " o t t ^ ; A - z % i - q » . " t z „ L " " t n ^ 0 ¶ } e @ wake-up frame (' Magic packet) „ % f C f „ client " . | , < d t o f r a m e < O S _ S t f t % z % T " " MAC f S j ; A > Y f t " " ; A < K t E z a t A . N f p f P a % " « U ¶ } ^ % . ¶ } ; C r @ ... , U a . N % " " t n ^ 0 t A f a % f " u S @ F

Q: » ÆË Æ AGP (Accelerated Graphic Port)?

A: AGP <O> ^ f P C I ¶ } < y - " " . s ¶ } p A % D > n % ... - f b ' " ~ 3 D ^ „ „ " t A f ¶ } . ' O - - 0 - " ... g S @ (Memory Read/Write Operation) ' M @ „ @ ¶ } z Ø (Single-Master Single-Slave one to one only); cAGP a % 66 MHz Clock " " W % t (Rising Edge) U % t (Falling Edge) a ¶ } ... ; A < G ¶ } z Ø t v < 66 MHz x 4 Byte x 2 = 528 MB/S ; c A X 6 L C a % " Intel LX . „ Ø < K % i . ' AGP % \ ; C

ĚqĀ1Ě÷YUDĚ0ë

Q: fb440BX ĚD "O WAGP "" Wtv< f ?

A: ĚH' „AGP "" Wtv†Ě<O>† Intel 440LX „„† < fĚY 440LX ĩ]75Mhz;AĚhAGP]>nĩ]75Mhz;Cf f]< Intel 440BX „„† ĚiĚH „' 100Mhz Ě~ WĀ†ow,gĩW ĚXAGP ""†W ĚA' ĚHŠ > Ě†ĩ•Š □ Wtv<O<†66Mhz;A L% ĚD "O""Ě~ W< f ĩA†o... , æ,ß %T<O††""ĥG'w'

Q: < > fb Windows 95 "", „,m" †z>ß U;AAGP VGA Ěd>P PCI-to-PCI bridge (' AGP bridge) • |< %~< ?

A: †o<OĚĚ-""<†Ěf]< Windows95 Ě>Ě „' AGP;A' ĚH• |f†>~ŠP""†CŠ „LĥA†oĚĚ• |...v-T†††††A' ĚHĚ» «j-jĚhŠ@>ĥĚ Microsoft Windows 98 – ĚX«ĚA• • |, □Mf„ ĩC

Q: „ § fbĥBIOS Setup w,gĩ}– Fĥ@APM;Af < f Windows 95 –□ Ěx, ` <O □Sf†-ĚĚ ĩu,q•%” †zĥv„Ě 'O ?

A: ^D<OĚXfb-zfwWindows 95;@ □ĚĚ...– ĚAPM Ě\ ĩC' ĚH-zĚ†ĩ•f†BIOS ""APM Ě\ – ĚĚA>«•sfAfww, .@f, Windows 95;C

Q: < f ††fb Win95 U L"ĥĥi J suspend ... f ĥ?

A: †of†Ěi <O>Ff]'CDROM †]'w>ĥAf]< fbWin95 CDROM ""f fw• ĥ†q " " ĥ ĩ „w†]>"<Oĥ }–ĥA" ĚH††• |«~ "---z'CDROM;AĚH<KfĚD ø'æ J ĚiĚHf ĥAf †o-N• |...v-T†††††ĥiJ;@ suspend ... f ĥC>n, □M†o> ĥ†D ĚiĚHĥiJ--□ ĚĚ †† Ě „, „m" †z>Ě CDROM Ě †]'w>ĥA^ †Ěf fw• ĥ†q" ĥ ĩ ŠYĚĥC

Q: Āô0ĥĀfĀ ĀāŪp Windows 95 ĀôĀĀĥŌĀ` ?

A: -zĚiĚH□ • ĚHU""□BJ□ , Windows 95 "" ""'ĚĥG

1. « □ Ūu--□ Ěxĥvĥ†††††v„ĚC
2. ĥ □q u @° ĥv...–ĥC
3. -qĥ†††††v...–^D-zĚiĚH†M• ĥ""< ĚWindows 95 "" ""'ĚĥG

- | | |
|-----------|--|
| 4.00.950 | Windows 95 |
| 4.00.950A | Windows 95 + Service Pack or OEM Service Release 1 |
| 4.00.950B | OEM Service Release 2 or OEM Service Release 2.1 |
| 4.00.950C | OEM Service Release 2.5 |

ËqÂ½Ê÷ÝUDÊÒë

>Y-zt {f b f " " <O OSR 2.1;Aifb-â ¥x" "iu.s.Wt £ {f i;v, < a "USB Supplement to OSR2";Ct¥~;Afb Windows\System\Vmm32 ,@ §a U;A ,<d Windows\System\Vmm32 " " Ntkern.vxd <O§_<0.03.1212 " " "C

Q: LX/TX/BX ¥D "O" "atfw, fn **Win95** «A;Afb;u, ,,m" t z>B;v U • |¥Xt {·X· "?"
t , ,,iA§ †, fpf £tca ,, 'O;H

A: †ca ,, <O¥ 'Win95 L "k¥;T;L°LX/TX/BX · „ø† ' >A§YafpfiA-z" "at
†a M¥i¥H¥ç-` „B§@ „L ' @ca¥" >ncaA§ >]·£ca F@> AOchip
ua {f i;¥i §U-z, caM†o> ;C†D> {f i;ca¥W<D-` ;A¥ ¥B A¥' ' f†
" "LX/TX/BX ¥D "QAf £¥u>>'w' AOpen " "†£<†C¥u>n-z~-o f i;A¥i¥Hf
¥ U, »P·†;C¥t¥~>Y-z·Q>nca¥ USB ,, ,m A` ¥†¶·f USB -X {f i;A†ofb
Win98 -N·|·£ca C

Q: fpf fw, , Windows 95 USB -X {f i ?

A: >Y-z<O Win'95 OSR 2.0 " "ca¥" (.950B, -a¥ < "PCI Universal Serial
Devices");A¥-¥ -q Microsoft " , '¶R„q„£" „...t †Bca-o Microsoft USB
supplement (USBSUPP.EXE) †o> {f i;Afw, , §«A;A-z·|fb-â ¥x" "iu.s.W
t £ {f i;v< a "USB Supplement to OSR2";Cw, , §«A;A f AOchip.exe ;A
fpf „·|f u, ,,m" t z>B;vU-a¥;@> USB Controller;v;C
>Y-z<O Win'95 OSR 2.1 ' 2.5 " "ca¥" ;A<h¥u>nca¥ AOchip.exe ·Nfn¶C
† «A;A-Y-z<O Win'95 ¥çf i " " "ca¥" (.950 or .950A);A Microsoft ¥ «e` caSf†·£
¥X, caM " ;A f „w>p Windows'98 -N¥i¥H, caM†o> ;C†D

Q: » <O jumper-less ¥D "O?"

A: AOpen AX6L/AX6LC/AX6B/AX6BC/AX6B Plus †£<O-~¥jumper-less †>¶C†o
" ¥D "O¥i¥Hf » CPU „q Aa^¥B¥i¥H -z CMOS Setup †|v CPU
W†v Af L¶·ca¥ jumper;Cf ¥B„@» CPU ,@ T·|fs'æ EEPROM ;A, U
@ CMOS caS„qfP S§ -...¥ç;TCPU W†v;A-z] L» ¥·¶} · ;Afd †o]
¥ç<O@° jumper-less ¥D "O† £«K" ;Ca

Q: » <O battery-less ¥D "O?"

A: AX6L/AX6LC/AX6B/AX6BC/AX6B Plus „B¥F EEPROM »PS %u, (w¥ %—M
§Q;A¥i¥H -z xfs¥ «CPU »P CMOS Setup † "A¥B L» ca¥„qC¥u>n
¥D„q·%uf†·;j RTC (real time clock) ·¥i¥H«ø~ „B§@ ¥CMOS ,@·N¥~
ç ¥çFA-z¥u>n¥ EEPROM >«·s, j CMOS † "A A††«K·|f^·_a ¥ç-` ¶C"A

ËqÂ1/Æ÷ÝUDÊÒë

Q: ¿f ·_fi«O I •"fn†B< f ?

A: ¶†""_fi«O I• (pico-fuse) >Y¿N·jA«K»>>«s,m·«@;C†o> §@¥†¶
-M·~""ß> u {v~ jA«O S» ¶O^B¥~""f«¥» < § f'_¶i¿B
AOpen ¥D "O w¶}l«¥† ·s""f ·_fi«O I • (Resetable fuse) jA†o"
PolySwitch § f†~fa«O^-z""` %USB „q, jC• f†< "i„q<y†fA†o>
PolySwitch ·|fb·¥ u"" ¶i"„F« "jA «Efb< "i¥qA&f f^·_f«>
'l""<"AC
>n¥R ·' USB ""... ·j' ¥\A† fn` <O«¥†o" f ·_fi«QCI •

Q: » <Ofh>yat BIOS?

A: < ·f« AOpen «¥" † «"" jA' AOpen †n-Ø¶} o†; "ø,g„Lf'_""jA†, '
§J"AF' f†"" jA¿«¥\ oi¥X·f« fh»y«¥"" BIOS ""§ †N<
-z¥i¥H-q§ > """,U, -z' >BIOS ""¥»(„†<O) jA»«s¿N¿ «-z""¥D
"OWCYH«E¶iJ BIOS Setup e>-jA¥u>n« U F9 « ` jA·N¥i¥H`·«f« ""
e>-jA fA« @f,F9 §Y¥if^« >^ q<-
< «H†o> § †N„ -zfb†BIOS ¶ ¥ jA-Nf†' §¶Q

Q: » <O w^-Ø"-- (Hardware Monitoring)?

A: AOpen ATX (AX5TC/AX6L/AX6LC/AX6B/AX6BC/AX6B Plus) ¥D "O ·'Ffh
" f†¥"" w^-Ø"--¶Q
1. „L„q<y«O^-« f†CPU „q f„L„q<y«O^-«¶X\†fX> « "" „q·%« †
„-N¥i·f« ¥ f "" u, «QC@ O
2. «†„q f"-- :«ø~ "--«††u§@„¶ f«d<OS_f†«††„q f¶W„L„¥ ...-·
""-j§A¥f††o" -jN·|,g¥ u«ª {fj (« f†Hardware Monitor Utility)
„ «¥" o¥X~ §i;¶S
3. CPU „L.. «O^-« CPU ·-« "" „w¥ 'w,q""·jA CPU †t« -Nf >
§CjA«^¥B,g¥ †¥†n-Ø o¥X†C §i
4. >· "-- :¥D "OWf†« > >· -jA«¥ ¥i¥' CPU >· jA f ¥t«> «h
¥i¥H „ · "">·jC«„Lu«ª {fj (« f†Hardware Monitor Utility);A«†
†fb>· ^a-jA-N¥if ·f¥X~;§i

Q: » <O Hardware Monitoring Utility (AOHW100)?

A: †o<O¥ AOpen <a o¥X« "" w^-Ø"--¶Q¥H¥« "--«††„¶·E« »P>·
¥jA« fW<AOHW100jA« 100 " ¥ ""¥A¥H«E¥i ·|f†§;csz¥i¥H¥
H" ""¥«"-ø'§ > """,««-o†opC †n-Ø

ËqÂ½Ë÷ÝUDÊÖë

Q: < f « ø AOpen¥D "Oa¥†\fh„q, (**Electrolytic**) „q ef fœ¥'Z% (Tantalum) „q e?

A: „q „q e«~% «<„ „.t t†s•¥ j;C @ °a »i'Z%„q e"«~% œ„q „q e fn;Af »ø 'øQ«~% ua}“„q „q e«o æ'Z%„q e>nfn;C† f> AOpen ¥D "O CPU <œ¥ 100uF ""'Z%„q e« > §CCPU „q f"§" (voltage ripple) Af •s ""§ †N§ ¶ifœ¥\""-1000uF •¥§C ESR (Equivalent Serial Resistor) "" „q „q e;A« ESR < 0.15 ohm; Af 'Z%„q ef >n 0.7 ohm; C¥HU<O¥ «eø AOpen œ¥"" „q e†jG

'Z% SPRAGUE 100uF,
Part number 595D107X06R3C2T,
Max ESR is 0.7 at 25 degree 100KHz.
„q, SANYO 1000uF,
Part number 16MV1000CG,
Max ESR is 0.15 at 20 degree 100khz.

¶Vfh„q e«^f¥N" CPU „q f@'w§ fA~p †fb„q e" f;C†°%T""k< œ¥ xfsfj¥ "i ""%- CPU „q f;A• Mœ¥" « ^†o...;C†o'I AOpen §„ ¥ † j; Intel; AMD Cyrix ""†] >p†W%œ^¥B†q„ Intel; AMD Cyrix ""»{^;C

Q: » <O PC 100 SDRAM?

A: ` M440BX „ø† f† ‚' 100MHz ¥~ W Af ¶†"" FPM »PEDO DRAM «O L "kfb†o" ¥~ WU¥¿-` „ES@F¥R ‚' 100MHz ¥~ W A Intel •f¥XF@> "PC SDRAM Specifications" « <•s @¥N SDRAM †] >p"« ;A†o]•N•O' ¿ "" PC 100 SDRAM †W;C >nfb100MHz <f § ""¥~ j;W†oœ ,§fn""~ »P^> 'w'iA§ > -jP< ~†-z¿ ¥†-f PC 100 †W "" SDRAM;C

ĖqĀ½Ė÷ÝUĎĒ0Ė

Q: 440LX »P440BX . „ 0† “ŲD>n t†š< f ?

A: 440LX »P440BX “ŲD>nt†šfpŲ“ 'jĀ

S'	440LX	440BX
‡ “Ų~ W	66MHz	100MHz
SIMM “” . '	Yes	No
‡ j DRAM eŲq	1GB EDO DRAM ' 512MB SDRAM	1GB SDRAM

Ä Ü B ÜÏÃ ÅXÕóÈàÊ½

,U@z,,J□ ^D;A UfC §¥i¥H §U-z § ,:œ;Afp'Gt~@ L%— , t~ G
¶ iC



Ï½ ö: ÅÍmÿÅÍ¿èÄÖÈ ÈàÀsÁóÇæÄÖÈ ÇÐ%Ö»RÅf jumper
ÍnÅŞÇa»RÍæÑ†ÄÖ BIOS %eBi ÈäÍ' À»»RÈqÅ½Æ-ÝUI¿¿¿T
×eÍÖÍiÁóÇæÄÖÈ ÈÖ»RÆ-Æ-Æ ÁpÀÍÈ' Ö-Ç€ÄÖÍÄÈñ»RÁó
ÇæÄÖÈ Á"Æ »X

Taiwan <http://www.aopen.com.tw>

USA <http://www.aopen-usa.com>
<http://www.aopenusa.com>

Europe <http://www.aopen.nl>



ÇÁÇ€: ÅsÎ^ÅUÖxÖaÊíÄ Áf%ÁÁ»R×e×eÅŞÈ' %ŞÖiÓ]¾ÀT
ÅXÕóÈàÊ½' Áá»RÁÝÖx¾fÖiÍiÄÖÈ-ÝUIÖÇ' »R¿nÆ¿UÖ
Ä`ÅÄÖ»SBIOS ÄÄ¿ÖÍ¿¿TÍÖÇ' ÀrÑfÖiÍi»RÁóÇæÍSI' %e
ÖèÄ^ÄÖÍ¿¿ÄnÑfÄð»RÁóPÍÈ' ¿è email Àè FAX»RÖ,,ÖöÑÖ
Í,,Ö Í]ÈPÍ%eSÄ Ä ×^»RÈ' ¿z¿YÁb¿èÄ Ü ÄÖÈ-ÝU¾eÚÍ
Ä (Technical Problem Report Form)»T

ÜÏÃ ÅXÕóËàÊ½

Í€Ôê/ ÑaÓÁ/ À]Ä Ø ¼¼ ÜËqÃèÏ]Ä| ¼¼ ÁQ:

- , <d+n"/f@fC^fC-%u<O§_¥j%T
- ¶}„q•% tn"- Y<O§_iAS@•«¶ ¥f+tn"-/•„«/fL" , iC

Ð"Ø ÈãÛp×]Ï]ÊãÁQÃè BIOS Býçö Keyboard Controller Error:

- , <d` %LED çO(Numeric Lock) <O§_'æ• ¥ç+C
- , <d¥D "O«O I(Fuse) <O§_çN´(ç¥„qç , <d<O§_ µA«O I •f ,m
fb` %L• i¶+q-`< "æf ...-¥ < F1, 3A/125V);C

Ï]Ä| Ú<À† BIOS Setup ãèÓ„ÀÚÁdÓ„ (COMS data lost, Battery Low):

- , <d„qf <O§_çS(sç' 2.5V);C
- , <dClear CMOS Jumper <O§_¥ç%µC

ÜÏÃ ÅXÕóËàÊ½

Technical Problem Report Form	
<p>Ï¼Å • Model Name:</p> <p>Åá00 Serial Number:</p>	
<p>ÛfÏ0%Å Contact:</p>	<p>Name: _____</p> <p>TEL: _____ FAX: _____</p> <p>Email: _____</p>
<p>Ê÷ÛÛÏ(Å) Error Symptom:</p>	
<p>Å‡ÏèÏÏÔR (×è0Ï Ç' ÅÅ00%eÅÅ¿0) System Configuration: (Please list model name and version.)</p>	<p>OS: _____ BIOS: _____</p> <p>CPU: _____ SIMM: _____</p> <p>HDD: _____ CDROM: _____</p> <p>VGA: _____ Sound: _____</p> <p>Modem: _____ Others: _____</p>

Ä Ü C Jumper ÍnÄŠÄ

Ü Øö CPU ÚhÌ%

Í, Ð çUØ Ä`Æ ÈPçèÏ Jumper Ínçf»TÈù%QÄÖjumper Æ Clear CMOS»RÍ, ççYçèÄ Äë È`È`xi ÄÖÈ»Äf%f »T

ÍnÄŠ CPU ÚhÌ%ÄÖ% Ä|Æ:

BOIS Setup à Chipset Features Setup à CPU Clock Frequency
(ççÉúÄÖÍnÄŠçÄÍ 66, 68.5, 75, 83.3, 100, 103, 112 òa 133.3 MHz)

BOIS Setup à Chipset Features Setup à CPU Clock Ratio
(ççÉúÄÖÍnÄŠçÄÍ 1.5x, 2x, 2.5x, 3x, 3.5x, 4x, 4.5x, 5x, 5.5x, 6x, 6.5x, 7x, 7.5x, òa 8x)

INTEL Pentium II	CPU %ÖÍ»ÚhÌ%	çÛÚh%	ç•Úh
Pentium II - 233	233MHz =	3.5x	66MHz
Pentium II - 266	266MHz =	4x	66MHz
Pentium II - 300	300MHz =	4.5x	66MHz
Pentium II - 333	333MHz =	5x	66MHz
Pentium II - 350	350MHz=	3.5x	100MHz
Pentium II - 400	400MHz=	4x	100MHz
Pentium II - 450	450MHz=	4.5x	100MHz
Celeron 266	266MHz	4x	66MHz
Celeron 300	300MHz	4.5x	66MHz
Celeron 300A	300MHz	4.5x	66MHz
Celeron 333	333MHz	5x	66MHz



ÞÍÁz: INTEL 440BX ÍÓ% Ìi ÍæÄÄçz%ÞÍÄ 100MHz CPU ç• Úh»R103»S112 Òa 133.3MHz ÐaÄë%ÖÍ»Í Öi çè»TÍ, ÄaÍnÄŠçÄ %ŠÐhçi BX ÍÓ% Ìi ÄÖÍhÈ »R ççÉúñ`Ó ÄçÌèÍ(ÄÄÑ}Èq»T

Jumper ÍnÃŠÄ

Ù Øö CPU Ó„Ú½

¿ÛUØ Ä`%PÍÄPentium II VID ¿ñú»R¿¿YÀöÊäÊÖÏ CPU Èð%úÓ„Ú½»RÄp«uÍ Às 1.3V Ä 3.5V %ÄD»»T

Clear CMOS

JP14	Clear CMOS
1-2	¿ÛËøÁQ (ÓŠÍñ)
2-3	Ï ^ Ê½CMOS



Ï½ö: ÀfÄXË' ÄÖÄ¿ÏÈÀnÆÉDhÚhÁíÑ ÈÖÄeÏ|Ä|D"Ø »R¿¿¿YÏ^Ê½ CMOS»RB Ä¿ÏÈÀoÄ ÓŠÍñÄÖÄÄDR»TÈ½¿Äé¿è JP14 %Ä¿•»R È' %ñ ¿¿¿YÄsD"Ø ÈÄÄ %f <Home> ÄíÏ^Ê½CMOS»T

AGP Ratio

JP23	AGP Ratio
1-2	Auto (ÓŠÍñ)
3-4	2/3
5-6	1/1