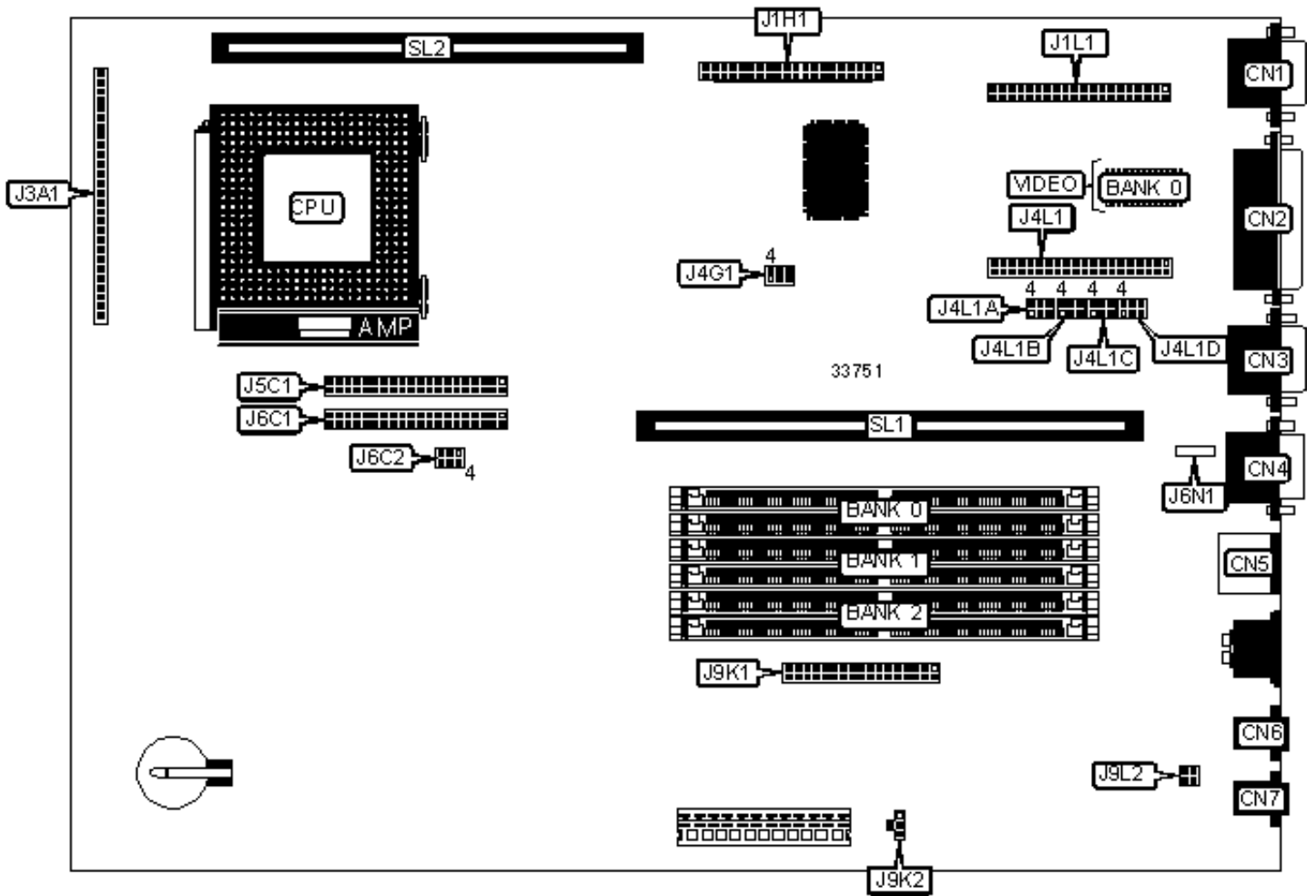


AST RESEARCH, INC.

BRAVO MS-T (VIXEN)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
VGA port	CN1	IDE interface 2	J5C1
Parallel port	CN2	IDE interface 1	J6C1
Serial port	CN3	CD-ROM audio connector	J6N1
Serial port	CN4	Floppy drive interface	J9K1
PS/2 mouse port	CN5	Power supply connector	J9K2
Microphone in connector	CN6	Modem audio connector	J9L2
Audio out connector	CN7	Riser slot	SL1
ATI media connector	J1H1	Cache slot	SL2
Front panel connector	J3A1		

USER CONFIGURABLE SETTINGS

Function	Label	Position
» CMOS memory normal operation	J4L1A	Pins 4 & 5 closed
CMOS memory clear	J4L1A	Pins 5 & 6 closed
» Password enabled	J4L1A	Pins 1 & 2 closed
Password disabled	J4L1A	Pins 2 & 3 closed
» Setup access enabled	J4L1B	Pins 1 & 2 closed
Setup access disabled	J4L1B	Pins 2 & 3 closed
» Factory configured - do not alter	J4L1B	Pins 4 & 5 closed
» Factory configured - do not alter	J6C2	Pins 1 & 2 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
8MB	(2) 1M x 36	None	None

16MB	(2) 1M x 36	(2) 1M x 36	None
16MB	(2) 2M x 36	None	None
24MB	(2) 2M x 36	(2) 1M x 36	None
24MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None	None
32MB	(2) 2M x 36	(2) 2M x 36	None
40MB	(2) 4M x 36	(2) 1M x 36	None
48MB	(2) 4M x 36	(2) 2M x 36	None
48MB	(2) 4M x 36	(2) 1M x 36	(2) 1M x 36
64MB	(2) 8M x 36	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None
80MB	(2) 8M x 36	(2) 2M x 36	None
80MB	(2) 8M x 36	(2) 1M x 36	(2) 1M x 36

DRAM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2
88MB	(2) 8M x 36	(2) 2M x 36	(2) 1M x 36
96MB	(2) 8M x 36	(2) 4M x 36	None
96MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36
96MB	(2) 8M x 36	(2) 2M x 36	(2) 2M x 36
128MB	(2) 8M x 36	(2) 4M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36	None
128MB	(2) 8M x 36	(2) 8M x 36	None
136MB	(2) 8M x 36	(2) 8M x 36	(2) 1M x 36
144MB	(2) 8M x 36	(2) 8M x 36	(2) 2M x 36
160MB	(2) 8M x 36	(2) 8M x 36	(2) 4M x 36

192MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36
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CACHE CONFIGURATION	
Size	SL2
256KB	256KB module installed
512KB	512KB module installed

VIDEO MEMORY CONFIGURATION		
Size	Bank 0	J1L1/J4L1
1MB	1MB	Not installed
2MB	1MB	1MB module installed

Note: Bank 0 is factory installed and is not configurable.

CPU SPEED SELECTION				
CPU speed	Clock speed	Multiplier	J4L1C	J4L1D
75MHz	50MHz	1.5x	2 & 3, 5 & 6	1 & 2, 4 & 5
90MHz	60MHz	1.5x	2 & 3, 4 & 5	1 & 2, 4 & 5
100MHz	66MHz	1.5x	1 & 2, 5 & 6	1 & 2, 4 & 5
120MHz	60MHz	2x	2 & 3, 4 & 5	2 & 3, 4 & 5
133MHz	66MHz	2x	1 & 2, 5 & 6	2 & 3, 4 & 5
150MHz	60MHz	2.5x	2 & 3, 4 & 5	2 & 3, 5 & 6
166MHz	66MHz	2.5x	1 & 2, 5 & 6	2 & 3, 5 & 6
200MHz	66MHz	3x	1 & 2, 5 & 6	1 & 2, 4 & 5

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION

Type	J6C2
Standard (3.3v)	Pins 5 & 6 closed
VRE (3.6v)	Pins 4 & 5 closed

PCI SLOT SELECTION	
Setting	J4G1
» Riser card with 2 slots	Pins 1 & 2, 4 & 5 closed
Riser card with 3 slots	Pins 2 & 3, 5 & 6 closed