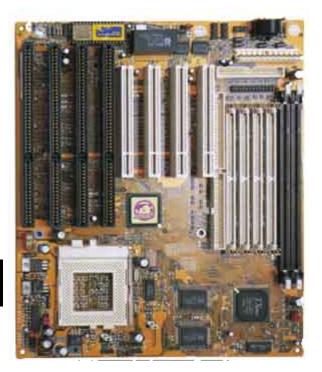


Model: AM-539TXPro

# TX Pro Pentium PCI Motherboard







BIOS\_Update

## Technical Specifications

#### **CPU**

Pentium<sup>TM</sup> Processor at 75-233MHz.

Intel® Pentium<sup>TM</sup> P54C and P55C (MMX),

Cyrix<sup>TM</sup>/IBM<sup>TM</sup> 6x86/6x86L, MX,

IDT C6,

AMD<sup>TM</sup> K5/K6.

## **Cache Memory**

Supports on board 512k pipelined burst synchronous L2 cache.

## Chipset

TX Pro Pentium® PCI Chipset.

## **System Memory**

Supports 64M bit (16Mx4, 8Mx8, 4Mx16) technology DRAM/SDRAM. Provides four 74-pin SIMM auto banking in multiple configuration up to 256MB. Provides two 168-pin DIMM to support SDRM/EDO/FPM DRAM.

#### **Expansion Slots**

Four PCI local bus slot and four ISA bus slots. All four PCI slots support bus master mode.

## **Power Management**

BIOS supports NCR810 SCSI BIOS firmware and green feature function.

On Board I/O

On board PCI bus master IDE interface supports four IDE devices with two channels, BIOS supports four IDE harddisk drives which do not need device driver for software application and the capacity of each harddisk can be larger than 528MB and up to 8.4GB.

PCI IDE controller supports Ultra DMA/33 at maximum transfer rate of 33MB/sec and bus master IDE DMA mode 2.

On board super multi-I/O chip supports two serial ports with 16550 fast UART compatible, one parallel port with EPP/ECP capabilities, and one floppy disk drive interface.

Supports PS/2 mouse connector and the USB interface.

#### **Other Features**

Switching power provides CPU core voltage from 2.5V to 3.5V. Support 60/66/75/83.3MHz clock speed with the PCI clock which is divided by 2 or 2.5, a figure which meets the PCI clock specification.

Supports Write Allocation feature for AMD K6; linear wrap mode for Cyrix M1, MX; ECC and parity for DRAM.

Supports table free configuration so that DIMM and SIMM can be installed in any combinations up to 384MB of system memory.

Supports Ultra DMA/33 and ACPI.

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