

USER'S MANUAL
A68 MAIN BOARD



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This user's manual describes the jumper setting of A68 Main Board. For detailed technical information, please refer to CDs attached with PC system unit.

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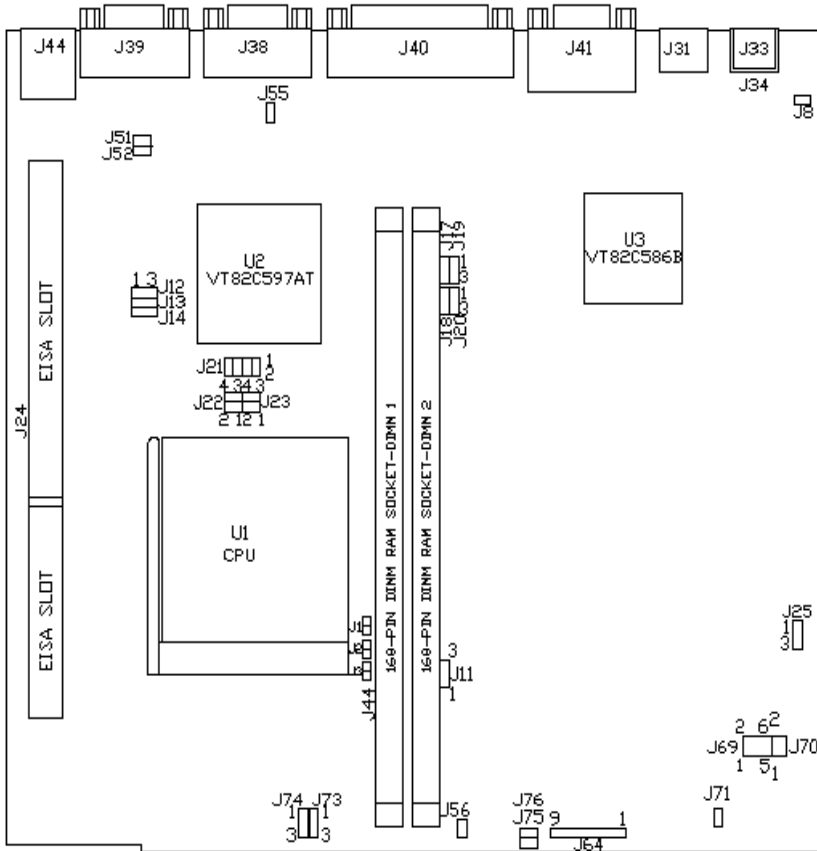
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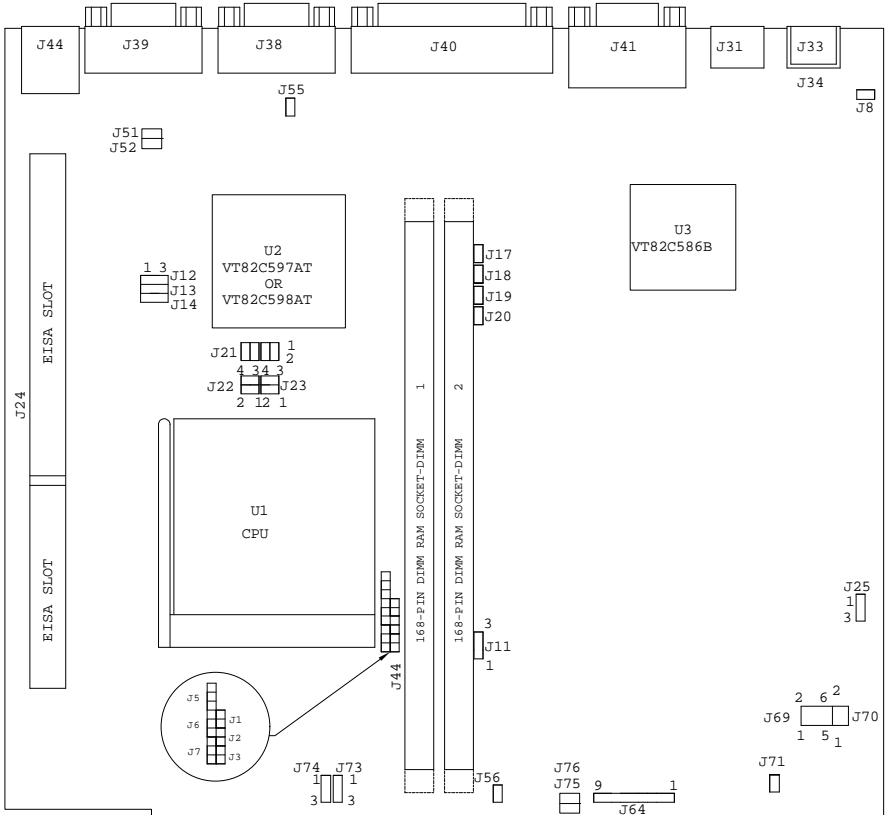
1. Jumpers Location Diagram

1.1. "A68 V1.0a " main board

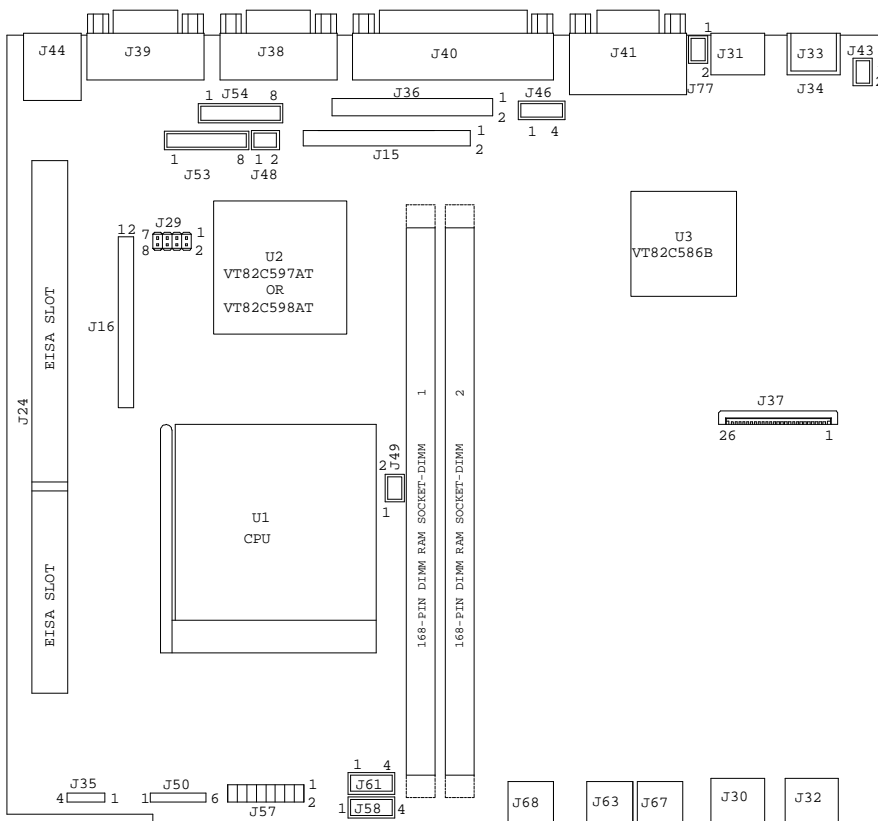
(Supports VIA chipset VT 82C597 & CPU Clock Generator IC- works)



1.2. "A68 V1.0as" "A68 V1.1 " main board (Supports VIA chipset VT82C597/ VT82C598 & CPU Clock Generator IC-S clock generator)



1.3. Connector I/O pin-header Location Diagram for "A68 V1.X "main board



2. Memory configuration for “A68 V1.X” main board

- **System Memory Configuration**

Capacity	DIMM 1	DIMM 2
16MB	16MB	none
16MB	none	16MB
32MB	16MB	16MB
80MB	16MB	64MB
80MB	64MB	16MB
64MB	64MB	none
64MB	none	64MB
128MB	64MB	64MB

- **External Cache RAM Configuration.**

Capacity	SRAM Type	Quantity	Location
256KB (default)	32K x 32 Pipeline	2 pcs.	U4 , U5
512KB (optional)	64K x 32 Pipeline	2 pcs.	U4 , U5

- **VGA Memory Configuration.**

Capacity	U25	U26	U27	U28	Video DRAM Type
2MB (default)	ON	ON	OFF	OFF	256KB X 32 SOP package
4MB (optional)	ON	ON	ON	ON	256KB X 32 SOP package

3. Jumper Setting

IMPORTANT

- A68 main board consists of three version : V1.0a, V1.0as and V1.1.
Following table described how to recognize the version and its differential.

The main board version can be identified according to the printing on main board's soldering side as version "1.0a", "1.0as" or "1.1". The location of printing is closed to front panel side. The label typed with "IC-S" or "IC-W" is sticked on main board's component's side closed to core logic VT82C597 /VT82C598 for your clarification.

Main Board Type	A68 V1.0a	A68 V1.0as	A68 V1.1
Components			
Core Logic (VIA)	VT82C597	VT82C597 or VT82C598	VT82C598
Bus Speed of Core Logic	66MHz	VT82C597 at 66MHz VT82C598 at 100MHz	100MHz
CPU Clock Generator	IC-Works	IC-S	IC-S

3.1. A68 V1.0a Jumper Setting (supports VIA VP3 VT82C597 chipset)

- CPU type selection : J1, J2, J3, J17, J18, J19, J22, J23

CPU	J1	J2	J3	J17	J18	J19	J22	J23
Pentium 100MHz	Open	Open	Open	2-3	2-3	2-3	/	1-2,3-4
Pentium 120MHz	Open	Close	Open	2-3	2-3	1-2	/	1-2,3-4
Pentium 133MHz	Open	Close	Open	2-3	2-3	2-3	/	1-2,3-4
Pentium 150MHz	Close	Close	Open	2-3	2-3	1-2	/	1-2,3-4
Pentium 166MHz	Close	Close	Open	2-3	2-3	2-3	/	1-2,3-4
IDT C6 180MHz	Close	Open	Open	2-3	2-3	1-2	/	1-2,3-4
Pentium 200MHz MHz	Close	Open	Open	2-3	2-3	2-3	/	1-2,3-4
Pentium MMX 166MHz	Close	Close	Open	2-3	2-3	2-3	1-2,3-4	/
Pentium MMX 200MHz	Close	Open	Open	2-3	2-3	2-3	1-2,3-4	/
Pentium MMX 233MHz AMD K6/233MHz (AFR)	Open	Open	Open	2-3	2-3	2-3	1-2,3-4	/
AMD K6 266MHz(AFR) AMD K6-2/266MHz	Open	Close	Close	2-3	2-3	2-3	1-2,3-4	/
AMD K6-300MHz(AFR)	Close	Close	Close	2-3	2-3	2-3	1-2,3-4	/

- CPU voltage selection : J21

CPU power	J21 : Core
2.1V	1-2
2.2V	3-4
2.5V	1-2,5-6
2.8V	7-8
2.9V	1-2,7-8
3.2V	5-6,7-8
3.3V	1-2,5-6,7-8
3.52V	1-2,3-4,5-6,7-8

- SDRAM Clock synchronous :J20

Mode	J20
AGP	1 - 2
CPU	2 - 3

3.2 A68 main board V1.0as/V1.1 Jumper Setting (Supports VIA VP3 VT82C597 chipset & CPU Clock Generator IC-S)

- **CPU type selection : J1, J2, J3, J17, J18, J19, J22, J23**

CPU	J1	J2	J3	J17	J18	J19	J22	J23
Pentium 100MHz	open	open	open	1-2	1-2	2-3	/	1-2,3-4
Pentium 120MHz	open	close	open	1-2	1-2	1-2	/	1-2,3-4
Pentium 133MHz	open	close	open	1-2	1-2	2-3	/	1-2,3-4
Pentium 150MHz	close	close	open	1-2	1-2	1-2	/	1-2,3-4
Pentium 166MHz	close	close	open	1-2	1-2	2-3	/	1-2,3-4
IDT C6 180MHz	close	open	open	1-2	1-2	1-2	/	1-2,3-4
Pentium 200MHz	close	open	open	1-2	1-2	2-3	/	1-2,3-4
Pentium MMX 166MHz	close	close	open	1-2	1-2	2-3	1-2,3-4	/
Pentium MMX 200MHz	close	open	open	1-2	1-2	2-3	1-2,3-4	/
Pentium MMX 233MHz AMD K6/233MHz (AFR)	open	open	open	1-2	1-2	2-3	1-2,3-4	/
AMD K6 266MHz (AFR) AMD K6-2/266MHz	open	close	Close	1-2	1-2	2-3	1-2,3-4	/
AMD K6/300 (AFR)	close	close	close	1-2	1-2	2-3	1-2,3-4	/

- **CPU voltage selection : J21**

CPU power	J21 : Core
2.1V	1-2
2.2V	3-4
2.5V	1-2,5-6
2.8V	7-8
2.9V	1-2,7-8
3.2V	5-6,7-8
3.3V	1-2,5-6,7-8
3.52V	1-2,3-4,5-6,7-8

- **SDRAM Clock synchronous :J20**

Mode	J20
AGP	1-2
CPU	2-3

3.3. A68 main board V1.0as /V1.1 (Supports VIA MVP3 VT82C598 Chipset & CPU Clock Generator IC-S)

- **CPU type selection : J1, J2, J3, J17, J18, J19, J22, J23**

CPU	J1	J2	J3	J6	J7	J17	J18	J19	J22	J23
Pentium 100MHz	open	open	open	2-3	2-3	1-2	1-2	2-3	/	1-2,3-4
Pentium 120MHz	open	close	open	2-3	2-3	1-2	1-2	1-2	/	1-2,3-4
Pentium 133MHz	open	close	open	2-3	2-3	1-2	1-2	2-3	/	1-2,3-4
Pentium 150MHz	close	close	open	2-3	2-3	1-2	1-2	1-2	/	1-2,3-4
Pentium 166MHz	close	close	open	2-3	2-3	1-2	1-2	2-3	/	1-2,3-4
IDT C6 180MHz	close	open	open	2-3	2-3	1-2	1-2	1-2	/	1-2,3-4
Pentium 200MHz	close	open	open	2-3	2-3	1-2	1-2	2-3	/	1-2,3-4
Pentium MMX 166MHz	close	close	open	2-3	2-3	1-2	1-2	2-3	1-2,3-4	/
Pentium MMX 200MHz	close	open	open	2-3	2-3	1-2	1-2	2-3	1-2,3-4	/
Pentium MMX 233MHz AMD K6/233MHz (AFR)	open	open	open	2-3	2-3	1-2	1-2	2-3	1-2,3-4	/
AMD K6 266MHz (AFR) AMD K6-266MHz	open	close	close	2-3	2-3	1-2	1-2	2-3	1-2,3-4	/
**AMD K6-2/300 (AFR)	close	close	close	2-3	2-3	1-2	1-2	2-3	1-2,3-4	/
AMD K6-2/300MHz	Close	Open	Open	1-2	2-3	2-3	2-3	2-3	1-2,3-4	/
**AMD K6-2/350MHz AMD K6-3/350MHz	close	open	open	1-2	2-3	2-3	2-3	2-3	1-2,3-4	/
**AMD K6-2/400MHz AMD K6-3/400MHz	open	close	close	1-2	2-3	2-3	2-3	2-3	1-2,3-4	/

- **SDRAM Clock synchronous :J5, J20**

Mode	J5	J20
AGP	1-2	1-2

CPU	2-3	2-3
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Remark: If choose to run 100MHz CPU bus speed , please select jumper Setting J5 2-3 / J20 2-3 and use PC 100 SDRAM speed under 8ns synchronous.

● **CPU voltage selection : J21**

CPU power	J21 : Core
2.1V	1-2
2.2V	3-4
2.5V	1-2,5-6
2.8V	7-8
2.9V	1-2,7-8
3.2V	5-6,7-8
3.3V	1-2,5-6,7-8
3.52V	1-2,3-4,5-6,7-8