Declaration of conformity							
	(E					
QUANTUM DESIGNS(HK) LTD. 5/F Somerset House, TaiKoo Place 979 Kings Road, Quarry Bay, Hong Kong							
	declares	s that the product					
	Pentium	Il Motherboard					
	Α	dvance 4					
	is in o	conformity with					
(reference to th	e specification	n under which conformity is declared in					
acco	ordance with 8	9/336 EEC-EMC Directive)					
☑ EN 55022	Limits and me	thods of measurements of radio disturbance of information technology equipment					
EN 50081-1	Generic emiss Residential co	ion standard Part 1:					
EN 50082-1	Generic immu Residential co	nity standard Part 1:					
European Representative							
	כ	ODI COMPUTER (SCANDINAVIA) A/S					
QDI SYSTEM HANDEL GM	- 1BH	QDI COMPUTER (NETHERLANDS) B. V.					
QDICOMPUTER (FRANCE	E) SARL	QDICOMPUTER HANDELSGMBH					
QDI COMPUTER (ESPANA	ODICOMPLITER (ESPANA) S.A. ODICOMPLITER (SWEDEN) AB						
Signature :	<u> </u>	Place / Date : <u>HONG KONG/1998</u>					
Printed Name : Anders	s Cheung	Position/ Title : President					

Declaratio	on of conformity					
F	C					
Trade Name:	QDI Computer (U. S. A.) Inc.					
Model Name:	Advance 4					
Responsible Party:	QDI Computer (U. S. A.) Inc.					
Address:	41456 Christy Street					
	Fremont, CA 94538					
Telephone:	(510) 668-4933					
Facsimile:	(852) 668-4966					
Equipment Classification: Type of Product: Manufacturer : Address:	FCC Class B Subassembly AGP Pentium®II Motherboard Quantum Designs (HK) Inc. 5/F, Somerset House, TaiKoo Place 979 Kings Road, Quarry Bay, HONG KONG					
Supplementary Information: This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions : (1) this device may not cause harmful interfer- ence, and (2) this device must accept any interference received, including interference that may cause undesired operation.						
Signature :	Date : 1998					



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II

Chapter 1 Introduction

Overview

The Advance 4 green motherboard utilizes the Via Apollo Pro chipset and provides a highly integrated solution for fully compatible, high performance PC/AT platform. It provides 66/75/ 100/103/112MHz system bus support for all Intel Pentium®II and Celeron[™] processors. Both 66MHz/100MHz SDRAM with SPD and 66MHz EDO DIMMs are supported. It also provides advanced features such as AGP, wake-up on LAN and wake-up on internal/ external modem. The green function is in compliance with the ACPI specification.

Key Features

Form factor

• BabyAT form factor of 220mm x 220mm.

Microprocessor

- Supports all Intel Pentium®II processors at 233/266/300/333MHz with 66MHz bus speed and 350/400/450MHz with 100MHz bus speed.
- Supports all Intel®Celeron[™] processors at 266/300/333 MHz with 66MHz bus speed.
- Supports 66/75/100/103/112MHz host bus speed.
- CPU core frequency = Bus speed x2.5, x3, x3.5, x4, x4.5, x5, x5.5
- CPU core supply voltage adjustable from 1.3V to 3.5V through on- board switching voltage regulator with VID(Voltage ID).

Chipset

 Apollo Pro chipset: VT82C691 system controller VT82C596 PCI to ISA bridge

System memory

- Provides two 168 pin 3.3V unbuffered DIMM sockets.
- Supports both 66MHz/100MHz SDRAMs with SPD and 66MHz EDO DIMMs.
- Minimum memory size is 8MB, maximum memory size is 512MB.
- SDRAM 64 bit data interface with ECC support.



On-board IDE

- Supports two PCI PIO and Bus Master IDE ports.
- Two fast IDE interfaces supporting four IDE devices including IDE hard disks and CD - ROM drives.
- Supports up to PCI mode 4 timing.
- Supports "Ultra DMA/33" Synchronous DMA mode transferring up to 33 Mbytes/sec.
- Integrated 16x32bit buffer for IDE PCI Burst Transfers.

On-board I/O

- Use NS 87351 super I/O chip.
- One floppy port supporting up to two 3.5' ' or 5.25" floppy drives with 360K/720K/1.2M/1.44M/2.88M format.
- Two high speed 16550 fast compatible UARTs(COM1/COM2/COM3/COM4 selective) with 16-byte send/receive FIFOs.
- One enabled parallel port at the I/O address 378H/278H/3BCH with additional bi-direction I/O capability and multi-mode as SPP/EPP/ECP (IEEE 1284 compliant).
- Circuit protection provided, preventing damage to the parallel port when a connected printer is powered up or operates at a higher voltage.
- Supports LS-120 floppy disk drive.
- All I/O ports can be enabled/disabled in the BIOS setup.

Advanced features

- Provides Anti-Virus function.
- Provides on-board PS/2 mouse connector and its adapter.
- Two USB ports supported. •
- Provides infrared interface.
- Supports Windows 95/98 software power-down when using an ATX power supply.
- Supports external modem ring power-on when using an ATX power supply.
- Supports wake-up on LAN and wake-up on internal modem when using an ATX power supply.
- On-board W83782D supports system monitoring (monitors the CPU and system temperature, voltages and fan speed) (manufacturing option).



BIOS

- Licensed advanced AWARD BIOS, supports DIP flash ROM, plug and play ready.
- Supports IDE CD-ROM or SCSI boot up.

Green function

- Supports ACPI (Advanced Configuration and Power Interface) and ODPM (OS Directed Power Management).
- Supports three green modes: Doze, Standby and Suspend.

Expansion slots

- 2 ISA slots and 3 PCI slots.
- 1 AGP Slot.

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Chapter 2

Installation Instructions

This section covers Jumper Settings, External Connectors and Memory Configuration. Refer to the motherboard layout chart for locations of all jumpers, external connectors, slots and I/O ports. Furthermore, this section lists all necessary connector pin assignments for your reference. The particular state of the jumpers, connectors and ports are illustrated in the following figures. Before setting the jumpers or inserting these connectors, please pay attention to the directions.

Be sure to unplug the AC power supply before adding or removing expansion cards or other system peripherals, otherwise your motherboard and expansion cards might be seriously damaged.

Jumper Settings

Hardware jumper settings are integrated on the motherboard. Pin1 of all jumpers are located on side with a thick white line (Pin1fi), referring to the motherboard silkscreen. Jumpers with three pins will be shown as **b** which depicts pin1 & pin2 connected and **b** depicting pin2&pin3 connected. Numerically, "1-2" means connecting pin1& pin2. "3-4" means connecting pin3&pin4. "5-6" means connecting pin5&pin6. "---" means open.

Please refer to the chart below for the location of all jumpers related to CPU frequency setting.



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System Clock Selection (JF1, JF2, JF3)

These jumpers set the external frequency of the CPU, namely the Bus Clock. The AGP bus clock and PCI bus clock are also listed for your reference.

JF1	JF2	JF3	CPU	AGP	PCI
			(MHz)	(MHz)	(MHz)
2-3	1-2	1-2	66.6	66.6	33.3
2-3	2-3	1-2	75	75	37.5
1-2	1-2	1-2	100	66.6	33.3
1-2	1-2	2-3	103	66.67	34.33
1-2	2-3	1-2	112	74.6	37.3
1-2	2-3	2-3	133.3	88.87	44.43

Clock Multiple Selection (JFREQ1)

The motherboard provides eight selections of Clock Multiple. See the following jumper settings for details.

	JFREQ1							
Multiple	pin1 & pin2	pin3 & pin4	pin5 & pin6					
2.0	1-2	3-4	5-6					
2.5	1-2	3-4						
3.0	1-2		5-6					
3.5	1-2							
4.0		3-4	5-6					
4.5		3-4						
5.0			5-6					
5.5								

The Bus Clock multiplied by the Clock Multiple equals the CPU internal frequency. Carefully set the Bus Clock and Clock Multiple by referring to the CPU list below.

CPU Model	Freq. (MHz)	SC (MHz)	Ratio	JF1	JF2	JF3	JFREQ1
	233	66	3.5	2-3	1-2	1-2	1-2
	266	66	4.0	2-3	1-2	1-2	3-4, 5-6
Intel	300	66	4.5	2-3	1-2	1-2	3-4
Pentium II	333	66	5.0	2-3	1-2	1-2	5-6
(or Celeron)	350	100	3.5	1-2	1-2	1-2	1-2
	400	100	4.0	1-2	1-2	1-2	3-4, 5-6
	450	100	4.5	1-2	1-2	1-2	3-4
	500	100	5.0	1-2	1-2	1-2	5-6
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Memory Frequency Setting (J2)

Memory clocks can be programmed synchronous with either CPU external clocks or AGP clocks. Choose your appropriate setting by setting J2 differently, referring to the list below. Syn. with CPU clock is more stable than Asyn., therefore if PC-100 spec. SDRAMs are used on your system, sychronous with CPU clock. If PC-66 spec. SDRAMs or EDO DIMMs are used on your system, and the System Bus Clock is lower than or equals to 75MHz, synchronous with CPU clock. However, if the System Bus Clock is higher than 75MHz, synchronous with AGP clock.

J2	Memory Frequency
2-3	Same as CPU external frequency
1-2	Same as AGP frequency

Clear CMOS (JCC)

If you want to clear CMOS, unplug the AC power supply first, close JCC(pin2&pin3) once, set JCC back to the normal status with pin1&pin2 connected, then power on the system.



Installation Instruction

External Connectors

Keyboard Connector, PS/2 Mouse Connector

A standard AT size keyboard utilizes the keyboard connector. If using a PS/2 keyboard, an adapter should be used to fit this connector. A PS/2 mouse adapter with bracket is provided for utilizing a PS/2 mouse.

for utilizing a PS/2 mouse.



USB1, USB2

Two USB ports are available for connecting USB devices.



Parallel Port Connector and Serial Port Connector (UART1, UART2)

The parallel port connector can be connected to a parallel device such as a printer, while the serial port connectors can be connected to serial port devices such as a serial port mouse. You can enable/disable them and choose the IRQ or I/O address in "Integrated Peripherals" from AWARD BIOS SETUP. 1Parallel ribbon cable and 2 serial ribbon cables (9-pin connector, 25-pin connector) with brackets are provided for your convenience.



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AT Power Supply Connector(ATPS)

Insert the AT power supply plugs (P8 and P9) to this connector. Make sure the direction is right by referring to the picture below.



Direction of Power Plugs (P8&P9)

ATX Power Supply Connector(ATXPS)

When using an ATX power supply, be sure to connect the ATX power supply plug to the power connector in its proper orientation, and the power switch(power) should be connected to a momentary switch. when powering up your system, first turn on the mechanical switch of the power supply (if one is provided), then push once the power button. When powering off the system, you needn't turn off the mechanical switch, just



Note: * If you change "soft-off by PWR-BTTN" from default "Instant-off" to "Delay 4 Secs" in the "POWER MANAGEMENT SETUP" section of the BIOS, the power button should be pressed for more than 4 seconds before the system powers down.

Hard Disk LED Connector (HD_LED)

The connector connects to the case's IDE indicator LED indicating the activity status of IDE hard disk.

Reset Switch (RESET)

The connector connects to the case's reset switch. Press the switch once, the system resets.



Speaker Connector (SPEAKER)

The connector can be connected to the speaker on the case.

Power LED Connector (PWR_LED)

The power LED has three status. When no AC power supply is present, the LED is off. When the system is in soft power-down status, the LED glows dimly. When the system is powered up, the LED is on.

Key-Lock Connector (KEY_L)

The connector can be connected to the keyboard lock switch on the case for locking the keyboard.

Green LED Connector (GREEN_LED)

The green LED has four status. When no AC power supply is present, the LED is off. When the system is in soft power-down status, the LED glows dimly. When the system is powered up, the LED is on. When the system enters suspend mode, the LED will flash. If an AT power supply is used, the soft power-down status would not be available.

Hardware Green Connector (SLEEP)

Push once the switch connected to this header and the system enters suspend mode.



Infrared Header (IrDA)

This connector supports wireless transmitting and receiving. If using this function, configure the settings of 'Serial Port 2 Mode' from the INTEGRATED PERIPHERALS section of the BIOS.





Fan Connector (CPUFAN, CHSFAN, BAKFAN)

If the system monitor hardware is integrated on the motherboard, the system detects the fan speed, which can be viewed in "System Monitor" of CMOS setup.



Wake-Up On LAN (WOL)

Through the Wake-Up On LAN function, a wake event occurring from the network can wake up the system. If this function is to be used, please be sure an ATX 2.01 power supply of which 5VSB line is capable of delivering 720mA, and a LAN adapter which supports this function, are used. Then connect this header to the relevant connector on the LAN adapter, set "Wake Up On LAN/Ring" as Enabled in the "POWER MANAGEMENT SETUP" section of the BIOS. Save & exit, then boot the operating system once to make sure this function takes effect.

+5V standby Signal for waking up (active high)



Wake-Up On Internal Modem (WOM)

Through the Wake-Up On Internal Modem function, the system which is in the power-off status can be powered on by a ring signal received from the internal modem. If this function is to be used, be sure an internal modem card which supports the function is used. Then connect this header to the relevant connector on the modem card, set "Wake Up On LAN/Ring" as Enabled in the "POWER MANAGEMENT SETUP" section of the BIOS. Save & exit, then boot the operating system once to make sure this function takes effect.





Sound Connector (PC-PCI)

The PCI sound card utilizes this connector.



Expansion Slots & I/O Ports description

Slot / Port	Description	
ISA 1	First ISA slot.	
ISA 2	Second ISA slot.	
PCI1	First PCI slot.	
PCI2	Second PCI slot.	
PCI3	Third PCI slot.	
IDE1	Primary IDE port.	
IDE2	Secondary IDE port.	
FLOPPY	Floppy Drive Port.	
AGP	Accelerated Graphics Port.	

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Memory Configuration

This motherboard provides two 168 pin 3.3V un-buffered DIMM sockets which supports a flexible memory size ranging from 8MB/256MB for SDRAM or from 8MB/512MB for EDO memory. Both 66MHz/100MHz SDRAM with SPD and 66MHz EDO DIMMs are supported. The following set of rules allow optimum configurations.

General DIMM notes:

- EDO/SDRAM DIMMs can not be used on the same system, it is advised you use only one kind of DIMM.
- Using the serial presence detect (SPD) data structure, programmed into an E²PROM on the DIMM, the BIOS can determine the SDRAM's size and speed.
- The DRAM Timing register, which provides the DRAM speed grade control for the entire memory array, must be programmed to use the timing of the slowest DRAMs installed.
- Possible SDRAM DIMM memory sizes are 8MB, 16MB, 32MB, 64MB, 128MB in each DIMM socket.
- Possible EDO DIMM memory sizes are 8MB, 16MB, 32MB, 64MB, 128MB, 256MB in each DIMM socket.
- Memory clocks can be programmed synchronous with either CPU clocks or AGP clocks. Choose your appropriate settings by setting J2 differently. Syn. is more stable than Asyn., so if PC-100 spec. SDRAMs are used on your system, synchronous with CPU clock. If PC-66 spec. SDRAMs or EDO DIMMs are used on your system, and the System Bus Clock is lower than or equals to 75MHz, synchronous with CPU clock. However, if the System Bus Clock is higher than 75MHz, synchronous with AGP clock. Refer to 'Jumper Settings' for information on how to set jumper J2.

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Installation Instruction

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Chapter 3 BIOS Description

Utility Support:

FLASH.EXE

This is a flash memory write/read utility used for the purpose of updating your BIOS when necessary. Before doing so, please note:

- We strongly recommend you only upgrade BIOS when encountering problems.
- Before upgrading your BIOS, review the description below to avoid making mistakes, resulting in a destroyed BIOS and a non-working system.

When you are encountering problems, for example, you find your system doesn't support the new CPU which is released after our current motherboard, you may therefore update the BIOS.

Follow the steps exactly for a successful upgrade.

1. Create a bootable system floppy diskette, by typing FORMAT A:/s from the DOS prompt under DOS6.xx or Windows 9x environment.

2. Copy FLASH.EXE from the directory \Utility located on the QDI Motherboard Utility CD onto your new bootable diskette.

- 3. Obtain the updated BIOS file from your retailer. Please be sure to get the suitable BIOS file for the motherboard.
- 4. Uncompress the file received, copy the BIOS file (xx.bin) onto the bootable diskette, and note the checksum of this BIOS which is included in readme file.
- 5. Reboot the system from the bootable diskette created.
- 6. Then run the FLASH utility at the A:\ prompt. During this process, the system will prompt : 'Do you want to save the BIOS(Y/N)'. If you type 'Y', the system will prompt for the BIOS name. The system will also display the checksum which should be exactly the same as the checksum you copied from the readme file. Don't turn off power or reset the system until the BIOS upgrade has been completed.

Concerning how to run the FLASH utility, please refer to the following descriptions:

Usage: FLASH [BIOSfile] [/c[<command...>]][/n]

FLASH [BIOSfile] [/g]

/c: Flashing memory will clear previous settings. Default allows settings to remain.

<command> function definition:

c: clear CMOS;

p: clear PnP;

d: clear DMI.

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Award BIOS Description

/n: programs BIOS without prompting. If this option is chosen:

Be sure your new BIOS is compatible with your MB. If not, the system will be damaged.

/g: Retrieves BIOS file from BIOS ROM.

Examples:

A:\FLASH.EXE BIOSfile.bin A:\FLASH.EXE BIOSfile.bin /cdpc/n A:\FLASH.EXE BIOSfile.bin /g

Note: FLASH utility runs incorrectly at Windows DOS prompt.

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AWARD BIOS Description

Entering Setup

Power on the computer, when the following message briefly appears at the bottom of the screen during the POST (Power On Self Test), press key or simultaneously press the <Ctrl> + <Alt> + <Esc> keys, to enter the AWARD BIOS CMOS Setup Utility.

Press to enter SETUP

Once you have entered, the Main Menu (Figure 1) appears on the screen. The main menu allows you to select from eleven setup functions and two exit choices. Use the arrow keys to select among the items and press the <Enter> key to accept or enter the submenu.

ROM PELVISA BIOS (2061-F019) CHOS SETUP UTLITY RHARD SUFTMARE, INC.			
STANDARD CNOS SETUP	INTEGRATED PERIPHERALS		
BIOS FEATURES SETUP	System Monitor		
CHIPSET FEATURES SETUP	SUPERVISOR PASSMORD		
POWER WANAGEWENT SETUP	USER PASSMORD		
PMP/PCI CONFIGURATION	IDE HOD AUTO DETECTION		
LORD SETUP DEFAULTS	SHVE & EXIT SETUP		
	EXIT WITHOUT SAVING		
Esc : Quit F10 : Save & Exit Setup	1 # + : Select Item (Shift)F2 : Change Color		

Figure-1 Main Menu

Note:The 'System Monitor' item will not be displayed if there is no system monitor hardware on the motherboard.

Load Setup Defaults

The Setup Defaults are common and efficient. It is recommended that users load the setup defaults first, then modify the needed configuration settings.

Standard CMOS Setup

The basic CMOS settings included in 'Standard CMOS Setup' are Date, Time, Hard Disk Drive Types, Floppy Disk Drive Types, and VGA etc. Use the arrow keys to highlight the item, then use the <PgUp> or <PgDn> keys to select the value you want in each item.

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Award BIOS Description OM PCI/ISA BIOS (2869K010) STANDARD CNOS SETUP AWARD SOFTWARE, INC. 3 1998 Date (nm:dd:yy) Time (hh:mn:ss) Sat : Jan 20 HARD DISKS TYPE CYLS HERD PRECOMP LANDZ SECTOR MODE Auto Auto 00000 ste Auto Auto SL 1 1.44M, 3.5 in. None Drive A Drive B 97280 ECA/VCA All Error Video Halt On lotal H 983048 nory PU/P0/+/-Hodifu (Shift)F2 Select Chapee Duit Hele Iten

Figure-2 Standard CMOS Setup Menu

Hard Disk

Primary Master/Primary Slave/Secondary Master/Secondary Slave

These categories identify the HDD types of 2 IDE channels installed in the computer system. There are three choices provided for the Enhanced IDE BIOS: None, Auto, and User. 'None' means no HDD is installed or set; 'Auto' means the system can auto-detect the hard disk when booting up; by choosing 'user', the related information should be entered regarding the following items. Enter the information directly from the keyboard and press < Enter>:

CYLS	number of cylinders	HEAD	number of heads
PRECOMP	write pre-compensation	LANDZ	landing zone
SECTOR	number of sectors	MODE	HDD access mode

Video

Set this field to the type of video display card installed in your system.

EGA/ VGA	Enhanced Graphics Adapter / Video Graphic Array. For EG	
	VGA, SEGA, SVGA, or PGA monitor adapters.	
CGA 40	Color Graphic Adapter, powering up in 40 column mode.	
CGA 80	Color Graphic Adapter, powering up in 80 column mode.	
MONO	Monochrome adapter, including high resolution monochrome adapters.	

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Halt On

This category determines whether or not the computer will stop if an error is detected during powering up.

No errors	The system boot will not stop for any errors that may be detected.
All errors	Whenever the BIOS detects a non-fatal error, the system will stop and you will be prompted.
All, But Keyboard	The system boot will not stop for a keyboard error; but it will stop for all other errors.
All, But Diskette	The system boot will not stop for a disk error; but it will stop for all other errors.
All, But Disk/Key	The system boot will not stop for a keyboard or disk error, but it will stop for all other errors.

Memory

This is a Display-Only Category, determined by POST (Power On Self Test) of the BIOS.

Base Memory	nory The POST of the BIOS will determine the amount of base	
	(or conventional) memory installed in the system.	
Extended Memory	The BIOS determines how much extended memory is	
	presented during the POST.	
Other Memory	This is the memory that can be used for different	
	applications. Shadow RAM is most used in this area.	
Total Memory	Total memory of the system equals the sum of the above	
	memory.	

Award BIOS Description

BIOS Features Setup

ROM PCL/ISB BIOS (2HGLF019) BIOS FERTURES SETUP ANARO SOFTWARE, INC.		
Virus Warning Disabled CPU L1 Cache Enabled CPU L2 Cache Enabled CPU L2 Cache Enabled CPU L2 Cache Enabled Boat From UN Self Test Enabled Boat From UN First Disabled Boat Sequence C.A.SCSI Swap Floopy Drive Disabled Boat H20 Option Fast Gene H20 Option Fast Memory Parity/EDC Check Enabled Disabled Satur DB Satur DB Satur DS Control Bast Sequence Cache	Video 810S Shadow : Enabled CU000-OFFF Shadow : Disabled DR000-DFFF Shadow : Disabled DR000-DFFF Shadow : Disabled DU0000-OFFF Shadow : Disabled DU0000-OFFF Shadow : Disabled DC000-DFFFF Shadow : Disabled	
Report No FUU FOF 418 95 : No	ESC Quit ti++:SelectTiem F1 Help PU/PD/-/ Hodify F5 Old Values (Shift)F2 Color F7 Lood Setup Defaults	

Figure-4 BIOS Features Setup Menu

The following indicates the options for each item and describes their meaning.

<u>ltem</u>	<u>Option</u>	Description
 Virus Warning 	Enabled	Activated automatically when the system boots up
		causing a warning message to appear when
		anything attempts to access the boot sector or
		hard disk partition table.
	Disabled	No warning message appears when anything
		attempts to access the boot sector or hard disk
		partition table.
CPU	Enabled	Enables CPU internal Level1/Level2 cache.
L1/L2 Cache	Disabled	Disables CPU internal Level1/Level2 cache.
CPU	Enabled	Enables CPU L2 Cache ECC (Error Checking
L2 Cache		and Correction) function.
ECC	Disabled	Disables CPU L2 Cache ECC function.
Quick Power	Enabled	Enables quick POST. BIOS will shorten or skip some
On Self Test		check items during POST to speed up POST after
		you power on the computer.
	Disabled	Normal POST.
 Boot from 	Enabled	Boot from LAN is ahead of any boot sequence
LAN first		selection (LAN adapter must support this function)
	Disabled	Does not boot from LAN first.
Boot Sequence	C,A,SCSI,	Any search sequency can be chosen for booting.
	C,CDROM,A	
	LS/ZIP, C	Evolution the application of ASD flags, drives
 Swap Floppy Drive 	Disabled	The assignment of A&B floppy drives are normal
Dive	Disabled	The assignment of Act hoppy unves are normal.
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 Boot Up 	On	Keypad is used as number keys.
Numlock Status	Off	Keypad is used as arrow keys.
Gate A20 Option	Normal	The A20 signal is controlled by the keyboard controller or chipset hardware.
	Fast	Default setting. The A20 signal is controlled by Port 92 or the chipset specific method.
Password Setting	System	The system will not boot and access to BIOS Setup will be denied if the correct password is not entered when prompted.
	Setup	The system will boot up, but access to BIOS Setup will be denied if the correct password is not entered when prompted.
 IDE Second 	Enabled	Enables the second IDE channel.
Channel Control	Disabled	Disables the second IDE channel and releases the IRQ.
 OS Select For DRAM>64MB 	Non-OS2	If your operating system is not OS/2, please select this item.
	OS2	If system DRAM is more than 64MB and the operating system is OS/2, please select this item.
 Report No FDD 	Yes	Reports no FDD for Win95.
For Win95	No	Does not report FDD for Win95.
 Video BIOS Shadow 	Enabled	Video BIOS will be copied to RAM. Video Shadow will increase the video speed.
	Disabled	Video shadow is disabled.
 C8000~CBFFF Shadow: DC000-DFFFF 	Enabled	Optional ROM will be copied to RAM by 16K bytes per unit.
Shadow:	Disabled	The shadow function is disabled.

Award BIOS Description

Chipset Features Setup

ROM PEL/ISO BIOS (2061-019) CHIPSEI FEDIDRES SETUR ANNRO SUFTWIRE, INC.						
Bank 0/1 DRAM Timing : EP/LDO /2006 Bank 2/3 DRAM Timing : FP/EDO /Dras SDRAM Cycle Length : 3 Namory Hole At ISMb Addr: Disabled Rend Around write : Disabled Concurrent PCI/Nost : Disabled Vides RAM Cacheale : Disabled AGP Aperture Size : 66M	OnChip USB Disabled Close Empty DINK/PCI Clk Enabled Clock Spread Spectrum Enabled ESC : Quit 11++ : Select Item F1 : Help PU/PU/+/ : Modify F5 : Old Values (Shif1)E2 : Calor F7 : Load Setup Defaults					

Figure-5 Chipset Features Setup Menu

The following indicates the options for each item and describes their meaning.

<u>ltem</u>	<u>Option</u>	Description
 Bank 0/1, 2/3, 	60ns	These items are of selected EDO DRAM
DRAM Timing	70ns	read/write timing. Ensure your
-	Normal	DIMMs are as fast as 60ns, otherwise
	Medium	select 70ns. The faster you choose, the higher
	Fast	performance you receive.
 SDRAM Cycle 	2/3	Define the CLT timing parameter of SDRAM
Length		expressed in 66MHz clocks.
		Latency Time = 2 clocks
		Latency Time = 3 clocks
 Memory Hole at 	Enabled	Memory Hole at 15~16M is reserved for expanded
15MB Addr		ISA card.
	Disabled	Do not set this memory hole.
Read Around Write	Enabled	Enables read around Write.
	Disabled	Disables read around write.
 Concurrent PCI/ 	Enabled	Enables concurrent PCI/Host.
HOST	Disabled	Disables concurrent PCI/Host.
Video RAM	Enabled	Besides conventional memory, video RAM is
Cacheable		also cacheable.
	Disabled	Video RAM area is not cacheable.
 AGP Aperture Size 	4~256	Sets the effective size of the Graphics Aperture
(MB)		to be used in the particular PAC Configuration.
 Onchip USB 	Enabled	Enables the onchip USB controller.
	Disabled	Disables the onchip USB controller.
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- USB Keyboard Support
 Close Empty DIMM/PCI Clk
- Clock Spread
 Spectrum

Enabled USB Disabled USB Enabled Close EMI. Disabled Does Enabled Enab Disabled Disabled

USB keyboard support is enabled. USB keyboard support is disabled. Closes empty DIMM or PCI clock to reduce EMI. Does not close empty DIMM or PCI clock. Enables Clock Spread Spectrum to reduce EMI. Disables Clock Spread Spectrum.

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Award BIOS Description

Power Management Setup

ROM PC1/150 B	BIOS (296LF019)
POWER MONAGE	EMENT SETUP
ANARO SOFT	WARE, INC.
Power Management : User Define PN Control by APM : Yes Video Dff Reined : V/H SYNC-Blank HOBEH Use IRQ : NH Soft-Off by PURETN : Instant-Off 	Primary INTR ON 1803 (COM 2) Primary 1804 (COM 2) Primary 1805 (LPT 2) Primary 1806 (LPT 2) Primary 1806 (LPT 2) Primary 1806 (LPT 2) Primary 1807 (LPT 1) Primary 1808 (RC Alarm) Disabled 1809 (R02 Redir) Secondary 1809 (R02 Redir) Secondary 18010 (Reserved) Secondary 18011 (Reserved) Secondary 18012 (PS/2 Nouse) Primary 18013 (Coprocessor) Primary 18014 (Reserved) Disabled 18025 (Reserved) Disabled 18024 (Herd Disk) Primary 18015 (Reserved) Disabled 18025 (Did Values (Shift)F2 18026 (Did Values (Shift)F2 18027 Nouse (Shift)F2 18028 Setup Defaults

Figure-6 Power Management Setup Menu

The following indicates the options for each item and describes their meaning.

<u>ltem</u>	<u>Option</u>	Description
• Power	User Define	Users can configure their own Power Management
Management		Timer.
	Min Saving	Pre - defined timer values are used. All timers are
		in their MAX values.
	Max Saving	Pre - defined timer values are used. All timers are in
		their MIN values.
PM Control by	No	System BIOS will ignore APM when Power
APM		Management is enabled.
	Yes	System BIOS will wait for APM's prompt before
		entering any PM mode e.g. Standby or Suspend.
Video off Option	Suspend-off	The system BIOS will disable the video when
		entering suspend mode.
	All Modes-off	The system BIOS will disable the video when
		entering all power-saving mode.
	Always On	The video remains on.
 Video Off 	Blank Screen	The system BIOS will only blank off the screen
Method		when disabling video.
	V/H SYNC +	In addition to Blank Screen, BIOS will also turn
	Blank	off the V-SYNC & H - SYNC signals from VGA
		cards to monitor.
	DPMS	This function is enabled only for the VGA card
	Support	supporting DPMS.
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Soft-off by	Instant-off	The system will power off immediately once the
PWRBTN		power button is pressed.
	Delay 4 Sec	The system will not power off until the power button has been pressed continuously for more than 4 seconds.
HDD Power	Disabled	Disables HDD Power Down Timer.
Down	1 ~15 Min	Defines the continuous HDD idle time before the HDD enters power saving mode (motor off).
 Doze mode 	Disabled	The system never enters Doze mode.
	10Sec ~ 1 Hr	Defines the continuous idle time before the system enters Doze mode. If any items defined in "PM Events" are on and activated,
		the system will be woken up.
 Suspend Mode 	Disabled	The system never enters Suspend mode.
	10Sec~ 1Hr	Defines the continuous idle time before the system enters Suspend mode. If any items defined in "PM Events" are on and activated, the system will
		be woken up.
• VGA	On	VGA active reloads global timer.
	Off	VGA active has no influence to global timer.
 LPT&COM 	LPT/COM	Set the options of these items to reload global timer.
HDD&FDD	OFF/ON	
DMA/master	ON/OFF	
 Wake Up On LAN/ Ring 	Enabled	Allows the system to be powered on when a remote wake up signal comes up to the WOL header from LAN adapter, or when a ring indicator signal comes up to UART1/UART2 from an external modem or comes up to WOM header from an internal modem card.
	Disabled	Does not allow wake up on LAN.
 RTC Alarm 	Enabled	RTC alarm can be used to generate a wake event
Resume		to power up the system which is in soft power-
		down status. You can set any date or any time to power up the system.
	Disabled	RTC has no alarm function.
 Primary INTR 	Primary	Reload global timer.
IRQ (3-15)	Secondary	No influence to global timer, except finishing an operation that IRQ "X" requests.
	Disabled	No influence to global timer.



Award BIOS Description

PNP/PCI Configuration Setup

RDM PELI/ISA BIDS (2061-019) PNP/PEI CONFIGURATION ANGRO SUF TAGRE, INC.					
PNP OS Installed : 85 Resources Controlled By : Menual Reset Configuration Data : Disabled IRO-3 assigned to : PCL/ISA PnP IRO-5 assigned to : PCL/ISA PnP IRO-5 assigned to : PCL/ISA PnP IRO-7 assigned to : PCL/ISA PnP IRO-2 assigned to : PCL/ISA PnP IRO-10 assigned to : PCL/ISA PnP IRO-10 assigned to : PCL/ISA PnP IRO-12 assigned to : PCL/ISA PnP IRO-12 assigned to : PCL/ISA PnP IRO-14 assigned to : Logacy ISA IRO-14 assigned to : Logacy ISA	CPU to PCI Write Buffer : Enabled PCI Dynamic Bursting : Enabled PCI Batter 0 85 Write : Enabled PCI Belay Transaction : Enabled PCI Belay Transaction : Enabled PCI Belay Transaction : Enabled AGP Master 1 85 Write : Enabled AGP Master 1 95 Write : Enabled PCI IRO Actived By : Level Assign IRO For USB : Enabled Assign INO for VDB : Enabled				
DNA-1 assigned to : PCL/ISA PAP DNA-3 assigned to : PCL/ISA PAP DNA-3 assigned to : PCL/ISA PAP DNA-5 assigned to : PCL/ISA PAP DNA-5 assigned to : PCL/ISA PAP DNA / assigned to : PCL/ISA PAP	ESC Quit 11++ Select Item F1 Help PU/PU/-/ Modify F5 Old Values (Shift)F2 : Color F7 Lood Setup Defaults				

Figure-7 PNP/PCI Configuration Setup Menu

The following indicates the options for each item and describes their meaning.

ltem	<u>Option</u>	Description
 PNP OS Installed 	Yes	Device resources assigned by PnP OS.
	No	Device resources assigned by BIOS.
Resources Controlled By	Manual	Assigns the system resources(IRQ and DMA) manually .
	Auto	Assigns system resources (IRQ and DMA) automatically by BIOS.
Reset Configuration	Disabled	The Configuration data will not be reset.
Data	Enabled	The configuration data will be reset to the default setting.
• IRQ-3~IRQ-15	Legacy ISA	The specified IRQ-x will be assigned to ISA only.
assigned to	PCI/ISA PnP	The specified IRQ-x will be assigned to ISA or PCI.
 DMA-0~DMA-7 	Legacy ISA	The specified DMA-x will be assigned to ISA only.
assigned to	PCI/ISA PnP	The specified DMA-x will be assigned to ISA or PCI.
CPU to PCI Write	Enabled	Enables CPU to PCI Write Buffer.
Buffer	Disabled	Disables CPU to PCI Write Buffer.
PCI Dynamic	Enabled	Enables PCI Dynamic Bursting.
Bursting	Disabled	Disables PCI Dynamic Bursting.
PCI Master 0 ws	Enabled	Enables PCI Master ws Write.
Write	Disabled	Disables PCI Master ws Write.

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PCI Delay	Enabled	Er
Transacition	Disabled	Di
• PCI #2 Access #1	Enabled	Er
Retry	Disabled	Di
AGP Master 1 ws	Enabled	Er
Write	Disabled	Di
• AGP Master 1 ws	Enabled	Er
Read	Disabled	Di
 PCI IRQ Actived 	Level	Se
Ву	Edge	
 Assign IRQ 	Enabled	As
for USB		us
	Disabled	Do

Assign IRQ Enabled
 for VGA Disabled

Enables PCI Delay Transaction. Disables PCI Delay Transaction. Enables PCI #2 Access #1 Retry. Disables PCI #2 Access #1 Retry. Enables AGP Master 1 ws Write. Disables AGP Master 1 ws Read. Disables AGP Master 1 ws Read. Disables AGP Master 1 ws Read. Disables AGP Master 1 ws Read.

order to release the IRQ.

Assigns an IRQ for USB. If an USB device is used, enables this item. Does not assign an IRQ for USB. If no USB device is used, disabling this item can release the IRQ. Assigns the needed IRQ for the VGA Card. Does not assign an IRQ for the VGA card, in

Award BIO\$ Description

Integrated Peripherals

ROM PET/ISR BIDS (2960-F019) INTEGRATED PERIPHERALS ANTRU SUFTWORE, INC.					
OnChio IUE Channel0 : Enabled OnChip IDE Channel1 : Enabled IDE Prufetch Mode : Enabled IDE HUD Block Mode : Enabled Primary Master PID: Auto Primary Slave PID: Auto Secondary Master PID: Auto Primary Slave UDMA: Auto Primary Master UDMA: Auto Secondary Master/UDMA: Auto Secondary Master/UDMA: Auto Secondary Master/UDMA: Auto	Onboard Serial Part 1 : Huto Onboard Serial Part 2 : Auto Serial Part 2 Node : Stenderd Onboard Parollel Part : 378/IR97 Parallel Part Mode : SPP				
Onboard FOG Controller: Enabled	ESG : Quit 74++ ; Select Item F1 : Help PU/PD/+/- Modify F5 : Old Volues (Shift)F2 : Color F7 : Load Setup Defaults				

Figure-8 Integrated Peripherals Menu

The following indicates the options for each item and describes their meaning.

ltem	<u>Option</u>	Description
OnChip IDE	Enabled	Enables OnChip IDE First/Second Channel.
channel 0/1	Disabled	Disables OnChip IDE First/Second Channel.
IDE Prefetch/Mode	Enabled	Enables IDE Prefetch Mode.
	Disabled	Disables IDE Prefetch Mode.
 IDE HDD Block 	Enabled	Allows IDE HDD to read/write several sectors
Mode		at once.
	Disabled	IDE HDD only read/write a sector once.
• IDE	Mode 0 - 4	Defines the IDE primary/secondary master/ slave
Primary/ Secondary		PIO mode.
Master/Slave PIO	Auto	The IDE PIO mode is defined by auto -detection.
• IDE	Auto	Ultra DMA mode will be enabled if an ultra DMA
		device
Primary/ Secondary		is detected.
Master/Slave UDMA	Disabled	Disables this function.
 Init Display First 	PCI SLOT	Initializes the PCI VGA first. If a PCI VGA card
		and an AGP card are installed together in the
		system, the one initialized first functions.
	AGP	Initializes the AGP first.
 Onboard FDC 	Enabled	Onboard floppy disk controller is enabled.
Controller	Disabled	Onboard floppy disk controller is disabled.

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Onboard Serial	3F8/IRQ4,	Defines the onboard serial port address and required
Port 1/2	2F8/IRQ3,	interrupt number.
	3E8/IRQ4,	
	2E8/IRQ3,	
	Auto	Onboard serial port address and IRQ are auto- matically assigned
	Disabled	Onboard serial port is disabled.
Serial Port 2	Standard	Defines Serial Port 2 as standard serial port.
Mode	Sharp IR	Supports SHARP ASK-IR protocol with maximum
		baud rate up to 57600bps.
	IrDA SIR	Supports IrDA version 1.0 SIR protocol with
		maxiumum baud rate up to 115.2Kbps.
Onboard Parallel	378/IRQ7,	Defines onboard parallel port address and IRQ
Port	278/IRQ5,	channel.
	3BC/IRQ7	
	Disabled	Onboard parallel port is disabled.
Parallel Port Mode	SPP	Defines the parallel port mode as
	EPP	Standard Parallel Port (SPP), Enhanced
	ECP,	Parallel Port (EPP), or Extended
	ECP+EPP	Capabilities Port (ECP).

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Award BIOS Description					

System Monitor

ROM PCL/ISA BIOS (2AGLF019) System Monitor ANAMO SOFTMARE, INC.					
Current CPU Texperature : 33°C/102°F Current Suxtem Texp. : 30°C/102°F Current CPUERN Speed : 2010 RPM Current CHSERN Speed : 2100 RPM Current BHKIPH Speed : 1.37 V VCCVIDICPU) Voltage : 1.37 V VTI(-1.5V) Voltage : 1.37 V -3.3V Voltage : 1.37 V -5V Voltage : 4.83 V -12V Voltage : 4.83 V -12V Voltage : 4.85 V -5V Voltage : 4.85 V BHITERV Voltage : 4.85 V BHITERV Voltage : 4.83 V	ESC : Quit 11++ : Select Itee F1 : Help PU/PU/-/-: Modify F5 : Old Values (Shift)F2 : Color F7 : Lood Setup Defaults				

Figure-9 System Monitor Menu

The following describes the meaning of each item.

ltem	Current	Description
	Data Shown	
Current CPU Temperature	39°C/ 102°F	The temperature of CPU core.
Current System Temp.	30°C/ 86°F	The temperature inside the chassis.
Current CPUFAN Speed	4320RPM	RPM(Revolution Per Minute) speed of fan
Current CHSFAN Speed	2010RPM	connected to the fan header CPUFAN/ CHSFAN/BAKFAN.
Current BAKFAN Speed	2150RPM	Fan speed value is based on an assumption that tachometer signal is two pulses per revolution; In other cases, you should regard it relatively.
VCCVID(CPU) Voltage	1.98V	Displays current Voltage values including all
VTT(+1.5V) Voltage,	1.37V	significant voltages of the motherboard.
+3.3V Voltage	3.32V	+3.3V, +5V, +12V, -12V, -5V are voltages
+ 5V	4.84V	from the ATX power supply, VTT (+1.5)
+12V	11.79V	Voltage is GTL Termination Voltage from
-12V	-12.03V	the on-board regulator, and VCCVID (CPU)
- 5V	-4.85V	Voltage is CPU Core Voltage from the on board switching Power Supply.
BATTERY Voltage	2.96V	The voltage of the Lithium battery.
• 5V STANDBY Voltage	4.83V	The voltage of 5V standby from the power
		supply.

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IDE HDD Auto Detection

The Enhanced IDE features are included in all Award BIOS. Below is a brief description of these features.

	ROM PCI/ISA BIOS (2A69KQ10) CMOS SETUP UTILITY AWARD SOFTWARE, INC.							
н	ARD DI	SKS	TYPE	SIZE C	YLS HEA	D PRECOM	P LANDZ SE	CTOR MODE
P	rimary N	/laster:	:					
			Se	elect Pr	imary Mas	ster Option (N	I=Skip): N	
							- 17	
(I SIZE	CYLS	HEAD	PRECON	IP LANDZ	SECTOR	MODE
	2(Y)	541	525	32	0	1049	67	LBA
	1	541	1050	16	65535	1049	63	NORMAL
	3	541	525	32	65535	1049	63	LARG
	Note: Some OSes (like SCO-UNIX) must use "NORMAL" for installation							

Figure-11 IDE HDD Auto Detection Menu

1. Setup Changes

With auto-detection

- BIOS setup will display all possible modes supported by the HDD including NORMAL, LBA and LARGE.
- If HDD does not support LBA modes, no 'LBA' option will be shown.
- If number of physical cylinder is less than or equal to 1024,'LARGE' option may not be shown.
- Users can select their appropriate mode .

With Standard CMOS Setup

	CYLS	HEADS	PRECOMP	LAND	SECTOR	MODE
				ZONE		
Drive C: User(516ME	3) 1120) 16	65535	1119	59	Normal
Drive D: None(203MI	3) 684	l 16	65535	685	38	

When HDD type is set as ' user' , the ' $\mathsf{MODE'}$ option will be available for users to select their own HDD mode.

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2. HDD Modes

Award BIOS Description

The Award BIOS supports 3 HDD modes: NORMAL, LBA and LARGE, also Auto detect.

<u>NORMAL</u>

Generic access mode in which neither the BIOS nor the IDE controller will make any transformation during accessing. The maximum number of cylinders, heads and sectors for NORMAL mode are 1024,16 and 63.

If the user sets his HDD to NORMAL mode, the maximum accessible HDD size will be 528 megabytes even though its physical size may be greater than that.

LBA (Logical Block Addressing) mode

A new HDD accessing method to overcome the 528 Megabyte bottleneck. The number of cylinders, heads and sectors shown in setup may not be the number physically contained in the HDD.

During HDD accessing, the IDE controller will transform the logical address described by sector, head and cylinder number into its own physical address inside the HDD. The maximum HDD size supported by LBA mode is 8.4 Gigabytes.

LARGE mode

Some IDE HDDs contain more than 1024 cylinder without LBA support (in some cases, users do not want LBA). The Award BIOS provides another alternative to support these kinds of HDD.

BIOS tricks DOS (or other OS) into recognizing the number of cylinders is less than 1024 by dividing it by 2. At the same time, the number of heads is multiplied by 2. A reverse transformation process will be made inside INT13h in order to access the right HDD address.

Auto detect

If using Auto detect, the BIOS will automatically detect the IDE hard disk mode and set it as one of the three modes.

3. Remark

To support LBA or LARGE mode of HDDs, there must be some softwares involved which are located in Award HDD Service Routine(INT13h). It may fail to access a HDD with LBA (LARGE) mode selected if you are running under an Operating System which replaces the whole INT 13h.

Boot with BIOS defaults

If you have made all the changes to CMOS values and the system can not boot with the CMOS values selected in setup, clear CMOS after power-down, then power on again. System will boot with BIOS default settings.

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Supervisor/ User Password

When this function is selected, the following message appears at the center of the screen to assist you in creating a password.

ENTER PASSWORD

Type the password, up to eight characters, and press <Enter>. The password typed now will clear any previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection.

To disable password, just press <Enter> when you are prompted to enter password. A message will confirm the password being disabled. Once the password is disabled, the system will boot and you can enter BIOS Setup freely.

PASSWORD DISABLED

If you have selected '**System**' in 'Password Setting' of 'BIOS Features Setup' menu, you will be prompted for the password every time the system reboots or any time you try to enter BIOS Setup.

If you have selected '**Setup**' at 'Password Setting' from 'BIOS Features Setup' menu, you will be prompted for the password only when you enter BIOS Setup.

Supervisor Password has higher priority than User Password. You can use Supervisor Password when booting the system or entering 'CMOS Setup' to modify all settings. Also you can use User Password when booting the system or entering 'CMOS Setup' but can not modify any setting if Supervisor Password is enabled.

Advance 4

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Award BIOS Description

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Appendix	Ч

Appendix A QDI Motherboard Utility CD-ROM

A QDI Motherboard Utility CD-ROM is supplied with each motherboard. The contents used for this motherboard are:

1. Chipset Dispatches:

Via Chipset Drivers included in the directory \ChipDrv\Via\ApolloPro&VIABX can be used for this mainboard. Run \ChipDrv\Via\ApolloPro&VIABX\Autorun.exe, installing the drivers below one by one.

(1) IDE Driver

This is Via Bus Master PCI IDE Driver which can be installed on either Windows 95 or Windows NT system, for supporting Ultra DMA/33MB. It also can remove the yellow question mark in the Device Manager of Windows 95 after installation.

- (2) IRQ Routing Program Installed on Windows 95 or Windows 98.
- (3) VxD Driver Installed on Windows 95/98 for supporting AGP. For all AGP feature benefits, you need to upgrade your Windows 95 OSR2.0 to OSR2.1 by installing USB supplement provided by Microsoft, also DirectX 5.0 from Microsoft.
- (4) ACPI Patch Program Installed on Windows 95 or Windows 98.
- 2. PC-cillin Anti-Virus software:

For Windows 95/98 English version, it is located in the directory \Pccillin\Win9x. Run Setup.exe for installation.

For Windows NT English version, it is located in the directory \Pccillin\WinNT4.0. Run

Setup.exe for installation. S/N is PNEF-9991-6558-5857-5535.

3. QDI ManageEasy:

Run Setup.exe from the directory \QME to install the ManageEasy. For detailed information about QDI ManageEasy, refer to the ManageEasy Manual included in the directory \Doc. Please note, hardware is a manufacturing option.

- QDI Motherboard Utility: The utilities located in the directory \Utility are: FLASH.EXE CBLOGO.EXE LF.EXE Refer to the online help for information on how to use these utilities.
- Documents for QDI Motherboard: The files included in the directory \Doc are: Adobe Acrobat Reader V3.0 —ar32e301.exe ManageEasy Manuals —QMEV12.PDF.

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Appendix B.

Retention Mechanism & Pentium[®] II/ Celeron[™] Processor Installation Procedures

1. Place Plastic Guide with plastic caps on the motherboard, and secure all four caps.





Celeron fittings

Note: 1. Please choose four caps which match the motherboard.

2. If choosing to use ${\sf Celeron}^{\sf TM}$ Processor, snap-on Celeron fittings onto the Plastic Guide.

3. Please note the Plastic Guide has one orientation. If one way doesn' t fit, change the direction to the other way. Do not forcefully press the Plastic Guide onto the motherboard.

2. Install HSSBASE (Heatsink Support Base) on motherboard then insert the two plastic pins through the HSSBASE securing it to the motherboard.





^{3.} Insert Pentium[®] II or Celeron[™] Processor in Slot1.



4. Clip Plastic Bar onto the HSSBASE through the fins on the processors' heatsink.





5. The Retention Mechanism installation procedure is completed as shown below.



Remark:

Please skip step2 and step4 for Boxed Pentium[®] II Processor and refer to relevant details concerning this type of processor for your installation.

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P/N : 430-01015-301-00 Manual P6VPRO/A4 Ver 1.0

Item Checklist

Completely check your package. If you discover damaged or missing items, contact your retailer.

- P6VPRO/A4 motherboard
- Motherboard Utility CD-ROM
- A Retention Module
- 1 IDE ribbon cable
- 1 floppy ribbon cable
- 1 parallel ribbon cable and 1 UART ribbon cable (9-pin) with mounting bracket.
- I UART ribbon cable (25-pin connector) and PS/2 adapter with mounting bracket.
- 🗹 User's manual

Notice

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Board Layout of Advance 4 V1.0