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1. Set J5 to CMOS RAM discharge jumper (pin 2-3)
2. Set JP7 to select CPU speed
3. Set JP6-F, G to select CPU Internal Clock Speed
4. Set JP6-A, B, C, D, E to select CPU Voltage Regulator Output
5. Insert CPU to CPU socket
6. Insert 72-pin SIMM module into SIMM1-4
7. Install mainboard into system chassis
8. Connect keyboard to J1
9. Insert the display card and other peripheral cards (if required) onto the mainboard
10. Connect hard disk(s) to IDE primary/secondary connector(s)
11. Connect floppy drive(s) to FDC1 connector
12. Connect serial ports to COM1 and COM2 connectors
13. Connect parallel port to PRN1 connector
14. Connect J7-HDD LED to "Hard Disk Busy" LED on the system chassis
15. Connect J7-TB LED to Turbo LED on the system chassis
16. Connect J7-RST to Reset Switch on the system chassis
17. Connect J7-SPK to Speaker on the system chassis
18. Connect J7-KEYLOCK to keylock and power LED on the system chassis
19. Close system chassis, connect all external cables to your computer.

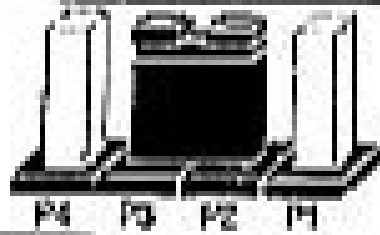
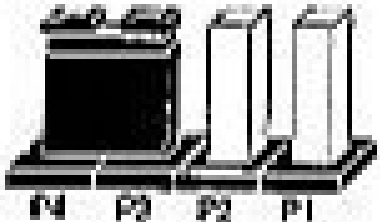
## J3 Power Supply Connector

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Pin	Description	Pin	Description
1	Power Good	7	Ground
2	+5VDC	8	Ground
3	+12VDC	9	-5VDC
4	-12VDC	10	+5VDC
5	Ground	11	-5VDC
6	Ground	12	+5VDC

## J5: CMOS RAM Discharge Jumper/External Battery Connector

Pin	Description
1	External Battery Positive
2	Internal Battery Positive
3	Connector to CMOS
4	Ground

Description	J5:
External Battery Mode	Connect an external battery to pins 1-4
Internal Battery Mode	
Discharge CMOS	

**J7: RST (Reset Switch Connector)****A101  
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Setting	Description
Open	Normal Mode
Short	Reset System

**J7: KEYLOCK (Keylock & Power LED Connector)**

Pin	Description
1	LED Output
2	N.C.
3	Ground
4	Keylock
5	Ground

**J7: TB LED (Turbo LED Connector)**

Pin	Description
1	Anode (+)
2	Cathode (-), Ground

**J7: HDD LED (Hard Disk LED Connector)**

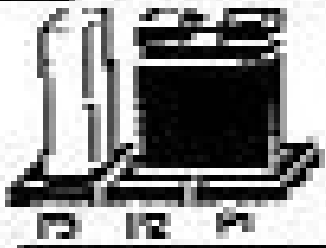
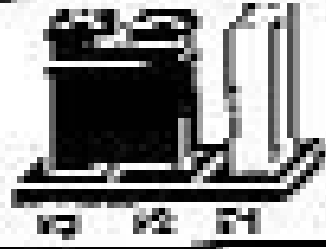
Pin	Description
1	Anode (+)
2	Cathode (-), Ground

## J7: SPK (Speaker Connector)


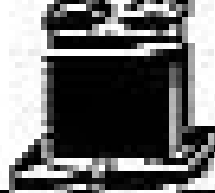
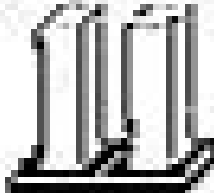
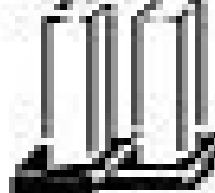

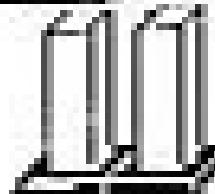
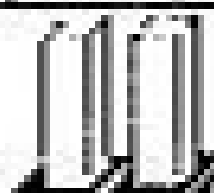

Pin	Description
1	Speaker Out
2	Ground
3	Ground
4	5V

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## JP2: Flash ROM Voltage Jumper



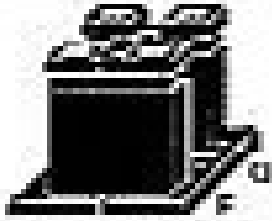
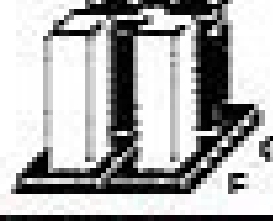
Description	JP2
12 Volt Flash programming	
5 Volt Flash programming	

## JP7: CPU Speed Jumpers

CPU Clock	JP7A	JP7B
50MHz		
55MHz		
60MHz		
66MHz		

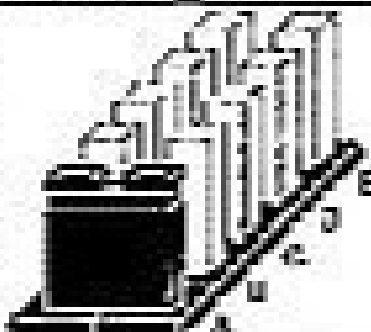
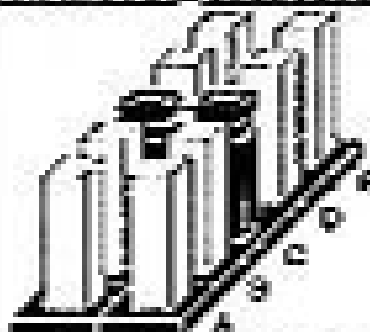

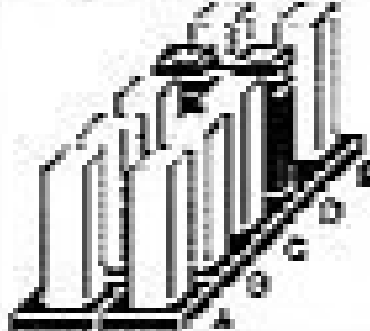
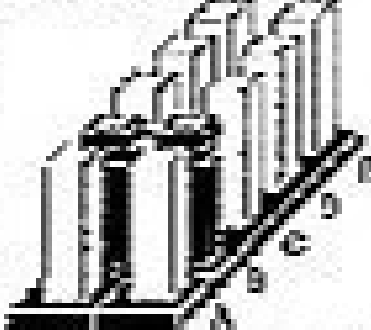
# JP6-F, G: CPU Internal Clock Speed Jumpers

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Intel	Cyrix	AMD	JP6-F
1.5X	Reserved	1.5X	
2.0X	2.0X	Reserved	
2.5X	Reserved	Reserved	
3.0X	Reserved	Reserved	

**2.5X F=ON, G=ON; 3.0X F=OFF, G=ON (RIGHT)**  
**2.5X F=OFF, G=ON, 3.0X F=ON, G=ON (WRONG)**

# JP6-A, B, C, D, E: CPU Voltage Regulator Output

	Setting		Setting
3.5V		2.8V	
3.3V		2.7V	
2.9V		2.5V	