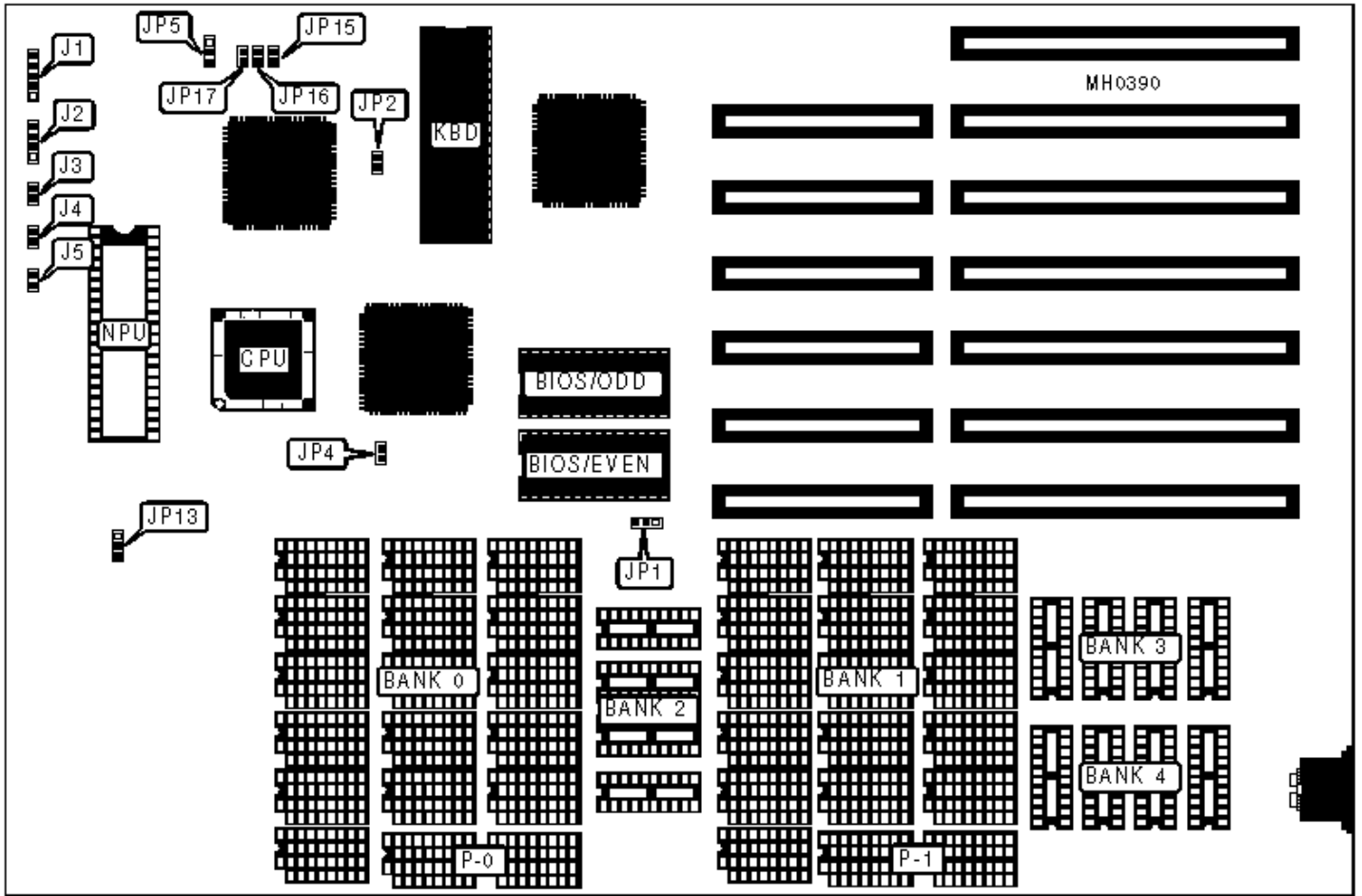


UNIDENTIFIED

286 MAIN BOARD (DIP VERSION)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Power LED & keylock	J1	Turbo switch	J4
Speaker	J2	Turbo LED	J5
Reset switch	J3		

USER CONFIGURABLE SETTINGS

	Function	Jumper	Position
	BIOS type select 27128	JP1	pins 1 & 2 closed
	BIOS type select 27256	JP1	pins 2 & 3 closed
»	Monitor type select color	JP2	Closed
	Monitor type select monochrome	JP2	Open
»	Factory configured - DRAM access speed 12MHz	JP5	pins 2 & 3 closed
	Invalid setting	JP5	pins 1 & 2 closed
»	NPU speed select ³ 8MHz	JP13	pins 1 & 2 closed
	NPU speed select _£ 6MHz	JP13	pins 2 & 3 closed
»	Bus speed select fast	JP15	Closed
	Bus speed select slow	JP15	Open
»	Early address latch enabled	JP16	Open
	Early address latch disabled	JP16	Closed
»	DRAM select 0 wait states	JP17	Closed
	DRAM select 1 wait state	JP17	Open
»	Parity check enabled	JP44	Open
	Parity check disabled	JP44	Closed
	Notes: Location of pin 1s unverified. Location of JP44 unverified		

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3	Bank 4	P0	P1	JP4
512KB	(16) 41256	NONE	NONE	NONE	NONE	(2) 41256	NONE	closed
512KB	NONE	(4) 44256	NONE	NONE	NONE	(2) 41256	NONE	closed
640KB	(16) 41256	(4) 4464	NONE	NONE	NONE	(2) 41256	(2) 4164	closed
640KB	NONE	(4) 44256	NONE	(4) 4464	NONE	(2) 41256	(2) 4164	closed
640KB	(16) 41256	NONE	(16) 4164	NONE	NONE	(2) 41256	(2) 4164	closed
640KB	NONE	(4) 44256	(16) 4164	NONE	NONE	(2) 41256	(2) 4164	closed
1MB	(16) 41256	NONE	(16) 41256	NONE	NONE	(2) 41256	(2) 41256	closed
1MB	NONE	(4) 44256	NONE	NONE	(4) 44256	(2) 41256	(2) 41256	closed
1MB	(16) 41256	NONE	NONE	NONE	(4) 44256	(2) 41256	(2) 41256	closed
1MB	NONE	(4) 44256	(16) 41256	NONE	NONE	(2) 41256	(2) 41256	closed
2MB	(16) 411000	NONE	NONE	NONE	NONE	(2) 411000	NONE	open
2.128MB	(16) 411000	NONE	(16) 4164	NONE	NONE	(2) 411000	(2) 4164	open
2.5MB	(16) 411000	NONE	(16) 41256	NONE	NONE	(2) 411000	(2) 41256	open
4MB	(16) 411000	NONE	(16) 411000	NONE	NONE	(2) 411000	(2) 411000	open