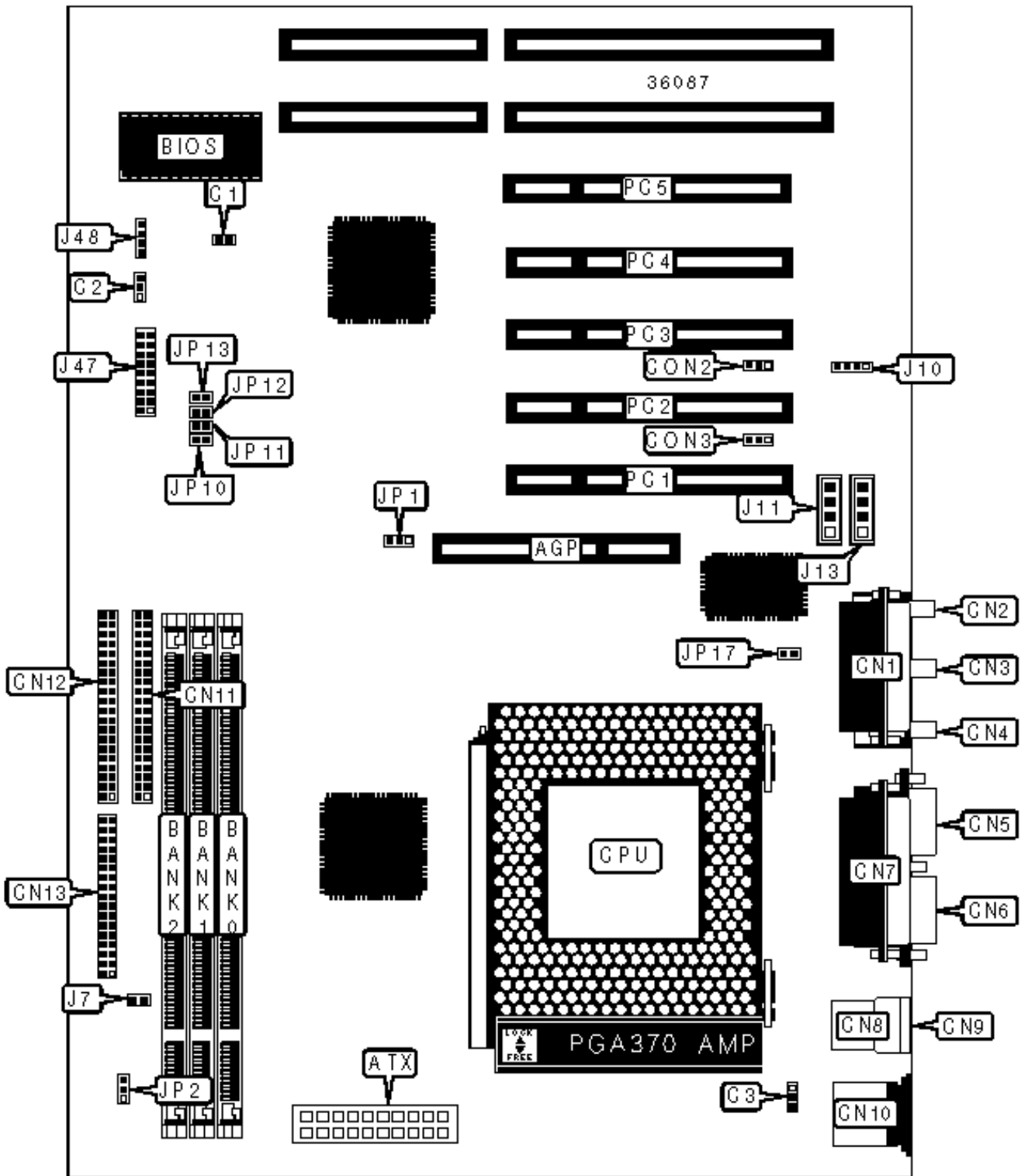


TYAN COMPUTER CORPORATION

S1856S (LA)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	PS/2 mouse port	CN10
ATX power connector	ATX	IDE interface 2	CN11
Green PC connector	C1	IDE interface 1	CN12
Chassis fan power	C2	Floppy drive interface	CN13
Chassis fan power	C3	Wake on modem connector	CON2
Game/MIDI port	CN1	Wake on LAN connector	CON3
Microphone in	CN2	Chassis intrusion connector	J7
Line in	CN3	Modem connector	J10
Line out	CN4	Audio in - CD-ROM	J11
Serial port 2	CN5	Audio in - CD-ROM	J13
Serial port 1	CN6	Front panel connector	J47
Parallel port	CN7	Speaker	J48
USB connector 1	CN8	32-bit PCI slots	PC1 - PC5
USB connector 2	CN9		

USER CONFIGURABLE SETTINGS

Function	Label	Position
» CMOS memory normal operation	JP2	Pins 1 & 2 closed
CMOS memory clear	JP2	Pins 2 & 3 closed
» Factory configured - do not alter	JP17	Unidentified

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
8MB	(1) 1M x 64	None	None

16MB	(1) 2M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
24MB	(1) 2M x 64	(1) 1M x 64	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
40MB	(1) 4M x 64	(1) 1M x 64	None
40MB	(1) 2M x 64	(1) 2M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
56MB	(1) 4M x 64	(1) 2M x 64	(1) 1M x 64
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
72MB	(1) 8M x 64	(1) 1M x 64	None
72MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64

DIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64	None
80MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64
88MB	(1) 8M x 64	(1) 2M x 64	(1) 1M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64

96MB	(1) 8M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
104MB	(1) 8M x 64	(1) 4M x 64	(1) 1M x 64
112MB	(1) 8M x 64	(1) 4M x 64	(1) 2M x 64
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
136MB	(1) 16M x 64	(1) 1M x 64	None
136MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64	None
144MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64	None
192MB	(1) 16M x 64	(1) 8M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
200MB	(1) 16M x 64	(1) 8M x 64	(1) 1M x 64
208MB	(1) 16M x 64	(1) 8M x 64	(1) 2M x 64
224MB	(1) 16M x 64	(1) 8M x 64	(1) 4M x 64
256MB	(1) 32M x 64	None	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
264MB	(1) 32M x 64	(1) 1M x 64	None
272MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
272MB	(1) 32M x 64	(1) 2M x 64	None
288MB	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
288MB	(1) 32M x 64	(1) 4M x 64	None
320MB	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64

320MB	(1) 32M x 64	(1) 8M x 64	None
384MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
384MB	(1) 32M x 64	(1) 16M x 64	None
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board accepts EDO memory.

CACHE CONFIGURATION

Note: 128KB cache is located on the Celeron 300A & 333 CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP10	JP11	JP12	JP13
300MHz	66MHz	4.5x	Open	Closed	Open	Closed
333MHz	66MHz	5x	Open	Open	Closed	Closed
350MHz	100MHz	3.5x	Closed	Open	Open	Closed
366MHz	66MHz	5.5x	Open	Open	Open	Closed
400MHz	100MHz	4x	Open	Closed	Closed	Closed
400MHz	66MHz	6x	Open	Closed	Closed	Open
433MHz	66MHz	6.5x	Closed	Closed	Open	Open
450MHz	100MHz	4.5x	Open	Closed	Open	Closed
466MHz	66MHz	7x	Closed	Open	Closed	Open
500MHz	100MHz	5x	Open	Open	Closed	Closed
550MHz	100MHz	5.5x	Open	Open	Open	Closed
600MHz	100MHz	6x	Closed	Closed	Closed	Open

BUS SPEED SELECTION

Speed	JP1
Auto detect	Pins 1 & 2 closed
66MHz	Pins 2 & 3 closed
100MHz	Open