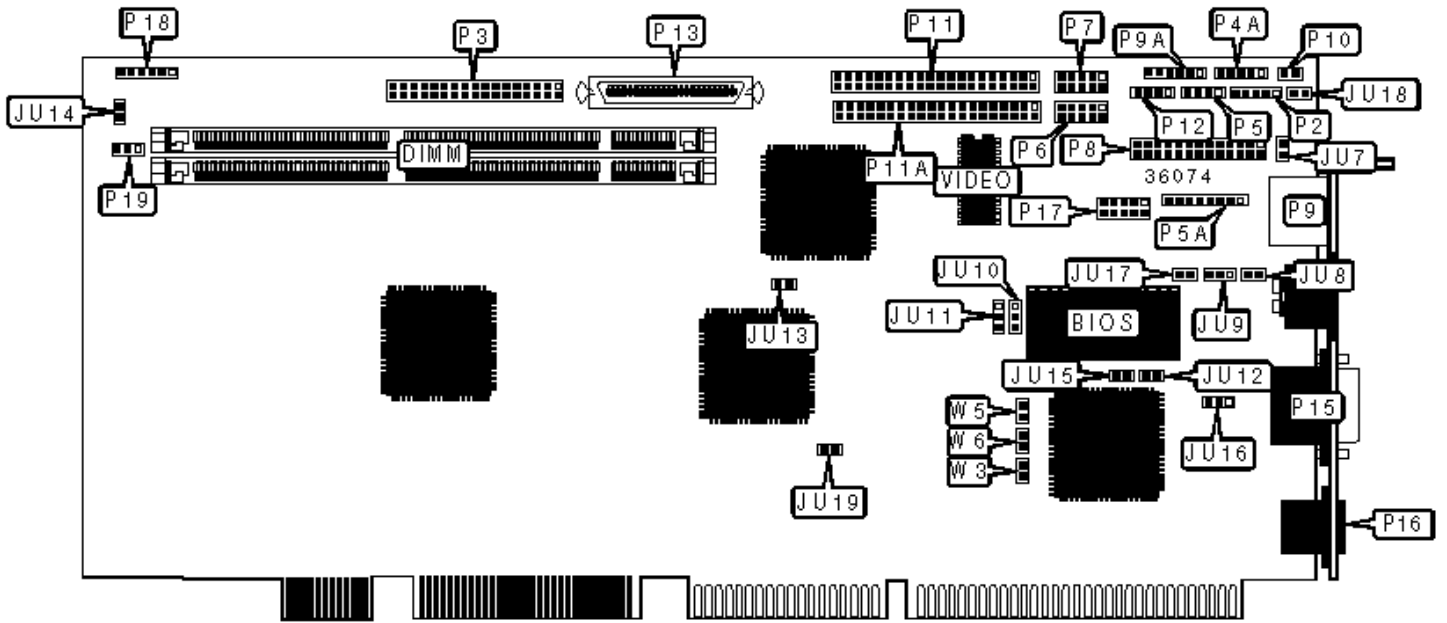


TRENTON TECHNOLOGY, INC.

TR-P2BX

Configuration



CONNECTIONS

| Purpose | Location | Purpose | Location |
|------------------------------|----------|----------------------------|----------|
| Power LED & keylock | P2 | Reset switch | P10 |
| Floppy drive interface | P3 | IDE interface 1 | P11 |
| Auxiliary keyboard connector | P4A | IDE interface 2 | P11A |
| Speaker | P5 | IDE interface LED | P12 |
| I/O connector | P5A | Ultra Wide SCSI interface | P13 |
| Serial port 1 | P6 | VGA port | P15 |
| Serial port 2 | P7 | Ethernet 10BaseT connector | P16 |
| Parallel port | P8 | USB connector | P17 |
| PS/2 mouse port | P9 | Hardware monitor connector | P18 |
| PS/2 mouse interface | P9A | CPU fan power | P19 |

USER CONFIGURABLE SETTINGS

| Function | Label | Position |
|---|-------|-------------------|
| » Speaker data signal to pin 8 on P5A enabled | JU7 | Closed |
| Speaker data signal to pin 8 on P5A disabled | JU7 | Open |
| » Password normal operation | JU8 | Open |
| Password clear | JU8 | Closed |
| » Monitor type select color | JU9 | Pins 1 & 2 closed |
| Monitor type select monochrome | JU9 | Pins 2 & 3 closed |
| » CMOS memory normal operation | JU12 | Open |
| CMOS memory clear | JU12 | Closed |
| » SCSI termination enabled | JU13 | Open |
| SCSI termination disabled | JU13 | Closed |
| » Factory configured - do not alter | JU14 | Open |

| | | | |
|---|--|------|-------------------|
| » | 3.3v monitor disabled | JU15 | Open |
| | 3.3v monitor enabled | JU15 | Closed |
| » | Watchdog timer normal reset | JU16 | Pins 2 & 3 closed |
| | Watchdog timer enabled | JU16 | Pins 1 & 2 closed |
| » | PS/2 mouse IRQ12 enabled | JU17 | Closed |
| | PS/2 mouse IRQ12 disabled | JU17 | Open |
| » | Reset data signal to pin 1 on P5A enabled | JU18 | Closed |
| | Reset data signal to pin 1 on P5A disabled | JU18 | Open |
| » | SCSI LED enabled | JU19 | Closed |
| | SCSI LED disabled | JU19 | Open |

DIMM CONFIGURATION

| Size | Bank 0 | Bank 1 |
|------|-------------|-------------|
| 8MB | (1) 1M x 64 | None |
| 16MB | (1) 2M x 64 | None |
| 16MB | (1) 1M x 64 | (1) 1M x 64 |
| 24MB | (1) 2M x 64 | (1) 1M x 64 |
| 32MB | (1) 4M x 64 | None |

DIMM CONFIGURATION (CON'T)

| Size | Bank 0 | Bank 1 |
|------|-------------|-------------|
| 32MB | (1) 2M x 64 | (1) 2M x 64 |
| 40MB | (1) 4M x 64 | (1) 1M x 64 |
| 48MB | (1) 4M x 64 | (1) 2M x 64 |
| 64MB | (1) 8M x 64 | None |
| 64MB | (1) 4M x 64 | (1) 4M x 64 |

| | | |
|---|--------------|--------------|
| 72MB | (1) 8M x 64 | (1) 1M x 64 |
| 80MB | (1) 8M x 64 | (1) 2M x 64 |
| 96MB | (1) 8M x 64 | (1) 4M x 64 |
| 128MB | (1) 8M x 64 | (1) 8M x 64 |
| 128MB | (1) 16M x 64 | None |
| 136MB | (1) 16M x 64 | (1) 1M x 64 |
| 144MB | (1) 16M x 64 | (1) 2M x 64 |
| 160MB | (1) 16M x 64 | (1) 4M x 64 |
| 192MB | (1) 16M x 64 | (1) 8M x 64 |
| 256MB | (1) 16M x 64 | (1) 16M x 64 |
| 256MB | (1) 32M x 64 | None |
| 264MB | (1) 32M x 64 | (1) 1M x 64 |
| 272MB | (1) 32M x 64 | (1) 2M x 64 |
| 288MB | (1) 32M x 64 | (1) 4M x 64 |
| 320MB | (1) 32M x 64 | (1) 8M x 64 |
| 384MB | (1) 32M x 64 | (1) 16M x 64 |
| 512MB | (1) 32M x 64 | (1) 32M x 64 |
| Note: Board accepts EDO & SDRAM memory. Maximum SDRAM is 256MB. Maximum EDO is 512MB. | | |

CACHE CONFIGURATION

Note: 512KB cache is located on the Pentium II CPU.

CPU SPEED SELECTION

| CPU speed | Clock speed | Multiplier | W3 | W5 | W6 |
|-----------|-------------|------------|--------|--------|------|
| 266MHz | 66MHz | 4x | Closed | Closed | Open |
| 300MHz | 66MHz | 4.5x | Open | Closed | Open |

| | | | | | |
|--------|--------|------|--------|--------|--------|
| 333MHz | 66MHz | 5x | Closed | Open | Open |
| 350MHz | 100MHz | 3.5x | Open | Open | Closed |
| 400MHz | 100MHz | 4x | Closed | Closed | Open |

FLASH ROM SELECTION

| Setting | | JU10 | JU11 |
|---------|---------------------------------|-------------------|-------------------|
| » | Normal PnP & program main block | Pins 2 & 3 closed | Pins 1 & 2 closed |
| | Program all | Pins 2 & 3 closed | Pins 2 & 3 closed |
| | Write protect | Pins 1 & 2 closed | Pins 1 & 2 closed |