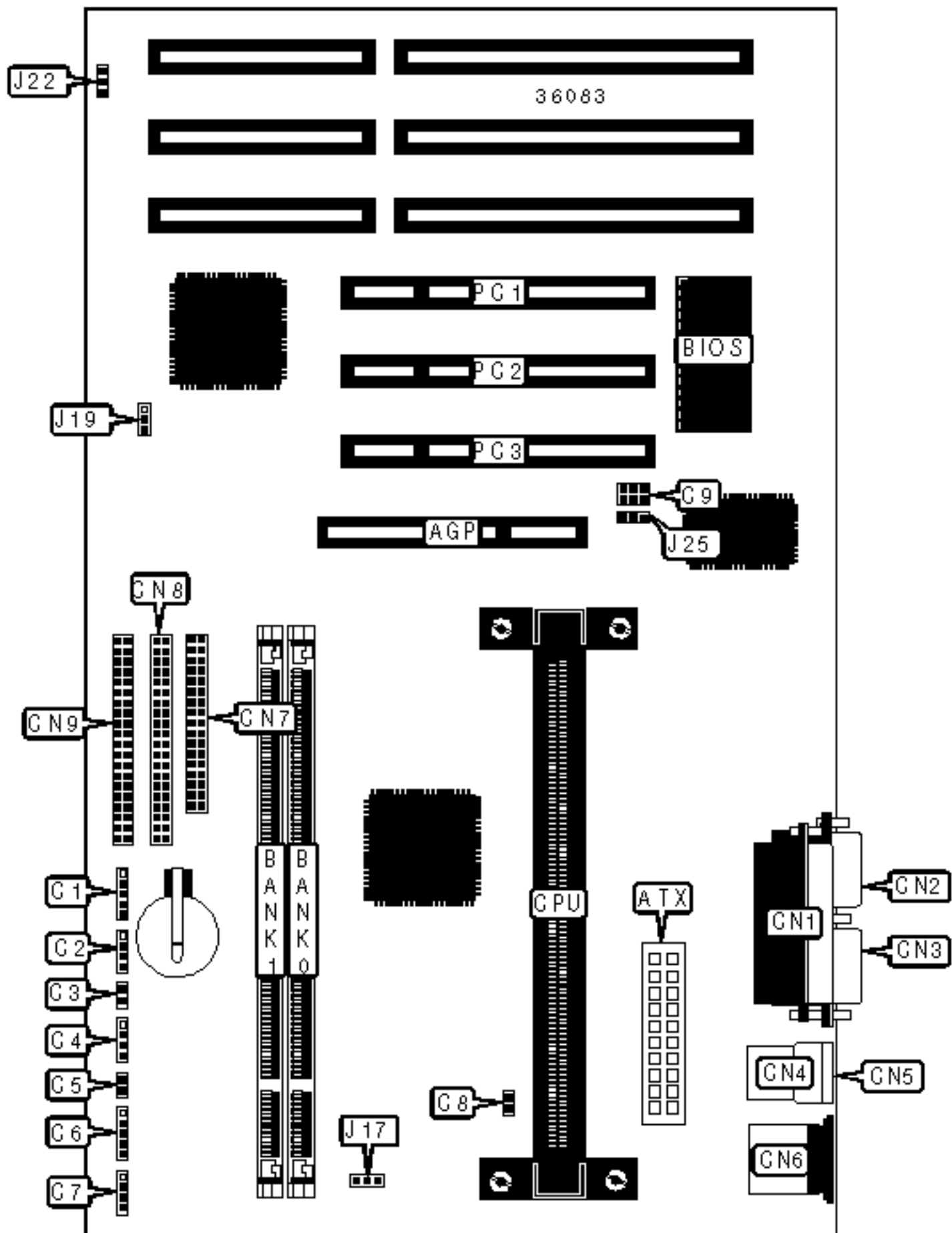


TOTEM TECHNOLOGY CO., LTD.

TM-P2EX

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	Serial port 2	CN2
ATX power connector	ATX	Serial port 1	CN3
IR connector	C1	USB connector 1	CN4
IR connector	C2	USB connector 2	CN5
Soft off power supply	C3	PS/2 mouse port	CN6
IDE interface LED	C4	Floppy drive interface	CN7
Reset switch	C5	IDE interface 1	CN8
Power LED & keylock	C6	IDE interface 2	CN9
Speaker	C7	CPU fan power	J17
Temperature sensor connector	C8	Chassis fan power	J22
SB-link connector	C9	32-bit PCI slots	PC1 - PC3
Parallel port	CN1		

USER CONFIGURABLE SETTINGS

Function	Label	Position
» CMOS memory normal operation	J19	Pins 1 & 2 closed
CMOS memory clear	J19	Pins 2 & 3 closed
» Factory configured - do not alter	J25	Unidentified

DIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64

24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None
64MB	(1) 4M x 64	(1) 4M x 64
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 16M x 64	None
128MB	(1) 8M x 64	(1) 8M x 64
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64

DIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64

Note: Board accepts EDO & SDRAM memory.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU.