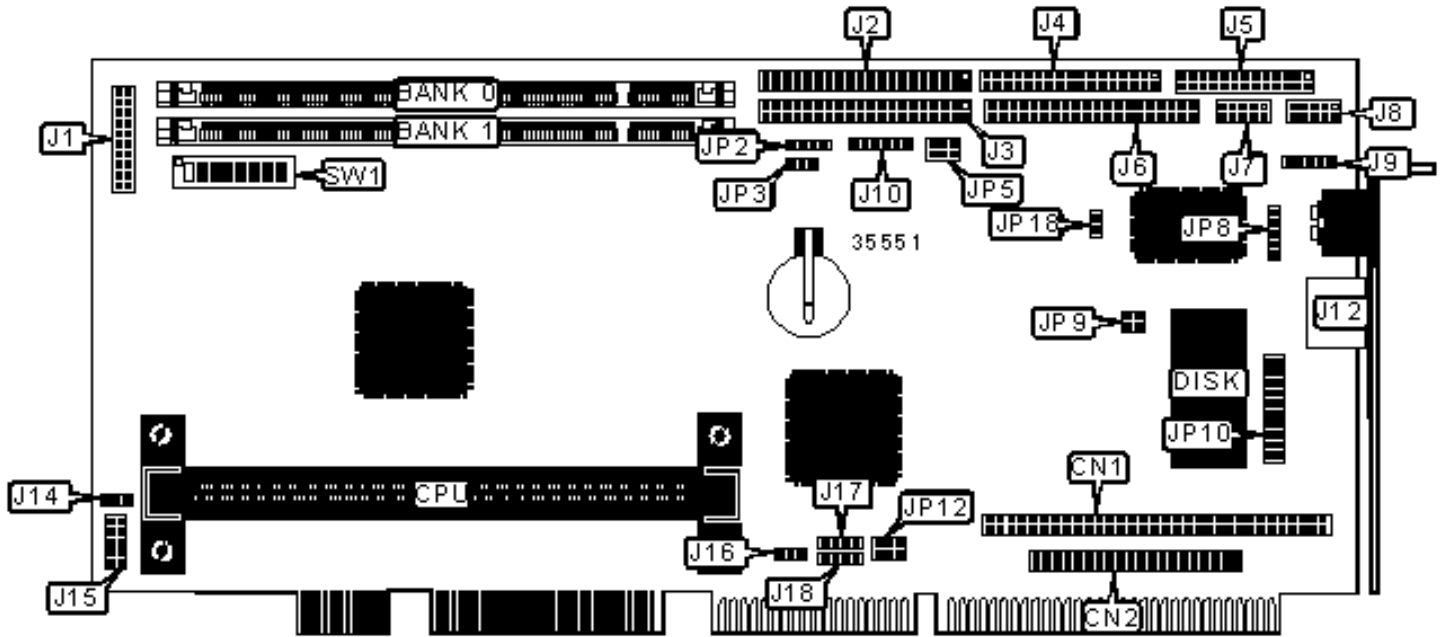


TMC RESEARCH CORPORATION

CI6BMX (VER. 1.0)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
PC/104 connector	CN1	LCD panel connector	J6
PC/104 connector	CN2	Serial port 1	J7
Speaker	J1/pins 1 - 4	Serial port 2	J8
Green PC connector	J1/pins 6 & 16	Auxiliary keyboard connector	J9
Soft off power supply	J1/pins 7 & 17	Auxiliary ATX power	J10
Turbo switch	J1/pins 8 & 18	PS/2 mouse port	J12
Reset switch	J1/pins 9 & 19	CPU fan power	J14
IDE interface LED	J1/pins 10 & 20	Chassis fan power	J16
Power LED & keylock	J1/pins 11 - 15	USB connector	J17
IDE interface 1	J2	USB connector	J18
IDE interface 2	J3	External battery	JP2
Floppy drive interface	J4	IR connector	JP8
Parallel port	J5	SB-link connector	JP12

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	J15	Unidentified
» Battery type select internal	JP2	Pins 3 & 4 closed
Battery type select external	JP2	Closed
» CMOS memory normal operation	JP3	Pins 1 & 2 closed
CMOS memory clear	JP3	Pins 2 & 3 closed
» LCD voltage select 5v	JP5	Pins 3 & 5, 4 & 6 closed
LCD voltage select 3.3v	JP5	Pins 1 & 3, 2 & 4 closed
» Display type select CRT	JP18	Closed

	Display type select LCD	JP18	Open
--	-------------------------	------	------

DIMM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(2) 1M x 64
24MB	(1) 2M x 64	(2) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(2) 2M x 64
40MB	(1) 4M x 64	(2) 1M x 64

DIMM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
48MB	(1) 4M x 64	(2) 2M x 64
64MB	(1) 8M x 64	None
64MB	(1) 4M x 64	(2) 4M x 64
72MB	(1) 8M x 64	(2) 1M x 64
80MB	(1) 8M x 64	(2) 2M x 64
96MB	(1) 8M x 64	(2) 4M x 64
128MB	(1) 8M x 64	(2) 8M x 64
128MB	(1) 16M x 64	None
136MB	(1) 16M x 64	(2) 1M x 64
144MB	(1) 16M x 64	(2) 2M x 64
160MB	(1) 16M x 64	(2) 4M x 64
192MB	(1) 16M x 64	(2) 8M x 64

256MB	(1) 16M x 64	(2) 16M x 64
256MB	(1) 32M x 64	None
264MB	(1) 32M x 64	(2) 1M x 64
272MB	(1) 32M x 64	(2) 2M x 64
288MB	(1) 32M x 64	(2) 4M x 64
320MB	(1) 32M x 64	(2) 8M x 64
384MB	(1) 32M x 64	(2) 16M x 64
512MB	(1) 32M x 64	(2) 32M x 64
Note: Board accepts SDRAM memory.		

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
233MHz	66MHz	3.5x	Off	Off	Off	Off
266MHz	66MHz	4x	Off	Off	Off	Off
300MHz	66MHz	4.5x	Off	Off	Off	Off
300MHz	100MHz	3x	Off	Off	Off	Off
333MHz	66MHz	5x	Off	Off	Off	Off
350MHz	100MHz	3.5x	Off	Off	Off	Off
400MHz	100MHz	4x	Off	Off	Off	Off
450MHz	100MHz	4.5x	Off	Off	Off	Off

CPU SPEED SELECTION (CON'T)

CPU speed	Clock speed	Multiplier	SW1/5	SW1/6	SW1/7	SW1/8
-----------	-------------	------------	-------	-------	-------	-------

233MHz	66MHz	3.5x	On	Off	Off	On
266MHz	66MHz	4x	Off	On	On	On
300MHz	66MHz	4.5x	Off	On	Off	On
300MHz	100MHz	3x	On	Off	On	On
333MHz	66MHz	5x	Off	Off	On	On
350MHz	100MHz	3.5x	On	Off	Off	On
400MHz	100MHz	4x	Off	On	On	On
450MHz	100MHz	4.5x	Off	On	Off	On

SERIAL PORT 2 SELECTION

Setting	JP2
RS-232	Open
RS-422	Pins 1 & 2, 3 & 4, 5 & 6, 7 & 8, 11 & 12, 15 & 16, 17 & 18, 19 & 20, 23 & 24 closed
RS-485	Pins 1 & 2, 3 & 4, 5 & 6, 7 & 8, 9 & 10, 11 & 12, 13 & 14, 15 & 16, 17 & 18, 19 & 20, 21 & 22, 23 & 24 closed

DISK ON CHIP ADDRESS SELECTION

Address	JP9
D8000	Pins 3 & 4 closed
D0000	Pins 1 & 2 closed