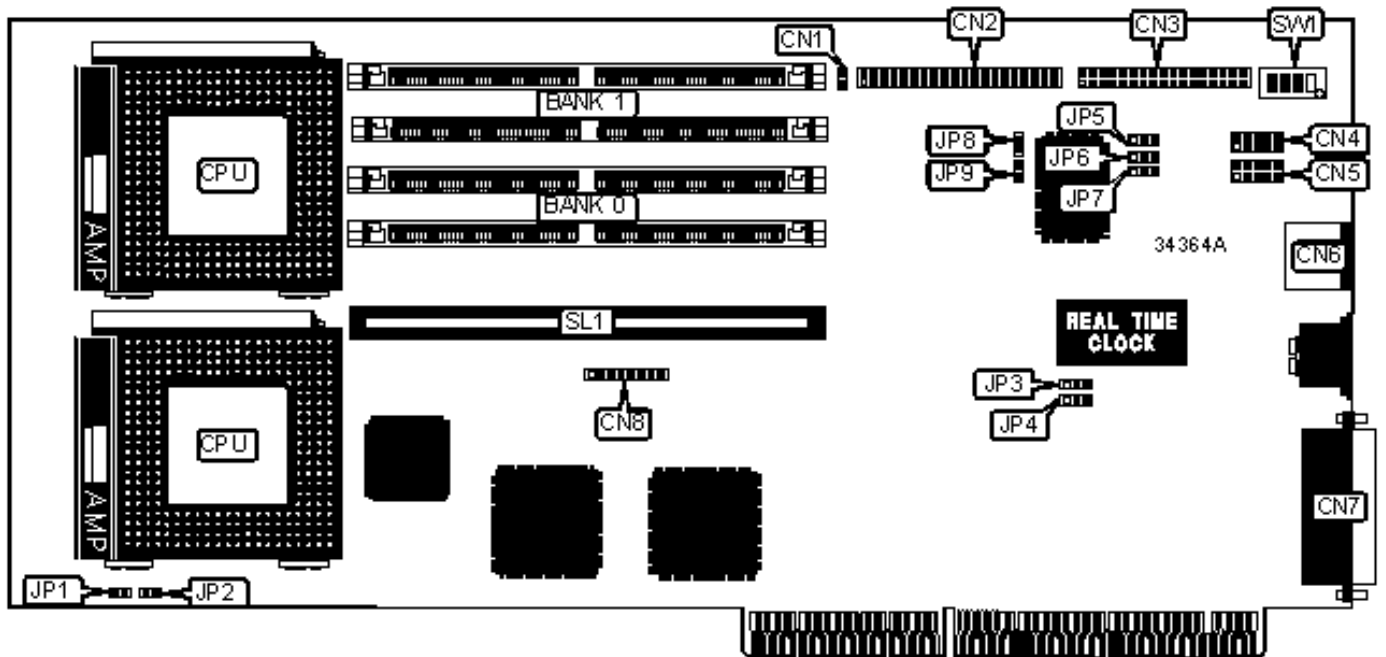


TEXAS MICRO SYSTEMS, INC.

PE5000HX

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
IDE interface LED	CN1	PS/2 mouse port	CN6
IDE interface	CN2	Parallel port	CN7
Floppy drive interface	CN3	Auxiliary keyboard connector	CN8
Serial port 1	CN4	Cache slot	SL1
Serial port 2	CN5		

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Watchdog timer disabled	JP3	Pins 2 & 3 closed
Watchdog timer enabled	JP3	Pins 1 & 2 closed
» PS/2 IRQ 12 enabled	JP4	Pins 2 & 3 closed
Next Step OS used	JP4	Pins 1 & 2 closed
» Monitor type select color	SW1/1	On
Monitor type select monochrome	SW1/1	Off
» Flash BIOS enabled	SW1/2	Off
Flash BIOS disabled	SW1/2	On
» CMOS memory normal operation	SW1/3	Off
CMOS memory clear	SW1/3	On
» I/O address select 270/271	SW1/4	Off
I/O address select 370/371	SW1/4	On

DRAM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None

16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

CACHE CONFIGURATION

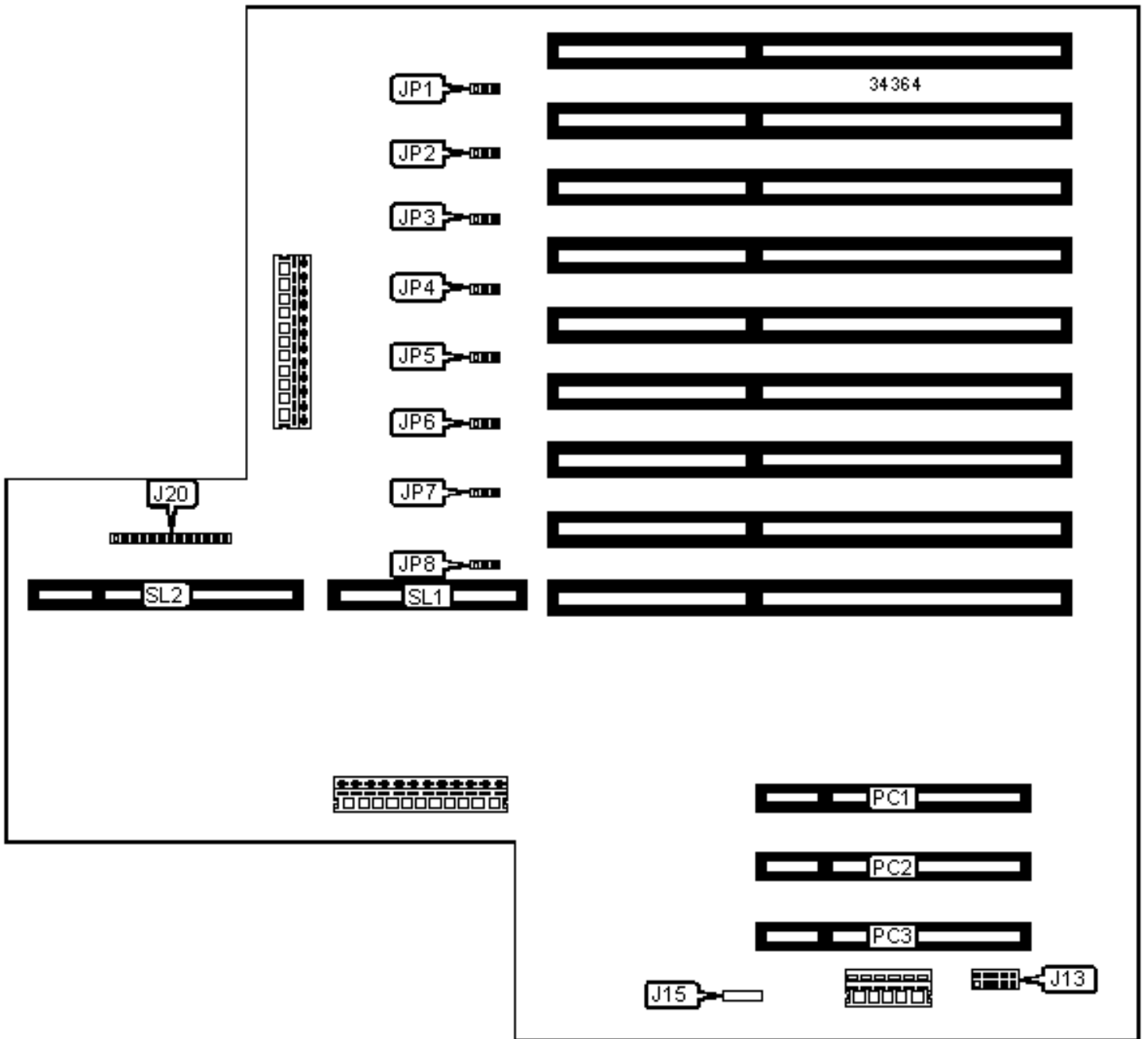
Size	SL1
256KB	256KB module installed
512KB	512KB module installed

CPU SPEED SELECTION

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CPU speed	Clock speed	Multiplier	JP1	JP2	JP8	JP9
100MHz	66MHz	1.5x	Open	Open	Open	Closed
133MHz	66MHz	2x	Closed	Open	Open	Closed
166MHz	66MHz	2.5x	Closed	Closed	Open	Closed
200MHz	66MHz	3x	Open	Closed	Open	Closed

SERIAL PORT 2 SELECTION				
Setting		JP5	JP6	JP7
»	RS-232	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
	RS-422	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
	RS-485	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed



CONNECTIONS

Purpose	Location	Purpose	Location
32-bit PCI slots	PC1 – PC3	PCI bus slot	SL2
Unidentified slot	SL1		

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	J13	Unidentified
» Factory configured - do not alter	J15	Unidentified

»	Factory configured - do not alter	J20	Unidentified
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EISA SLOT SELECTION				
Setting	JP1	JP2	JP3	JP4
ISA card installed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
EISA card installed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
Note: JP1 sets slot 1, JP2 sets slot 2, etc.				

EISA SLOT SELECTION (CON'T)				
Setting	JP5	JP6	JP7	JP8
ISA card installed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
EISA card installed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed