TEXAS MICRO, INC.

L5000HX SBC

Configuration

CN1   C1   CN2   CN3   SW1

BANK 0

SL1

CPU

AMP

35909

JP5

JP3   JP4

JP1   JP2

JP6

J1

JP8   2

C2

CN4

CN5

CN6
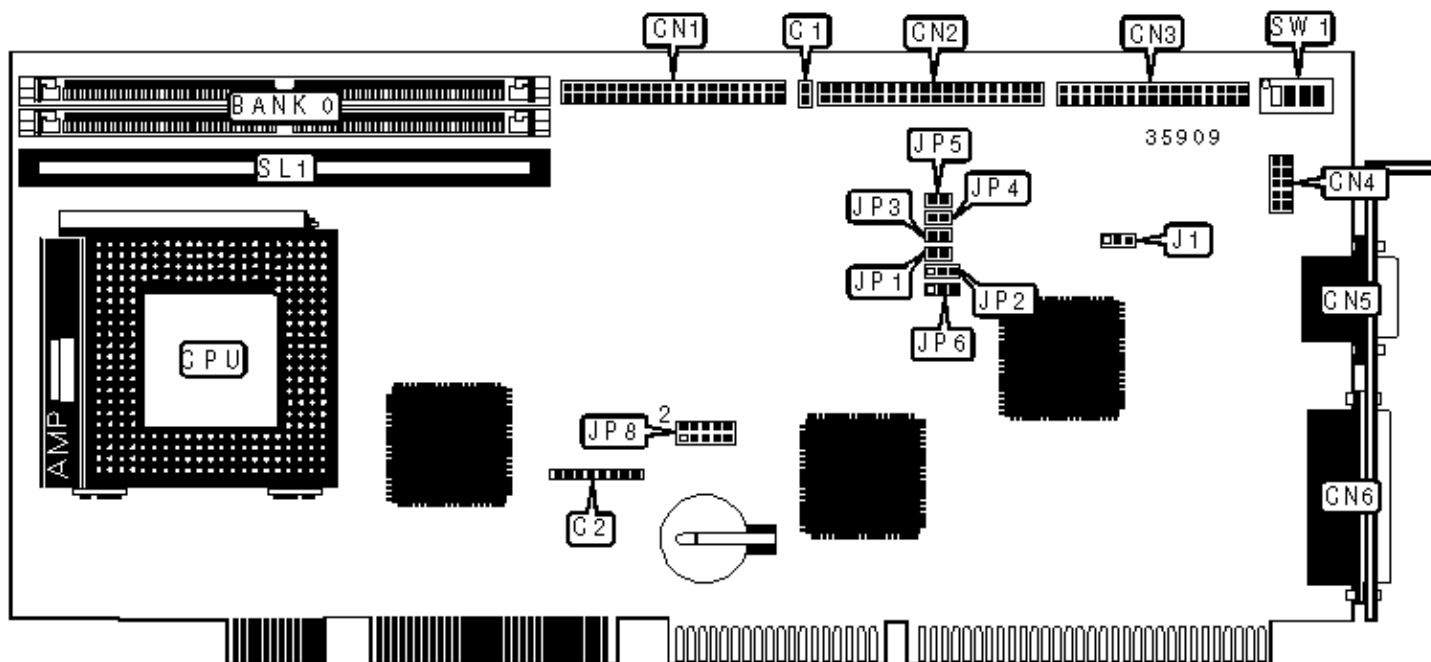
CONNECTIONS

Purpose	Location	Purpose	Location
IDE interface LED	C1	Serial port 1	CN4
Auxiliary keyboard connector	C2	Serial port 2	CN5
IDE interface 1	CN1	Parallel port	CN6
IDE interface 2	CN2	Cache slot	SL1
Floppy drive interface	CN3		

USER CONFIGURABLE SETTINGS

	Function	Label	Position
»	Factory configured - do not alter	J1	Unidentified
»	Factory configured - do not alter	JP2	Pins 1 & 2 closed
»	Factory configured - do not alter	JP6	Pins 1 & 2 closed
»	Monitor type select color	SW1/1	On
	Monitor type select monochrome	SW1/1	Off
»	Flash BIOS enabled; crisis recovery mode disabled	SW1/2	Off
	Flash BIOS disabled; crisis recovery mode enabled	SW1/2	On
»	CMOS memory normal operation	SW1/3	Off
	CMOS memory clear	SW1/3	On
»	Factory configured - do not alter	SW1/4	Off

SIMM CONFIGURATION

Size	Bank 2
8MB	(2) 1M x 36
16MB	(2) 2M x 36
32MB	(2) 4M x 36

64MB	(2) 8M x 36
128MB	(2) 16M x 36
Note: Board accepts EDO memory.	

CACHE CONFIGURATION

Size	SL1
256KB	256KB module installed
512KB	512KB module installed

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP8
100MHz	66MHz	1.5x	2 & 4, 7 & 9, 8 & 10
133MHz	66MHz	2x	2 & 4, 6 & 8, 7 & 9
166MHz	66MHz	2.5x	2 & 4, 5 & 7, 6 & 8
Note: Pins designated should be in the closed position.			

SERIAL PORT 2 SELECTION

	Setting	JP1	JP3	JP4	JP5
»	RS-232	Open	Closed	Open	Open
	RS-422	Closed	Open	Closed	Closed