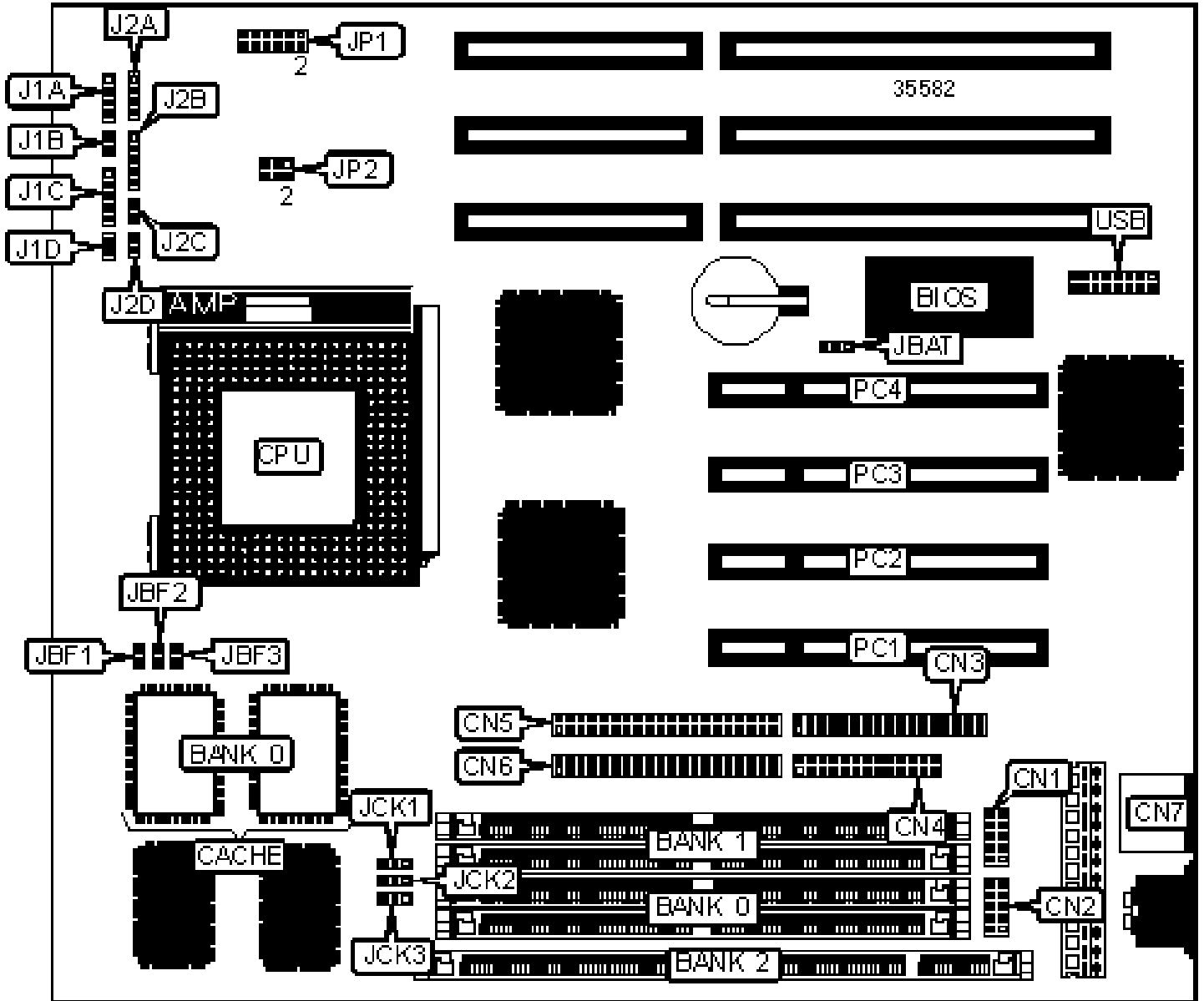


SOLTEK, INC.

SL-53D2, SL-53D5

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Serial port 2	CN1	IR connector	J2B
Serial port 1	CN2	Green PC connector	J2D
Parallel port	CN3	Speaker	J1A
Floppy drive interface	CN4	Reset switch	J1B
IDE interface 1	CN5	Power LED & keylock	J1C
IDE interface 2	CN6	Turbo LED	J1D
PS/2 mouse port	CN7	32-bit PCI slots	PC1 – PC4
IDE interface LED	J2A	USB connector	USB

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured – do not alter	J1C	Unidentified
» CMOS memory normal operation	JBAT	Pins 1 & 2 closed
CMOS memory clear	JBAT	Pins 2 & 3 closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36

48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
256MB	(2) 32M x 36	None
264MB	(2) 32M x 36	(2) 1M x 36

SIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1
272MB	(2) 32M x 36	(2) 2M x 36
288MB	(2) 32M x 36	(2) 4M x 36
320MB	(2) 32M x 36	(2) 8M x 36
384MB	(2) 32M x 36	(2) 16M x 36
512MB	(2) 32M x 36	(2) 32M x 36

Note: Board accepts EDO memory.

DIMM CONFIGURATION

Size	Bank 2
8MB	(1) 1M x 64
16MB	(1) 2M x 64
32MB	(1) 4M x 64
64MB	(1) 8M x 64
128MB	(1) 16M x 64
256MB	(1) 32M x 64
512MB	(1) 64M x 64

CACHE CONFIGURATION	
Size	Bank 0
256KB	(2) 32K x 32
512KB	(2) 64K x 32

CPU SPEED SELECTION (CX 6X86L)								
CPU speed	Clock speed	Multiplier	JBF1	JBF2	JBF3	JCK1	JCK2	JCK3
133MHz	55MHz	2x	2 & 3	2 & 3	1 & 2	Open	Open	Open
150MHz	60MHz	2x	1 & 2	2 & 3	2 & 3	Open	Open	Open
166MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	Open	Open	Open
200MHz	75MHz	2x	1 & 2	2 & 3	1 & 2	Open	Open	Open

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CX 6X86MX)								
CPU speed	Clock speed	Multiplier	JBF1	JBF2	JBF3	JCK1	JCK2	JCK3
150MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	Open	Open	Open
166MHz	60MHz	2x	1 & 2	2 & 3	2 & 3	Open	Open	Open

200MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	Open	Open	Open
233MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	Open	Open	Open
266MHz	75MHz	2x	1 & 2	2 & 3	1 & 2	Open	Open	Open

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K5)

CPU speed	Clock speed	Multiplier	JBF1	JBF2	JBF3	JCK1	JCK2	JCK3
90MHz	60MHz	1.5x	Open	Open	Open	1 & 2	2 & 3	2 & 3
100MHz	66MHz	1.5x	Open	Open	Open	2 & 3	1 & 2	2 & 3
120MHz	60MHz	2x	Closed	Open	Open	1 & 2	2 & 3	2 & 3
133MHz	66MHz	2x	Closed	Open	Open	2 & 3	1 & 2	2 & 3
150MHz	60MHz	2.5x	Closed	Closed	Open	1 & 2	2 & 3	2 & 3
166MHz	66MHz	2.5x	Closed	Closed	Open	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K6)

CPU speed	Clock speed	Multiplier	JBF1	JBF2	JBF3	JCK1	JCK2	JCK3
166MHz	66MHz	2.5x	Closed	Closed	Open	2 & 3	1 & 2	2 & 3
200MHz	66MHz	3x	Open	Closed	Open	2 & 3	1 & 2	2 & 3
133MHz	66MHz	3.5x	Open	Open	Open	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)

CPU speed	Clock speed	Multiplier	JBF1	JBF2	JBF3	JCK1	JCK2	JCK3
90MHz	60MHz	1.5x	Open	Open	Open	1 & 2	2 & 3	2 & 3
100MHz	66MHz	1.5x	Open	Open	Open	2 & 3	1 & 2	2 & 3

120MHz	60MHz	2x	Closed	Open	Open	1 & 2	2 & 3	2 & 3
133MHz	66MHz	2x	Closed	Open	Open	2 & 3	1 & 2	2 & 3
150MHz	60MHz	2.5x	Closed	Closed	Open	1 & 2	2 & 3	2 & 3
166MHz	66MHz	2.5x	Closed	Closed	Open	2 & 3	1 & 2	2 & 3
180MHz	60MHz	3x	Open	Closed	Open	1 & 2	2 & 3	2 & 3
200MHz	66MHz	3x	Open	Closed	Open	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL MMX)

CPU speed	Clock speed	Multiplier	JBF1	JBF2	JBF3	JCK1	JCK2	JCK3
166MHz	66MHz	2.5x	Closed	Closed	Open	2 & 3	1 & 2	2 & 3
200MHz	66MHz	3x	Open	Closed	Open	2 & 3	1 & 2	2 & 3
133MHz	66MHz	3.5x	Open	Open	Open	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION

Type	JP2
CX 6X86	Pins 3 & 5, 4 & 6 closed
CX 6X86L	Pins 1 & 3, 2 & 4 closed
CS 6X86MX	Pins 1 & 3, 2 & 4 closed
AM K5	Pins 3 & 5, 4 & 6 closed
AM K6	Pins 1 & 3, 2 & 4 closed
Pentium	Pins 3 & 5, 4 & 6 closed
MMX	Pins 1 & 3, 2 & 4 closed

CPU VOLTAGE SELECTION

Voltage	JP1
2.8v	Pins 9 & 10 closed
2.9v	Pins 7 & 8 closed
3.2v	Pins 5 & 6 closed
3.3v	Pins 3 & 4 closed
3.5v	Pins 1 & 2 closed