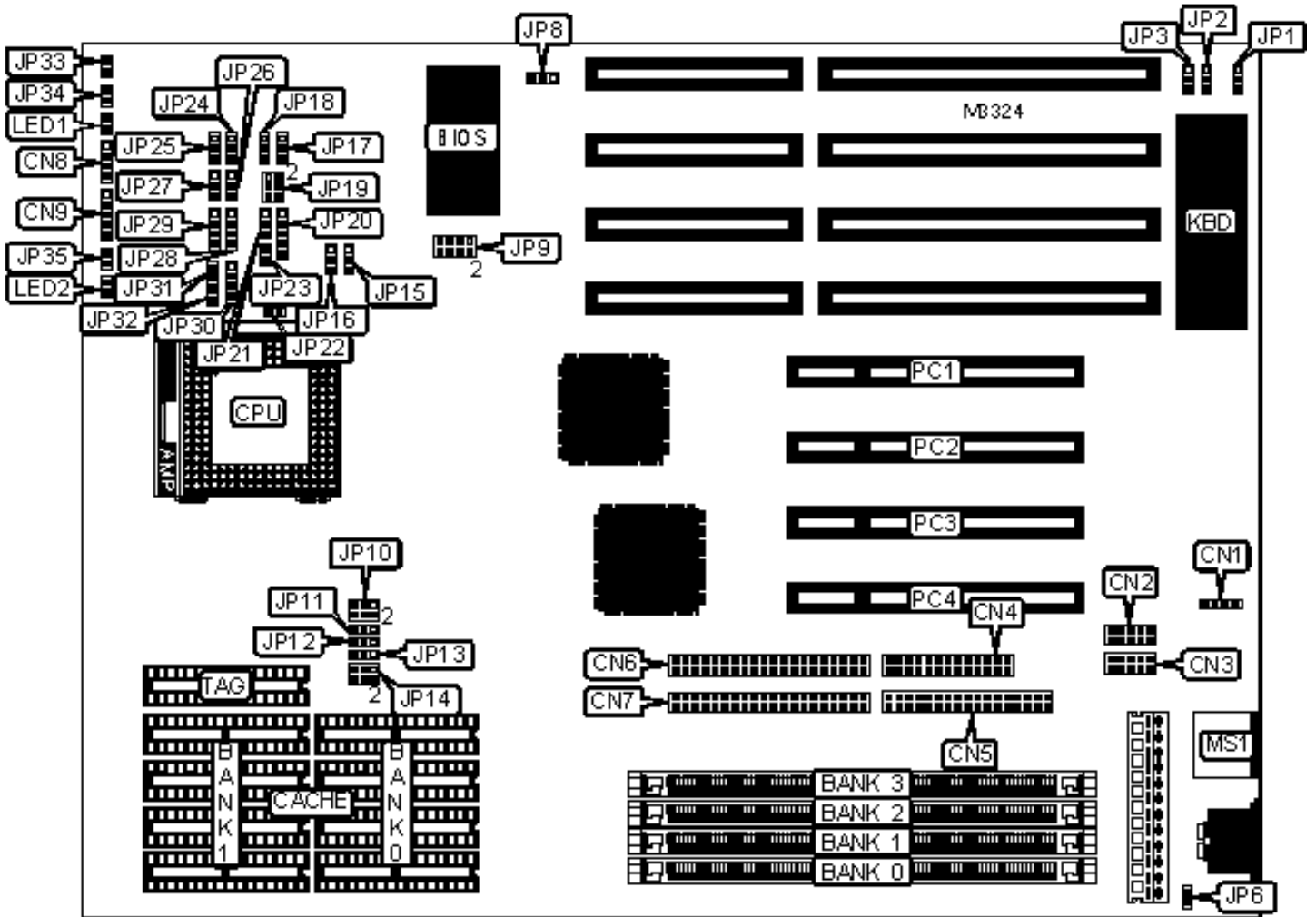


SHUTTLE COMPUTER INTERNATIONAL, INC.

HOT-433 (REV. 1)

Configuration



## CONNECTIONS

Function	Label	Function	Label
External battery	CN1	Green PC connector	JP6
Serial port 2	CN2	Reset switch	JP33
Serial port 1	CN3	Turbo switch	JP34
Parallel port	CN4	Green PC connector	JP35
Floppy drive interface	CN5	Green PC LED	LED1
IDE interface 2	CN6	IDE interface LED	LED2
IDE interface 1	CN7	PS/2 mouse port	MS1
Speaker	CN8	32-bit PCI slots	PC1 - PC4
Power LED & keylock	CN9		

## USER CONFIGURABLE SETTINGS

Setting	Label	Position
Flash BIOS voltage select 5v	JP8	Pins 2 & 3 closed
Flash BIOS voltage select 12v	JP8	Pins 1 & 2 closed

## DRAM

Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(1) 256K x 36	None	None	None
2MB	(1) 256K x 36	(1) 256K x 36	None	None
2MB	(1) 512K x 36	None	None	None
3MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	None
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
4MB	(1) 512K x 36	(1) 512K x 36	None	None
4MB	(1) 1M x 36	None	None	None

6MB	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36	None
6MB	(1) 256K x 36	(1) 256K x 36	(1) 512K x 36	(1) 512K x 36
8MB	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36
8MB	(1) 1M x 36	(1) 1M x 36	None	None
8MB	(1) 2M x 36	None	None	None
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	None
12MB	(1) 512K x 36	(1) 512K x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	(1) 2M x 36	None	None
16MB	(1) 4M x 36	None	None	None
18MB	(1) 256K x 36	(1) 256K x 36	(1) 2M x 36	(1) 2M x 36
20MB	(1) 512K x 36	(1) 512K x 36	(1) 2M x 36	(1) 2M x 36
24MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	None
24MB	(1) 1M x 36	(1) 1M x 36	(1) 2M x 36	(1) 2M x 36
32MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
32MB	(1) 4M x 36	(1) 4M x 36	None	None

**DRAM (CON'T)**

Size	Bank 0	Bank 1	Bank 2	Bank 3
32MB	(1) 8M x 36	None	None	None
34MB	(1) 256K x 36	(1) 256K x 36	(1) 4M x 36	(1) 4M x 36
36MB	(1) 512K x 36	(1) 512K x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	None
48MB	(1) 2M x 36	(1) 2M x 36	(1) 4M x 36	(1) 4M x 36

64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	(1) 8M x 36	None	None
64MB	(1) 16M x 36	None	None	None
66MB	(1) 256K x 36	(1) 256K x 36	(1) 8M x 36	(1) 8M x 36
68MB	(1) 512K x 36	(1) 512K x 36	(1) 8M x 36	(1) 8M x 36
72MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36
80MB	(1) 2M x 36	(1) 2M x 36	(1) 8M x 36	(1) 8M x 36
96MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	None
96MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 16M x 36	(1) 16M x 36	None	None
160MB	(1) 4M x 36	(1) 4M x 36	(1) 16M x 36	(1) 16M x 36
192MB	(1) 16M x 36	(1) 16M x 36	(1) 16M x 36	None
192MB	(1) 8M x 36	(1) 8M x 36	(1) 16M x 36	(1) 16M x 36
256MB	(1) 16M x 36	(1) 16M x 36	(1) 16M x 36	(1) 16M x 36

### CACHE SIZE

Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	None	(1) 8K x 8
256KB (A)	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
256KB (B)	(4) 64K x 8	None	(1) 32K x 8
512KB (A)	(4) 128K x 8	None	(1) 32K x 8
512KB (B)	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 64K x 8

### CACHE JUMPER

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Size	JP11	JP12	JP13	JP14
128KB	Open	Open	2 & 3	Open
256KB (A)	Open	Open	1 & 2	1 & 2
256KB (B)	Open	1 & 2	2 & 3	1 & 2
512KB (A)	1 & 2	1 & 2	2 & 3	1 & 2, 3 & 4
512KB (B)	Open	2 & 3	1 & 2	1 & 2, 3 & 4
1MB	2 & 3	2 & 3	1 & 2	1 & 2, 3 & 4, 5 & 6

Note: Pins designated should be in the closed position.

### CPU TYPE

Setting	JP10	JP17	JP19	JP20	JP21
CX486M6	1 & 3, 4 & 6	2 & 3	3 & 4	1 & 2, 3 & 4	1 & 2
80486SX	1 & 3, 2 & 4	Open	1 & 2	Open	Open
SL80486SX	1 & 3, 2 & 4	1 & 2	3 & 4	2 & 3, 4 & 5	1 & 2
UMC U5	2 & 4, 3 & 5	Open	1 & 2	Open	2 & 3
CX486DX	1 & 3, 4 & 6	2 & 3	3 & 4	1 & 2, 3 & 4	1 & 2
AM486DX	1 & 3, 2 & 4	Open	1 & 2	Open	Open
80486DX	1 & 3, 2 & 4	Open	1 & 2	Open	Open
SL80486DX	1 & 3, 2 & 4	1 & 2	3 & 4	2 & 3, 4 & 5	1 & 2
CX486DX2	1 & 3, 4 & 6	2 & 3	3 & 4	1 & 2, 3 & 4	1 & 2
AM486DX2	1 & 3, 2 & 4	Open	1 & 2	Open	Open
80486DX2	1 & 3, 2 & 4	Open	1 & 2	Open	Open
SL80486DX2	1 & 3, 2 & 4	1 & 2	3 & 4	2 & 3, 4 & 5	1 & 2
CX486DX4	1 & 3, 4 & 6	1 & 2	3 & 4	2 & 3, 4 & 5	1 & 2
AM486DX4	1 & 3, 2 & 4	Open	1 & 2	Open	Open
80486DX4	1 & 3, 2 & 4	1 & 2	3 & 4	2 & 3, 4 & 5	1 & 2

P24D	1 & 3, 2 & 4	1 & 2	3 & 4	2 & 3, 4 & 5	1 & 2
P24T	1 & 3, 2 & 4	1 & 2	3 & 4, 5 & 6	2 & 3, 4 & 5	1 & 2
Note: Pins designated should be in the closed position.					

<b>CPU TYPE (CON'T)</b>					
Setting	JP22	JP23	JP25	JP26	JP27
CX486M6	Open	Open	2 & 3	2 & 3	1 & 2
80486SX	Open	Open	2 & 3	Open	Open
SL80486SX	Open	Open	2 & 3	Open	Open
UMC U5	Open	Open	2 & 3	1 & 2	2 & 3
CX486DX	Open	Open	2 & 3	2 & 3	1 & 2
AM486DX	Open	Open	2 & 3	Open	Open
80486DX	Open	Open	2 & 3	Open	Open
SL80486DX	Open	Open	2 & 3	Open	Open
CX486DX2	Open	Open	2 & 3	2 & 3	1 & 2
AM486DX2	Open	Open	2 & 3	Open	Open
80486DX2	Open	Open	2 & 3	Open	Open
SL80486DX2	Open	Open	2 & 3	Open	Open
CX486DX4	Open	Open	2 & 3	2 & 3	1 & 2
AM486DX4	Open	Open	2 & 3	Open	Open
80486DX4	Open	Open	2 & 3	Open	Open
P24D	Closed	Closed	2 & 3	Open	Open
P24T	Open	Open	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.					

<b>CPU TYPE (CON'T)</b>
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Setting	JP28	JP29	JP30	JP31	JP32
CX486M6	2 & 3	1 & 2	Open	Open	Open
80486SX	2 & 3	Open	Open	Open	Open
SL80486SX	2 & 3	Open	Open	Open	Open
UMC U5	2 & 3	3 & 4	1 & 2, 3 & 4	Open	Open
CX486DX	1 & 2, 3 & 4	2 & 3	3 & 4	Open	Open
AM486DX	1 & 2, 3 & 4	Open	3 & 4	Open	Open
80486DX	1 & 2, 3 & 4	Open	3 & 4	Open	Open
SL80486DX	1 & 2, 3 & 4	Open	3 & 4	Open	Open
CX486DX2	1 & 2, 3 & 4	2 & 3	3 & 4	Open	Open
AM486DX2	1 & 2, 3 & 4	Open	3 & 4	Open	Open
80486DX2	1 & 2, 3 & 4	Open	3 & 4	Open	Open
SL80486DX2	1 & 2, 3 & 4	Open	3 & 4	Open	Open
CX486DX4	1 & 2, 3 & 4	Open	3 & 4	Open	Open
AM486DX4	1 & 2, 3 & 4	Open	3 & 4	Open	Open
80486DX4	1 & 2, 3 & 4	Open	3 & 4	Open	Open
P24D	1 & 2, 3 & 4	Open	3 & 4	Closed	Closed
P24T	1 & 2, 3 & 4	3 & 4	2 & 3	Open	Open

Note: Pins designated should be in the closed position.

<b>CPU TYPE (P24D ONLY)</b>	
Setting	JP24
Write back	Pins 2 & 3 closed
Write through	Pins 1 & 2 closed
Other CPU types	Open

### CPU SPEED

Setting	JP1	JP2	JP3
25MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
33MHz	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
40MHz	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
50iMHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
50MHz	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
66iMHz	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
80iMHz	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
100iMHz	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed

### CPU MULTIPLIER (DX4 ONLY)

Internal clock	External clock	JP18
75iMHz	25MHz	Open
100iMHz	33MHz	Open
100iMHz	50MHz	Pins 2 & 3 closed

### CPU MULTIPLIER (AM486DX2/DX4 ONLY)

Internal clock	External clock	JP24
75iMHz	25MHz	Pins 2 & 3 closed
80iMHz	40MHz	Pins 1 & 2 closed
100iMHz	33MHz	Pins 2 & 3 closed
100iMHz	50MHz	Pins 1 & 2 closed
Other CPU types		Open

### CPU VOLTAGE



Setting	JP9	JP15	JP16
3.3v	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
3.45v	Pins 3 & 4 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
3.6v	Pins 5 & 6 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
4v	Pins 7 & 8 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
5v	Any setting	Pins 1 & 2 closed	Pins 1 & 2 closed