PEACOCK AG

80386SX-25

Configuration
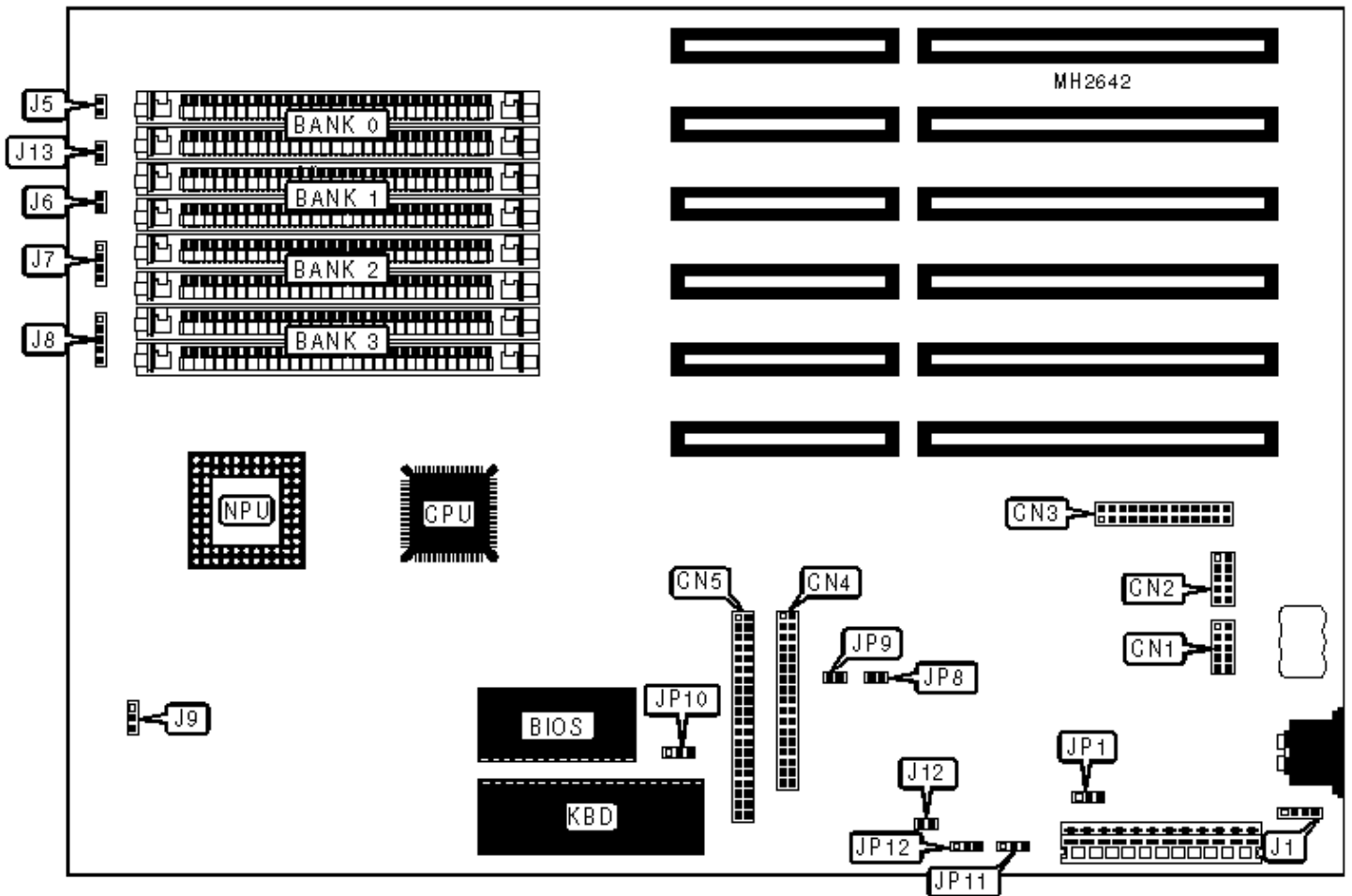
CONNECTIONS

Purpose	Location	Purpose	Location
Serial port 1	CN1	Turbo LED	J5
Serial port 2	CN2	Reset switch	J6
Parallel port	CN3	Speaker	J7
Floppy drive interface	CN4	Power LED & keylock	J8
IDE interface	CN5	IDE interface LED	J12
External battery	J1	Turbo switch	J13

USER CONFIGURABLE SETTINGS

	Function	Jumper	Position
»	Power good signal detect from board	JP1	pins 1 & 2 closed
	Power good signal detect from power supply	JP1	pins 2 & 3 closed
»	Monitor type select color	JP10	pins 2 & 3 closed
	Monitor type select monochrome	JP10	pins 1 & 2 closed
»	Battery type select internal	JP11	pins 1 & 2 closed
	Battery type select external	JP11	pins 2 & 3 closed
»	CMOS memory normal operation	JP12	pins 1 & 2 closed
	CMOS memory clear	JP12	pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	NONE	NONE	NONE
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
1.5MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9

2MB	(2) 1M x 9	NONE	NONE	NONE
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9
9MB	(2) 256K x 9	(2) 256K x 9	(2) 4M x 9	NONE
10MB	(2) 1M x 9	(2) 4M x 9	NONE	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE

DRAM JUMPER CONFIGURATION	
Size	J9
512KB	pins 1 & 2 closed
1MB	pins 1 & 2 closed
1.5MB	pins 1 & 2 closed
2MB	pins 1 & 2 closed
2MB	pins 1 & 2 closed
3MB	pins 1 & 2 closed
4MB	pins 1 & 2 closed
5MB	pins 1 & 2 closed
6MB	pins 1 & 2 closed
8MB	pins 1 & 2 closed
9MB	pins 2 & 3 closed
10MB	pins 2 & 3 closed
16MB	pins 2 & 3 closed

IDE CONFIGURATION		
Setting	JP8	JP9
On board IDE enabled	Closed	Closed
On board IDE disabled	Open	Open