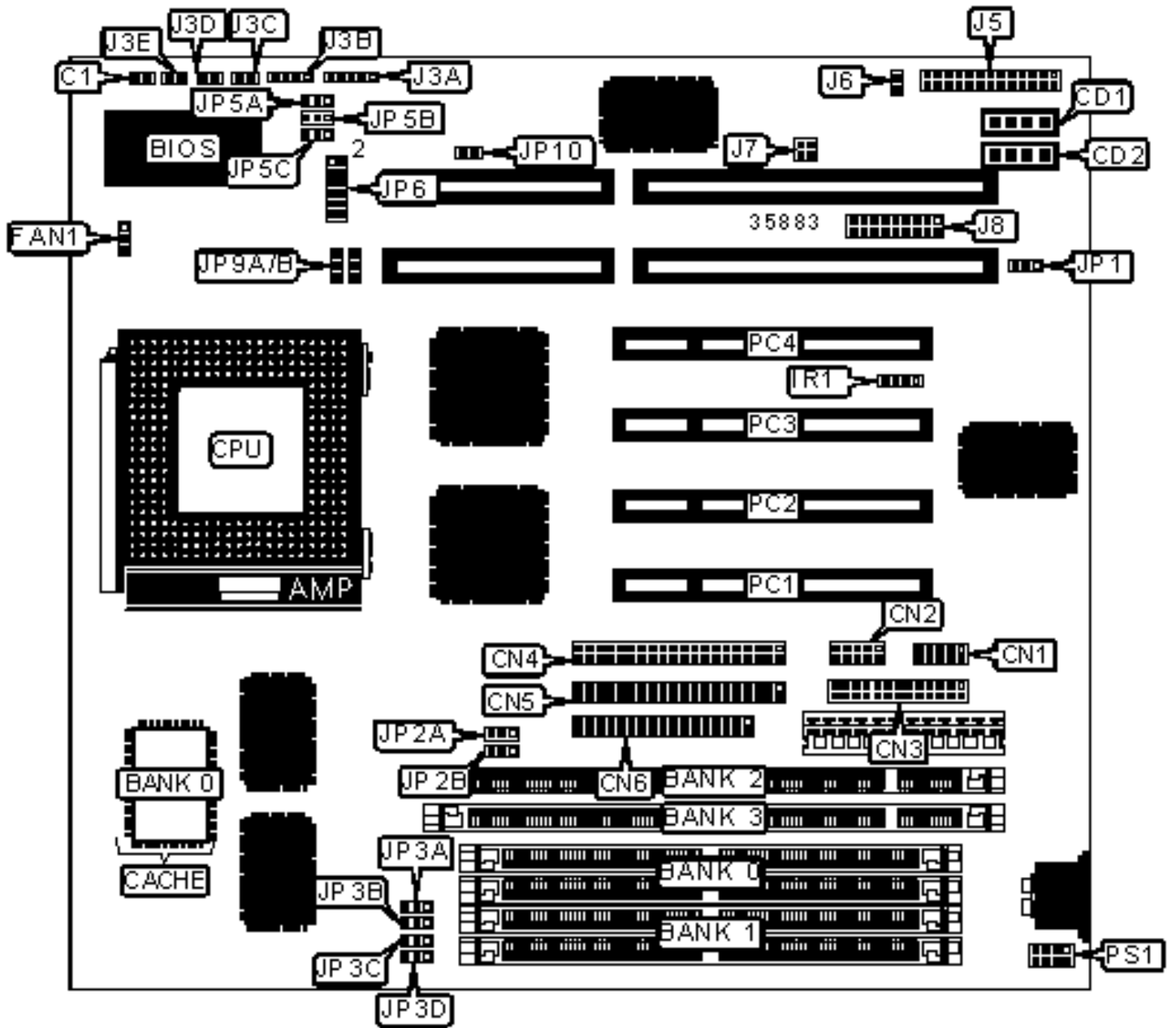


PC CHIPS MANUFACTURING, LTD.

M573

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Audio in – CD-ROM	CD1	Power LED & keylock	J3A
Audio in – CD-ROM	CD2	Speaker	J3B
Serial port 1	CN1	Reset switch	J3C
Serial port 2	CN2	IDE interface LED	J3D
Parallel port	CN3	Turbo LED	J3E
IDE interface 2	CN4	Sound/game connector	J5
IDE interface 1	CN5	Digital audio in/out connector	J7
Floppy drive interface	CN6	ATX form card connector	J8
CPU fan power	FAN1	32-bit PCI slots	PC1 – PC4
IR connector	IR1	PS/2 mouse interface	PS1

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	C1	Unidentified
Microphone type select normal	J6	Closed
Microphone type select special	J6	Open
» CMOS memory normal operation	JP1	Pins 1 & 2 closed
CMOS memory clear	JP1	Pins 2 & 3 closed
PCI CLK select CPU CLK/2	JP3D	Pins 1 & 2 closed
PCI CLK select = 33MHz	JP3D	Pins 2 & 3 closed
On board sound enabled	JP10	Open
On board sound disabled	JP10	Closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None

SIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

Note: Board accepts EDO memory.

DIMM CONFIGURATION

Size	Bank 2	Bank 3
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None
64MB	(1) 4M x 64	(1) 4M x 64
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 16M x 64	None
128MB	(1) 8M x 64	(1) 8M x 64
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64

Note: Board accepts SDRAM memory.

DIMM VOLTAGE CONFIGURATION

Voltage	JP2A	JP2B
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3.3v	Pins 2 & 3 closed	Pins 2 & 3 closed
5v	Pins 1 & 2 closed	Pins 1 & 2 closed

CACHE CONFIGURATION

Size	Bank 0
512KB	(1) 64K x 64

CPU SPEED SELECTION (CX 6X86)

CPU speed	Clock speed	Multiplier	JP3A	JP3B	JP3C	JP5A	JP5B	JP5C
120MHz	50MHz	2x	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2
133MHz	55MHz	2x	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2
150MHz	60MHz	2x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
166MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2
200MHz	75MHz	2x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CX 6X86L)

CPU speed	Clock speed	Multiplier	JP3A	JP3B	JP3C	JP5A	JP5B	JP5C
120MHz	50MHz	2x	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2
133MHz	55MHz	2x	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2
150MHz	60MHz	2x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
166MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2
200MHz	75MHz	2x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CX 6X86MX)

CPU speed	Clock speed	Multiplier	JP3A	JP3B	JP3C	JP5A	JP5B	JP5C
166MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2
200MHz	66MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2
233MHz	75MHz	2.5x	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (IDT C6)

CPU speed	Clock speed	Multiplier	JP3A	JP3B	JP3C	JP5A	JP5B	JP5C
150MHz	50MHz	3x	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2
180MHz	60MHz	3x	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
200MHz	66MHz	3x	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K5)

CPU speed	Clock speed	Multiplier	JP3A	JP3B	JP3C	JP5A	JP5B	JP5C
75MHz	50MHz	1.5x	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
90MHz	60MHz	1.5x	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2
100MHz	66MHz	1.5x	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2
150MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2
166MHz	66MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K6)

CPU speed	Clock speed	Multiplier	JP3A	JP3B	JP3C	JP5A	JP5B	JP5C
166MHz	66MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2
200MHz	66MHz	3x	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2

233MHz	66MHz	3.5x	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.								

CPU SPEED SELECTION (INTEL)								
CPU speed	Clock speed	Multiplier	JP3A	JP3B	JP3C	JP5A	JP5B	JP5C
75MHz	50MHz	1.5x	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
90MHz	60MHz	1.5x	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2
100MHz	66MHz	1.5x	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2
120MHz	60MHz	2x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2
150MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2
166MHz	66MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2
200MHz	66MHz	3x	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2
Note: Pins designated should be in the closed position.								

CPU SPEED SELECTION (INTEL MMX)								
CPU speed	Clock speed	Multiplier	JP3A	JP3B	JP3C	JP5A	JP5B	JP5C
166MHz	66MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2
200MHz	66MHz	3x	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2
233MHz	66MHz	3.5x	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.								

CPU TYPE SELECTION		
Type	JP9A	JP9B
Single voltage	Pins 2 & 3 closed	Pins 2 & 3 closed
Dual voltage	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU VOLTAGE SELECTION

Voltage	JP6
2.2v	Open
2.5v	Pins 11 & 12 closed
2.8v	Pins 9 & 10, 11 & 12 closed
2.9v	Pins 7 & 8, 11 & 12 closed
3.2v	Pins 5 & 6, 11 & 12 closed
3.3v	Pins 3 & 4, 11 & 12 closed
3.5v	Pins 1 & 2, 11 & 12 closed