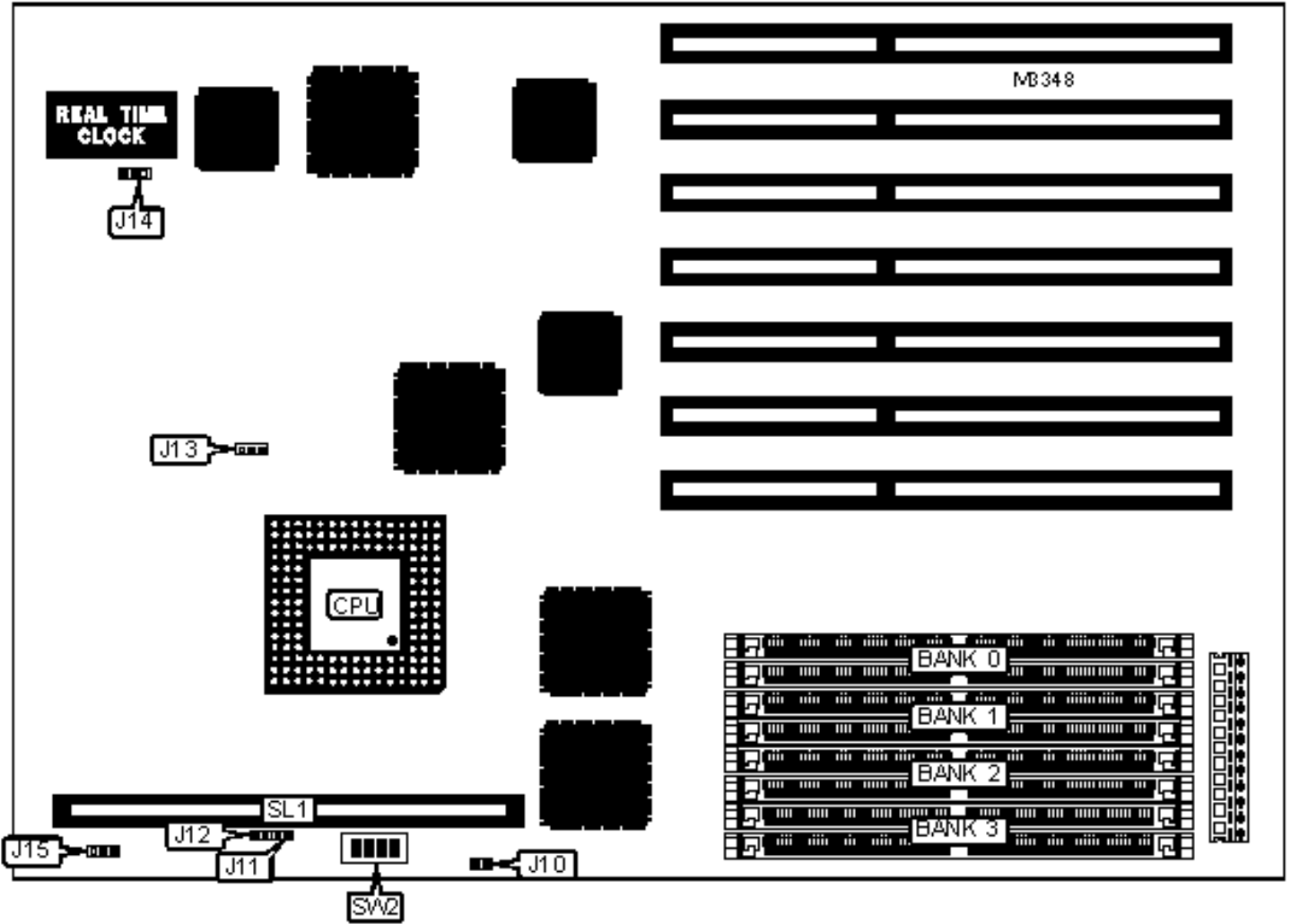


OLIVETTI

LSX5025E (BA386)

Configuration



CONNECTIONS

Purpose	Location
Cache slot	SL1

USER CONFIGURABLE SETTINGS

Function	Label	Position
»	Normal operation	J10 Closed
	Reboot system without using CMOS and EISA configuration	J10 Open
	Flash BIOS voltage select 5v	J14 Pins 1 & 2 closed
	Flash BIOS voltage select 12v	J14 Pins 2 & 3 closed
	Flash BIOS normal operation	J14 Open
»	15th megabyte memory disabled	J15 Pins 2 & 3 closed
	Normal operation	J15 Pins 1 & 2 closed
»	Factory configured - do not alter	SW2/1 Off
»	Factory configured - do not alter	SW2/2 Off

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(2) 512K x 36	None	None	None
8MB	(2) 512K x 36	(2) 512K x 36	None	None
8MB	(2) 1M x 36	None	None	None
12MB	(2) 512K x 36	(2) 512K x 36	(2) 512K x 36	None
12MB	(2) 1M x 36	None	(2) 512K x 36	None
16MB	(2) 512K x 36	(2) 512K x 36	(2) 512K x 36	(2) 512K x 36
16MB	(2) 1M x 36	None	(2) 512K x 36	(2) 512K x 36
16MB	(2) 1M x 36	(2) 1M x 36	None	None

16MB	(2) 2M x 36	None	None	None
20MB	(2) 1M x 36	(2) 1M x 36	(2) 512K x 36	None
20MB	(2) 2M x 36	None	(2) 512K x 36	None
24MB	(2) 1M x 36	(2) 1M x 36	(2) 512K x 36	(2) 512K x 36
24MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36	None
24MB	(2) 2M x 36	None	(2) 512K x 36	(2) 512K x 36
24MB	(2) 2M x 36	None	(2) 1M x 36	None
32MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36
32MB (UNIX)	(2) 2M x 36	None	(2) 1M x 36	(2) 1M x 36
32MB (DOS)	(2) 1M x 36	(2) 1M x 36	(2) 2M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36	None	None
32MB	(2) 4M x 36	None	None	None
36MB	(2) 4M x 36	None	(2) 512K x 36	None
36MB	(2) 2M x 36	(2) 2M x 36	(2) 512K x 36	None
40MB	(2) 4M x 36	None	(2) 512K x 36	(2) 512K x 36
40MB	(2) 4M x 36	None	(2) 1M x 36	None
40MB	(2) 2M x 36	(2) 2M x 36	(2) 512K x 36	(2) 512K x 36
40MB	(2) 2M x 36	(2) 2M x 36	(2) 1M x 36	None
48MB	(2) 4M x 36	None	(2) 1M x 36	(2) 1M x 36
48MB	(2) 4M x 36	None	(2) 2M x 36	None
48MB	(2) 2M x 36	(2) 2M x 36	(2) 1M x 36	(2) 1M x 36
48MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36	None

DRAM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2	Bank 3
64MB	(2) 4M x 36	None	(2) 2M x 36	(2) 2M x 36

64MB	(2) 8M x 36	None	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None	None
64MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36
68MB	(2) 8M x 36	None	(2) 512K x 36	None
72MB	(2) 8M x 36	None	(2) 512K x 36	(2) 512K x 36
72MB	(2) 8M x 36	None	(2) 1M x 36	None
80MB	(2) 8M x 36	None	(2) 1M x 36	(2) 1M x 36
80MB	(2) 8M x 36	None	(2) 2M x 36	None
96MB	(2) 8M x 36	None	(2) 2M x 36	(2) 2M x 36
96MB	(2) 8M x 36	None	(2) 4M x 36	None
96MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36	None
128MB	(2) 8M x 36	None	(2) 4M x 36	(2) 4M x 36
128MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36	None	None
132MB	(2) 8M x 36	(2) 8M x 36	(2) 512K x 36	None
136MB	(2) 8M x 36	(2) 8M x 36	(2) 512K x 36	(2) 512K x 36
136MB	(2) 8M x 36	(2) 8M x 36	(2) 1M x 36	None
144MB	(2) 8M x 36	(2) 8M x 36	(2) 1M x 36	(2) 1M x 36
144MB	(2) 8M x 36	(2) 8M x 36	(2) 2M x 36	None
160MB	(2) 8M x 36	(2) 8M x 36	(2) 2M x 36	(2) 2M x 36
160MB	(2) 8M x 36	(2) 8M x 36	(2) 4M x 36	None
192MB	(2) 8M x 36	(2) 8M x 36	(2) 4M x 36	(2) 4M x 36
256MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION

Size	SL1
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128KB	Installed
512KB	Installed
Note: Only one module can be installed.	

CPU SPEED SELECTION		
Speed	SW2/3	SW/4
66iMHz	On	Off

CPU TYPE SELECTION		
Type	J11	J12
80486DX2	Closed	Open

CPU VOLTAGE SELECTION		
Voltage		J13
»	5v	Pins 1 & 2 closed
	3.3v	Pins 2 & 3 closed

MISCELLANEOUS TECHNICAL NOTE
The location of pin 1 on the switch is unidentified.