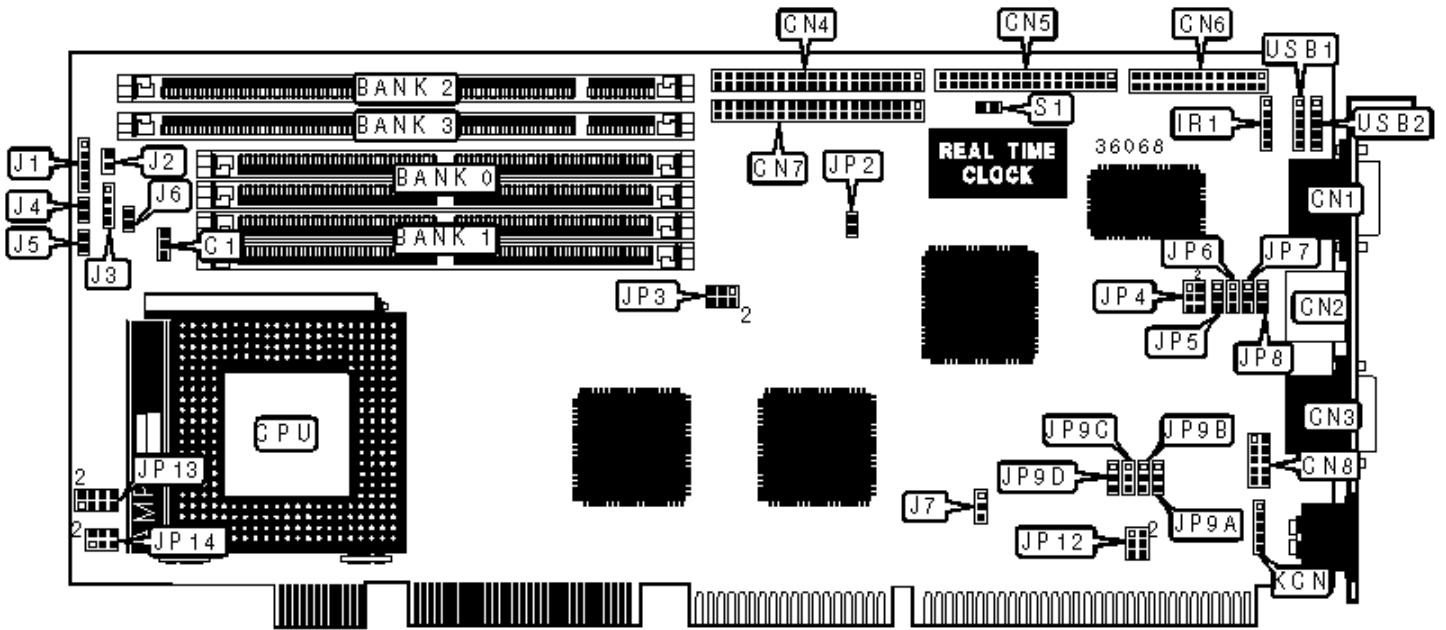


LANNER ELECTRONICS, INC.

AP-540TX

Configuration



CONNECTIONS

| Purpose | Location | Purpose | Location |
|------------------------|----------|------------------------------|----------|
| Chassis fan power | C1 | Power LED & keylock | J1 |
| VGA port | CN1 | Reset switch | J2 |
| PS/2 mouse port | CN2 | Speaker | J3 |
| Serial port 1 | CN3 | IDE interface LED | J4 |
| IDE interface 1 | CN4 | Turbo LED | J5 |
| Floppy drive interface | CN5 | Green PC connector | J6 |
| Parallel port | CN6 | Auxiliary keyboard connector | KCN |
| IDE interface 2 | CN7 | USB connector 1 | USB1 |
| Serial port 2 | CN8 | USB connector 2 | USB2 |
| IR connector | IR1 | | |

USER CONFIGURABLE SETTINGS

| Function | Label | Position |
|-------------------------------------|-------|--------------|
| » Factory configured - do not alter | JP2 | Unidentified |
| » CMOS memory normal operation | S1 | Open |
| CMOS memory clear | S1 | Closed |

SIMM CONFIGURATION

| Size | Bank 0 | Bank 1 |
|------|-------------|-------------|
| 8MB | (2) 1M x 36 | None |
| 16MB | (2) 2M x 36 | None |
| 16MB | (2) 1M x 36 | (2) 1M x 36 |
| 24MB | (2) 2M x 36 | (2) 1M x 36 |
| 32MB | (2) 4M x 36 | None |

| | | |
|-------|--------------|--------------|
| 32MB | (2) 2M x 36 | (2) 2M x 36 |
| 40MB | (2) 4M x 36 | (2) 1M x 36 |
| 48MB | (2) 4M x 36 | (2) 2M x 36 |
| 64MB | (2) 8M x 36 | None |
| 64MB | (2) 4M x 36 | (2) 4M x 36 |
| 72MB | (2) 8M x 36 | (2) 1M x 36 |
| 80MB | (2) 8M x 36 | (2) 2M x 36 |
| 96MB | (2) 8M x 36 | (2) 4M x 36 |
| 128MB | (2) 8M x 36 | (2) 8M x 36 |
| 128MB | (2) 16M x 36 | None |
| 136MB | (2) 16M x 36 | (2) 1M x 36 |
| 144MB | (2) 16M x 36 | (2) 2M x 36 |
| 160MB | (2) 16M x 36 | (2) 4M x 36 |
| 192MB | (2) 16M x 36 | (2) 8M x 36 |
| 256MB | (2) 16M x 36 | (2) 16M x 36 |

| DIMM CONFIGURATION | | |
|---------------------------|---------------|---------------|
| Size | Bank 2 | Bank 3 |
| 8MB | (1) 1M x 64 | None |
| 16MB | (1) 2M x 64 | None |
| 16MB | (1) 1M x 64 | (1) 1M x 64 |
| 24MB | (1) 2M x 64 | (1) 1M x 64 |
| 32MB | (1) 4M x 64 | None |
| 32MB | (1) 2M x 64 | (1) 2M x 64 |
| 40MB | (1) 4M x 64 | (1) 1M x 64 |
| 48MB | (1) 4M x 64 | (1) 2M x 64 |

| | | |
|-------|--------------|--------------|
| 64MB | (1) 8M x 64 | None |
| 64MB | (1) 4M x 64 | (1) 4M x 64 |
| 72MB | (1) 8M x 64 | (1) 1M x 64 |
| 80MB | (1) 8M x 64 | (1) 2M x 64 |
| 96MB | (1) 8M x 64 | (1) 4M x 64 |
| 128MB | (1) 16M x 64 | None |
| 128MB | (1) 8M x 64 | (1) 8M x 64 |
| 136MB | (1) 16M x 64 | (1) 1M x 64 |
| 144MB | (1) 16M x 64 | (1) 2M x 64 |
| 160MB | (1) 16M x 64 | (1) 4M x 64 |
| 192MB | (1) 16M x 64 | (1) 8M x 64 |
| 256MB | (1) 16M x 64 | (1) 16M x 64 |

CACHE CONFIGURATION

Note: The location of the cache is unidentified.

VIDEO MEMORY CONFIGURATION

Note: The location of the video memory is unidentified.

CPU SPEED SELECTION (CX 6X86)

| CPU speed | Clock speed | Multiplier | JP3 | JP14 |
|-----------|-------------|------------|---------------------|-------|
| 120MHz | 50MHz | 2x | 1 & 2, 3 & 4, 5 & 6 | 1 & 2 |
| 150MHz | 60MHz | 2x | 1 & 2 | 1 & 2 |
| 166MHz | 66MHz | 2x | Open | 1 & 2 |

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CX 6X86L)

| CPU speed | Clock speed | Multiplier | JP3 | JP14 |
|-----------|-------------|------------|---------------------|-------|
| 120MHz | 50MHz | 2x | 1 & 2, 3 & 4, 5 & 6 | 1 & 2 |
| 150MHz | 60MHz | 2x | 1 & 2 | 1 & 2 |
| 166MHz | 66MHz | 2x | Open | 1 & 2 |

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CX MII)

| CPU speed | Clock speed | Multiplier | JP3 | JP14 |
|-----------|-------------|------------|--------------|--------------|
| 166MHz | 66MHz | 2x | Open | 1 & 2 |
| 166MHz | 60MHz | 2.5x | 1 & 2 | 1 & 2, 3 & 4 |
| 200MHz | 75MHz | 2x | 3 & 4, 5 & 6 | 1 & 2 |
| 200MHz | 66MHz | 2.5x | Open | 1 & 2, 3 & 4 |
| 233MHz | 83MHz | 2x | 1 & 2, 5 & 6 | 1 & 2 |
| 233MHz | 75MHz | 2.5x | 3 & 4, 5 & 6 | 1 & 2, 3 & 4 |

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K5)

| CPU speed | Clock speed | Multiplier | JP3 | JP14 |
|-----------|-------------|------------|---------------------|--------------|
| 75MHz | 50MHz | 1.5x | 1 & 2, 3 & 4, 5 & 6 | Open |
| 90MHz | 60MHz | 1.5x | 1 & 2 | Open |
| 100MHz | 66MHz | 1.5x | Open | Open |
| 120MHz | 60MHz | 2x | 1 & 2 | 1 & 2 |
| 133MHz | 66MHz | 2x | Open | 1 & 2 |
| 150MHz | 60MHz | 2.5x | 1 & 2 | 1 & 2, 3 & 4 |
| 166MHz | 66MHz | 2.5x | Open | 1 & 2, 3 & 4 |

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K6)

| CPU speed | Clock speed | Multiplier | JP3 | JP14 |
|-----------|-------------|------------|------|--------------|
| 166MHz | 66MHz | 2.5x | Open | 1 & 2, 3 & 4 |
| 200MHz | 66MHz | 3x | Open | 3 & 4 |
| 233MHz | 66MHz | 3.5x | Open | Open |

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)

| CPU speed | Clock speed | Multiplier | JP3 | JP14 |
|-----------|-------------|------------|---------------------|--------------|
| 75MHz | 50MHz | 1.5x | 1 & 2, 3 & 4, 5 & 6 | Open |
| 90MHz | 60MHz | 1.5x | 1 & 2 | Open |
| 100MHz | 66MHz | 1.5x | Open | Open |
| 120MHz | 60MHz | 2x | 1 & 2 | 1 & 2 |
| 133MHz | 66MHz | 2x | Open | 1 & 2 |
| 150MHz | 60MHz | 2.5x | 1 & 2 | 1 & 2, 3 & 4 |
| 166MHz | 66MHz | 2.5x | Open | 1 & 2, 3 & 4 |
| 200MHz | 66MHz | 3x | Open | 3 & 4 |

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL MMX)

| CPU speed | Clock speed | Multiplier | JP3 | JP14 |
|-----------|-------------|------------|------|--------------|
| 166MHz | 66MHz | 2.5x | Open | 1 & 2, 3 & 4 |
| 200MHz | 66MHz | 3x | Open | 3 & 4 |
| 233MHz | 66MHz | 3.5x | Open | Open |

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION

| Voltage | JP13/pins 1 & 2 | JP13/pins 3 & 4 | JP13/pins 5 & 6 | JP13/pins 7 & 8 |
|---------|-----------------|-----------------|-----------------|-----------------|
| 2.0v | Closed | Closed | Closed | Closed |
| 2.1v | Open | Closed | Closed | Closed |
| 2.2v | Closed | Open | Closed | Closed |
| 2.3v | Open | Open | Closed | Closed |
| 2.4v | Closed | Closed | Open | Closed |
| 2.5v | Open | Closed | Open | Closed |
| 2.6v | Closed | Open | Open | Closed |
| 2.7v | Open | Open | Open | Closed |
| » 2.8v | Closed | Closed | Closed | Open |
| 2.9v | Open | Closed | Closed | Open |
| 3.0v | Closed | Open | Closed | Open |
| 3.1v | Open | Open | Closed | Open |
| 3.2v | Closed | Closed | Open | Open |
| 3.3v | Open | Closed | Open | Open |
| 3.4v | Closed | Open | Open | Open |
| 3.5v | Open | Open | Open | Open |

SERIAL PORT 2 SELECTION

| Setting | JP4 | JP5 | JP6 | JP7 | JP8 |
|----------|-------|-------|-------|-------|-------|
| » RS-232 | 5 & 6 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 |
| RS-422 | 3 & 4 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| RS-485 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |

Note: Pins designated should be in the closed position.

WATCHDOG TIME OUT SELECTION

| Seconds | JP9A | JP9B | JP9C | JP9D |
|---------|-------------------|-------------------|-------------------|-------------------|
| .5 | Pins 1 & 2 closed | Open | Open | Open |
| 1 | Open | Pins 1 & 2 closed | Open | Open |
| 2 | Open | Open | Pins 1 & 2 closed | Open |
| 4 | Open | Open | Open | Pins 1 & 2 closed |
| 8 | Open | Open | Open | Pins 2 & 3 closed |
| 16 | Open | Open | Pins 2 & 3 closed | Open |
| 32 | Open | Pins 2 & 3 closed | Open | Open |
| 64 | Pins 2 & 3 closed | Open | Open | Open |

WATCHDOG SELECTION

| Setting | J7 |
|--------------|-------------------|
| Reset system | Pins 2 & 3 closed |
| NMI system | Pins 1 & 2 closed |
| Disabled | Open |

DISK ON CHIP ADDRESS SELECTION

| Address | JP12 |
|----------|-------------------|
| Disabled | Pins 2 & 4 closed |
| C8000 | Pins 1 & 2 closed |
| D0000 | Pins 3 & 4 closed |
| D8000 | Pins 5 & 6 closed |