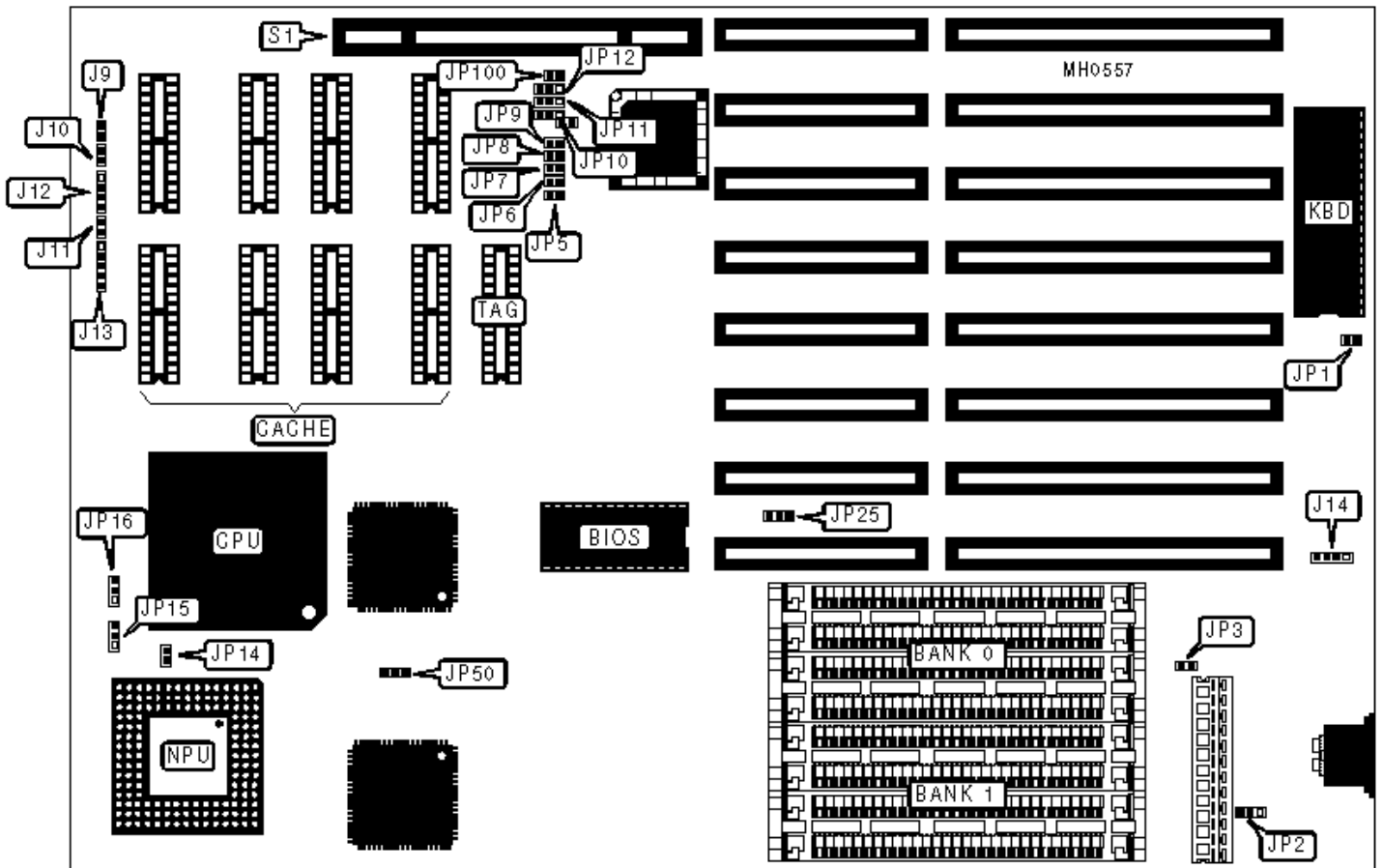


# JOINDATA SYSTEMS, INC.

## G486PLB

### Configuration



## CONNECTIONS

Purpose	Location	Purpose	Location
Reset switch	J9	Power LED & keylock	J13
Turbo LED	J10	External battery	J14
Turbo switch	J11	32-bit VESA local bus slot	S1
Speaker	J12		

## USER CONFIGURABLE SETTINGS

Function	Jumper	Position
» Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
» Power good signal detect from power supply	JP2	pins 1 & 2 closed
Power good signal detect from board	JP2	pins 2 & 3 closed
» CMOS memory normal operation	JP3	pins 2 & 3 closed
CMOS memory clear	JP3	pins 1 & 2 closed
» Factory configured - do not alter	JP10	pins 1 & 2 closed
» Factory configured - do not alter	JP11	pins 1 & 2 closed
» Factory configured - do not alter	JP12	pins 1 & 2 closed
» Factory configured - do not alter	JP25	Open
» Factory configured - do not alter	JP50	Open
» VESA local bus enabled	JP100	Open
VESA local bus disabled	JP100	Closed

## DRAM CONFIGURATION

Size	Bank 0	Bank 1
1MB	(4) 256K x 8	NONE

2MB	(4) 256K x 8	(4) 256K x 8
4MB	(4) 1M x 8	NONE
5MB	(4) 256K x 8	(4) 1M x 8
8MB	(4) 1M x 8	(4) 1M x 8
16MB	(4) 4M x 8	NONE
20MB	(4) 1M x 8	(4) 4M x 8
32MB	(4) 4M x 8	(4) 4M x 8

<b>CACHE CONFIGURATION</b>		
Size	Bank 0	TAG
64KB	(8) 8K x 8	(1) 8K x 8
256KB	(8) 32K x 8	(1) 32K x 8

<b>CACHE JUMPER CONFIGURATION</b>					
Size	JP5	JP6	JP7	JP8	JP9
64KB	Open	Open	Open	Open	Open
256KB	Closed	Closed	Open	Closed	Open

<b>CPU TYPE CONFIGURATION</b>			
Type	JP14	JP15	JP16
80486SX	Open	pins 2 & 3 closed	Open
80487SX	Closed	pins 1 & 2 closed	pins 1 & 2 closed
80486DX	Closed	pins 1 & 2 closed	pins 2 & 3 closed