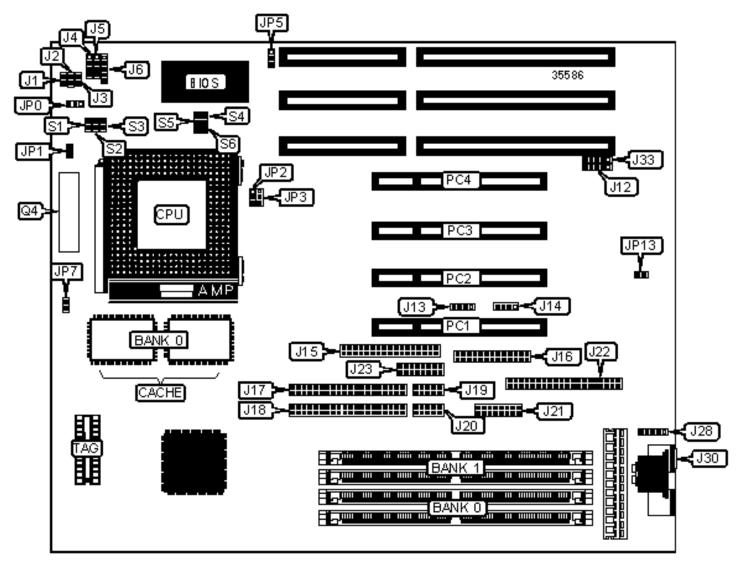
PAM-0050S (VER. 1.12)

Configuration



DTK

CONNECTIONS					
Purpose	Location	Purpose	Location		
Reset switch	J1	Serial port 1	J19		
Suspend LED	J2	Serial port 2	J20		
SMI switch	J3	VGA feature connector	J21		
Speaker	J4	Audio extension card connector	J22		
IDE interface LED	J5	Game port	J23		
Power LED & keylock	J6	PS/2 mouse interface	J28		
IR connector	J12	AT power connector	J29		
Audio in CD-ROM	J13	PS/2 mouse port	J30		
Audio in CD-ROM (Sony)	J14	PS/2 keyboard connector	J31		
Floppy drive interface	J15	AT keyboard connector	J32		
Parallel port	J16	Fast IR connector	J33		
IDE interface 1	J17	32-bit PCI slots	PC1 - PC4		
IDE interface 2	J18				

	USER CONFIGURABLE SETTINGS						
	Function	Function Label					
»	CMOS memory normal operation	JP0	Pins 1 & 2 closed				
	CMOS memory clear	JP0	Pins 2 & 3 closed				
»	5V Flash EPROM as system ROM	JP5	Pins 2 & 3 closed				
	12V Flash EPROM as system ROM	JP5	Pins 1 & 2 closed				
»	Linear burst mode (Cyrix 6x86 only) enabled	JP7	Closed				
	Linear burst mode (Cyrix 6x86 only) disabled	JP7	Open				
»	Onboard sound enabled	JP13	Closed				
	Onboard sound disabled	JP13	Open				

	SIMM CONFIGURATION					
Size	Bank 0	Bank 1				
8MB	(2) 1M x 36	None				
16MB	(2) 2M x 36	None				
16MB	(2) 1M x 36	(2) 1M x 36				
24MB	(2) 2M x 36	(2) 1M x 36				
32MB	(2) 4M x 36	None				
32MB	(2) 2M x 36	(2) 2M x 36				
40MB	(2) 4M x 36	(2) 1M x 36				
48MB	(2) 4M x 36	(2) 2M x 36				
64MB	(2) 8M x 36	None				
64MB	(2) 4M x 36	(2) 4M x 36				
72MB	(2) 8M x 36	(2) 1M x 36				
80MB	(2) 8M x 36	(2) 2M x 36				
96MB	(2) 8M x 36	(2) 4M x 36				
128MB	(2) 8M x 36	(2) 8M x 36				
128MB	(2) 16M x 36	None				
136MB	(2) 16M x 36	(2) 1M x 36				
144MB	(2) 16M x 36	(2) 2M x 36				
160MB	(2) 16M x 36	(2) 4M x 36				
192MB	(2) 16M x 36	(2) 8M x 36				
256MB	(2) 16M x 36	(2) 16M x 36				
256MB	(2) 32M x 36	None				
264MB	(2) 32M x 36	(2) 1M x 36				
272MB	(2) 32M x 36	(2) 2M x 36				

288MB	(2) 32M x 36	(2) 4M x 36			
320MB	(2) 32M x 36	(2) 8M x 36			
384MB	(2) 32M x 36	(2) 16M x 36			
512MB	(2) 32M x 36	(2) 32M x 36			
Note: Board accepts EDO memory.					

CACHE CONFIGURATION					
Size	Bank 0	Tag			
256KB	(2) 32KB x 16	8/16/32K x 8			

CPU SPEED SELECTION (INTEL)								
CPU speed	Clock speed	Multiplier	JP2	JP3	S4	S5	S6	
75MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open	
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Open	
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed	Open	
120MHz	60MHz	2x	1 & 2	2&3	Closed	Open	Open	
133MHz	66MHz	2x	1 & 2	2&3	Open	Closed	Open	
150MHz	60MHz	2.5x	2 & 3	2&3	Closed	Open	Open	
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	Closed	Open	
200MHz	66MHz	3x	2 & 3	1 & 2	Open	Closed	Open	
	Note: Pins designated are in the closed position							

CPU SPEED SELECTION (INTEL MMX)							
CPU speed	Clock speed	Multiplier	JP2	JP3	S4	S5	S6
166MHz	66MHz	2.5x	2 & 3	2&3	Open	Closed	Open
200MHz	66MHz	3x	2 & 3	1 & 2	Open	Closed	Open

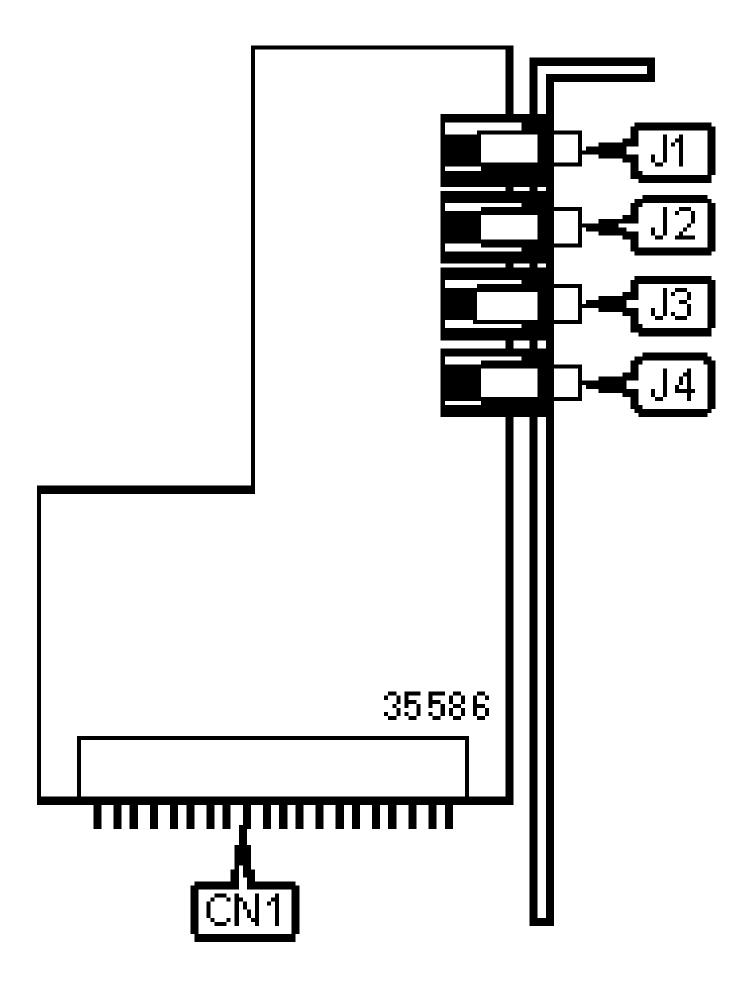
CPU SPEED SELECTION (CX 6X86)							
CPU speed	Clock speed	Multiplier	JP2	JP3	S4	S5	S6
120MHz	60MHz	2x	1 & 2	2 & 3	Closed	Closed	Open
133MHz	55MHz	2x	1 & 2	2 & 3	Open	Open	Open
150MHz	60MHz	2x	1 & 2	2 & 3	Closed	Open	Open
166MHz	66MHz	2 x	1 & 2	2 & 3	Open	Closed	Open
200MHz	75MHz	2x	1 & 2	2 & 3	Open	Open	Closed
	Note: Pins designated are in the closed position						

	CPU SPEED SELECTION (AM K5)							
CPU speed	Clock speed	Multiplier	JP2	JP3	S4	S5	S6	
75MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open	
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Open	
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed	Open	
120MHz	60MHz	2x	1 & 2	1 & 2	Closed	Open	Open	
133MHz	66MHz	2x	1&2	1 & 2	Open	Closed	Open	
150MHz	60MHz	2.5x	1&2	1 & 2	Open	Closed	Open	
166MHz	66MHz	2.5x	2&3	2 & 3	Open	Closed	Open	
			Note: Pins designated a	re in the closed position				

	CPU VOLTAGE SELECTION (SINGLE)						
Voltage JP1 S1 S2 S3							
3.4V	Closed	Closed	Closed	Closed			
3.5V	Open	Closed	Closed	Closed			

CPU VOLTAGE SELECTION (DUAL)						
V I/O	V Core	JP1	S1	S2	S3	
3.4V	2.8V	Closed	Open	Open	Open	

AUDIO EXTENSION CARD



CONNECTIONS						
Purpose Location Purpose Location						
Audio extension card connector	CN1	Line in	J3			
Speaker out	J1	Microphone	J4			
Line out	J2					