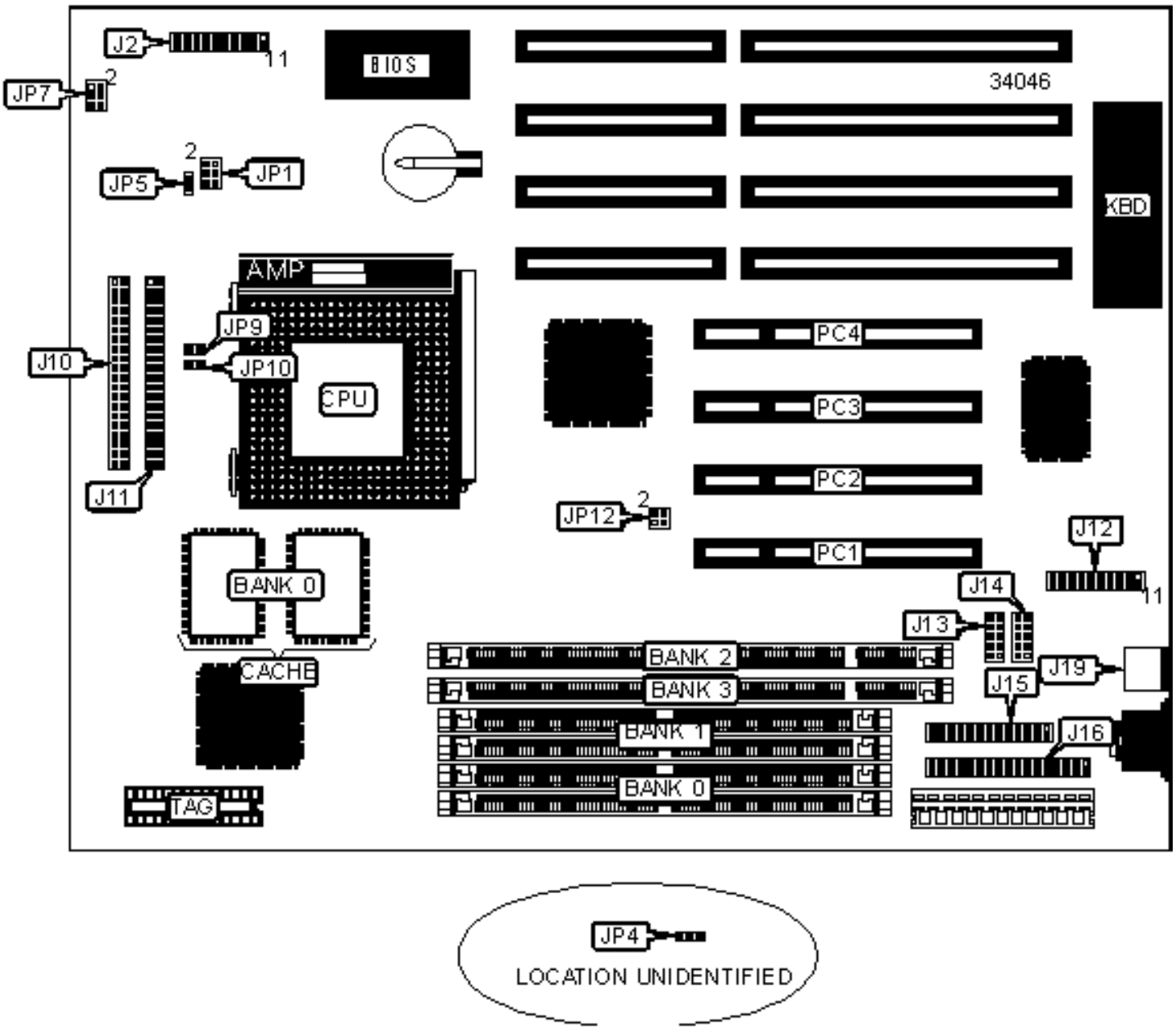
DTK COMPUTER, INC.

PAM-0057I (VER. 2.00)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
IDE interface LED	J2/pins 1 & 2	PS/2 mouse interface	J12/pins 6 – 10
Turbo LED	J2/pins 3 & 4	USB connector 2	J12/pins 12 - 15
Green PC connector	J2/pins 5 & 6	IR connector	J12/pins 16 - 20
Reset switch	J2/pins 9 & 10	Serial port 2	J13
Power LED & keylock	J2/pins 11 - 15	Serial port 1	J14
Speaker	J2/pins 17 - 20	Parallel port	J15
IDE interface 1	J10	Floppy drive interface	J16
IDE interface 2	J11	PS/2 mouse port	J19
USB connector 1	J12/pins 2 - 5	32-bit PCI slots	PC1 – PC4

USER CONFIGURABLE SETTINGS

Function	Label	Position
Flash BIOS voltage select 12v	JP4	Pins 2 & 3 closed
Flash BIOS voltage select 5v	JP4	Pins 1 & 2 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(2) 1M x 32	None	None	None
8MB	None	None	(1) 1M x 64	None
16MB	(2) 2M x 32	None	None	None
16MB	(2) 1M x 32	(2) 1M x 32	None	None
16MB	None	None	(1) 2M x 64	None
16MB	None	None	(1) 1M x 64	(1) 1M x 64
16MB	(2) 1M x 32	None	(1) 1M x 64	None

24MB	(2) 2M x 32	(2) 1M x 32	None	None
24MB	None	None	(1) 2M x 64	(1) 1M x 64
24MB	(2) 2M x 32	None	(1) 1M x 64	None
32MB	(2) 4M x 32	None	None	None
32MB	(2) 2M x 32	(2) 2M x 32	None	None
32MB	None	None	(1) 4M x 64	None
32MB	None	None	(1) 2M x 64	(1) 2M x 64
32MB	(2) 2M x 32	(2) 2M x 32	(1) 2M x 64	(1) 2M x 64
40MB	(2) 4M x 32	(2) 1M x 32	None	None
40MB	None	None	(1) 4M x 64	(1) 1M x 64
40MB	2) 4M x 32	None	(1) 1M x 64	None
48MB	(2) 4M x 32	(2) 2M x 32	None	None
48MB	None	None	(1) 4M x 64	(1) 2M x 64
48MB	(2) 4M x 32	None	(1) 2M x 64	None
64MB	(2) 8M x 32	None	None	None
64MB	(2) 4M x 32	(2) 4M x 32	None	None
64MB	None	None	(1) 8M x 64	None
64MB	None	None	(1) 4M x 64	(1) 4M x 64
64MB	(2) 4M x 32	(2) 4M x 32	(1) 4M x 64	(1) 4M x 64

DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
72MB	(2) 8M x 32	(2) 1M x 32		
72MB	None	None	(1) 8M x 64	(1) 1M x 64
72MB	(2) 8M x 32	None	(1) 1M x 64	None
80MB	(2) 8M x 32	(2) 2M x 32	None	None

80MB	None	None	(1) 8M x 64	(1) 2M x 64
80MB	(2) 8M x 32	None	(1) 2M x 64	None
96MB	(2) 8M x 32	(2) 4M x 32	None	None
96MB	None	None	(1) 8M x 64	(1) 4M x 64
96MB	(2) 8M x 32	None	(1) 4M x 64	None
128MB	(2) 8M x 32	(2) 8M x 32	None	None
128MB	(2) 16M x 32	None	None	None
128MB	None	None	(1) 8M x 64	(1) 8M x 64
128MB	(2) 16M x 32	None	(1) 8M x 64	None
136MB	(2) 16M x 32	(2) 1M x 32	None	None
136MB	(2) 16M x 32	None	(1) 1M x 64	None
144MB	(2) 16M x 32	(2) 2M x 32	None	None
144MB	(2) 16M x 32	None	(1) 2M x 64	None
160MB	(2) 16M x 32	(2) 4M x 32	None	None
160MB	(2) 16M x 32	None	(1) 4M x 64	None
192MB	(2) 16M x 32	(2) 8M x 32	None	None
192MB	(2) 16M x 32	None	(1) 8M x 64	None
256MB	(2) 16M x 32	(2) 16M x 32	None	None
Note: Board accepts EDO & SDRAM memory.				

CACHE CONFIGURATION

Size	Bank 0	TAG
256KB	(2) 32K x 32	(1) 8K/16K x 8
512KB	(2) 64K x 32	(1) 16K x 8

CPU SPEED SELECTION (CX 6X86)

CPU speed	Clock speed	Multiplier	JP9	JP10	JP12
133MHz	66MHz	2x	Closed	Open	1 & 2, 3 & 4
150MHz	60MHz	2x	Closed	Open	1 & 2
166MHz	66MHz	2x	Closed	Open	Open
200MHz	66MHz	3x	Closed	Open	3 & 4
Note: Pins designated should be in the closed position.					

CPU SPEED SELECTION (AM K5)					
CPU speed	Clock speed	Multiplier	JP9	JP10	JP12
90MHz	60MHz	1.5x	Open	Open	1 & 2
100MHz	66MHz	1.5x	Open	Open	Open
120MHz	60MHz	2x	Open	Open	1 & 2
133MHz	66MHz	2x	Closed	Open	Open
150MHz	60MHz	2.5x	Open	Open	Open
166MHz	66MHz	2.5x	Closed	Closed	Open
Note: Pins designated should be in the closed position.					

CPU SPEED SELECTION (AM K6)					
CPU speed	Clock speed	Multiplier	JP9	JP10	JP12
166MHz	66MHz	2.5x	Closed	Closed	Open
200MHz	66MHz	3x	Open	Closed	Open
233MHz	66MHz	3.5x	Open	Open	Open
Note: Pins designated should be in the closed position.					

CPU SPEED SELECTION (INTEL)					
CPU speed	Clock speed	Multiplier	JP9	JP10	JP12

90MHz	60MHz	1.5x	Open	Open	1 & 2
100MHz	66MHz	1.5x	Open	Open	Open
120MHz	60MHz	2x	Closed	Open	1 & 2
133MHz	66MHz	2x	Closed	Open	Open
150MHz	60MHz	2.5x	Closed	Closed	1 & 2
166MHz	66MHz	2.5x	Closed	Closed	Open
200MHz	66MHz	3x	Open	Closed	Open
233MHz	66MHz	3.5x	Open	Open	Open
Note: Pins designated should be in the closed position.					

CPU VOLTAGE SELECTION (SINGLE)

Voltage	JP1	JP5	JP7
3.3v	Closed	Closed	3 & 4, 5 & 6
3.5v	Closed	Open	1 & 2, 3 & 4, 5 & 6
Note: Pins designated should be in the closed position.			

CPU VOLTAGE SELECTION (DUAL)

Voltage	V core	JP1	JP5	JP7
3.3v	2.8v	Open	Closed	Open
3.3v	2.9v	Open	Closed	1 & 2
3.3v	3.2v	Open	Closed	1 & 2, 5 & 6
Note: Pins designated should be in the closed position.				