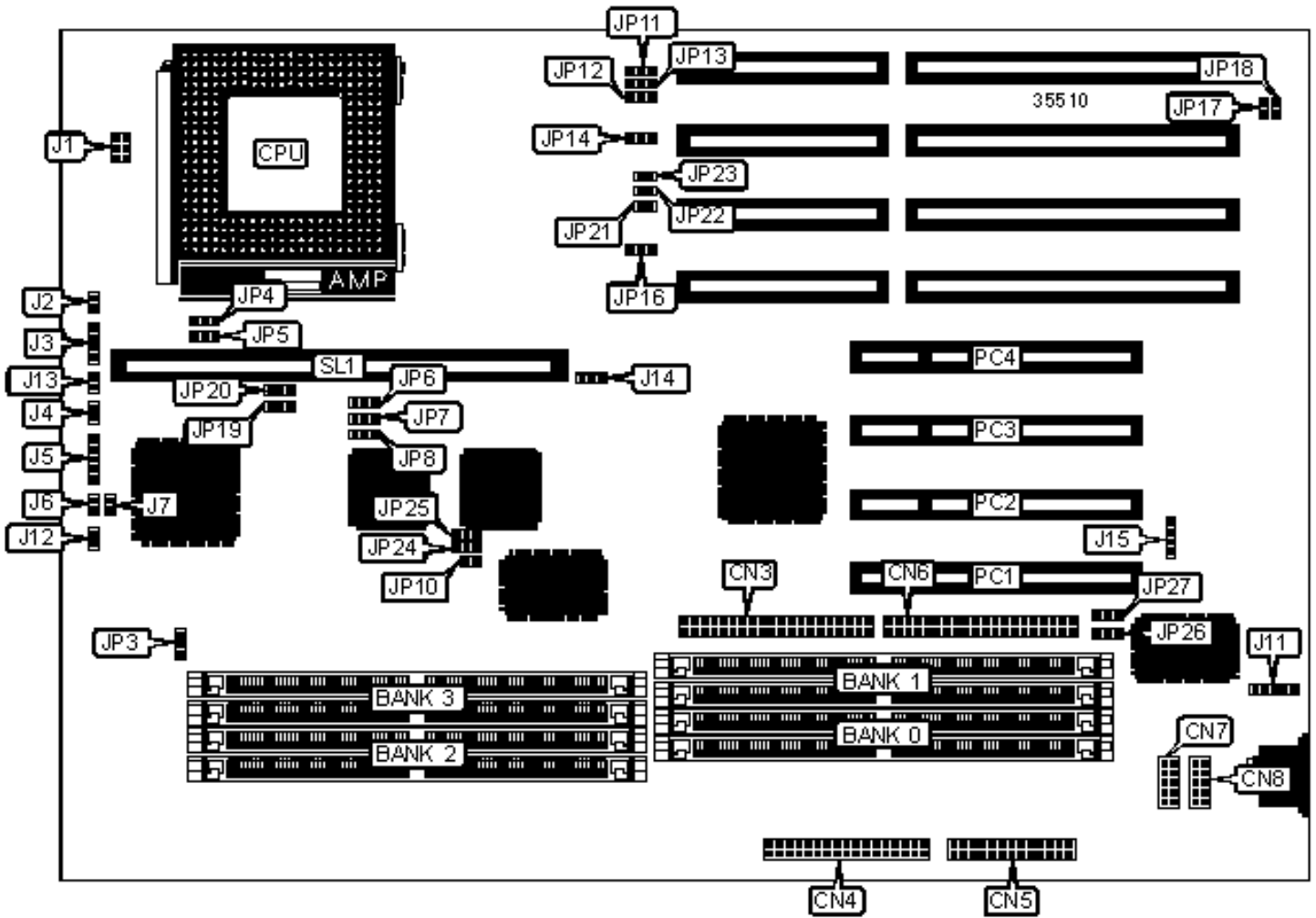


DIAMOND FLOWER, INC.

G586VPM/C (REV. 0+), G586VPM/T (REV. C+)

Configuration



## CONNECTIONS

Purpose	Location	Purpose	Location
IDE interface 1	CN3	Power LED & keylock	J5
Floppy drive interface	CN4	IDE interface LED	J6
Parallel port	CN5	Disk active connector	J7
IDE interface 2	CN6	PS/2 mouse interface	J11
Serial port 2	CN7	Green PC LED	J12
Serial port 1	CN8	Turbo LED	J13
Reset switch	J2	External battery	J15
Speaker	J3	32-bit PCI slots	PC1 - PC4
Turbo switch	J4	Cache slot	SL1

## USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	J14	Pins 1 & 2 closed
» Factory configured - do not alter	JP4	Pins 2 & 3 closed
» Factory configured - do not alter	JP5	Pins 2 & 3 closed
» Factory configured - do not alter	JP10	Open
» Factory configured - do not alter	JP13	Pins 1 & 2 closed
» Factory configured - do not alter	JP14	Pins 1 & 2 closed
» Factory configured - do not alter	JP16	Pins 1 & 2 closed
» Monitor type select color	JP17	Closed
» Monitor type select monochrome	JP17	Open
» PS/2 mouse IRQ12 enabled	JP18	Closed
» PS/2 mouse IRQ12 disabled	JP18	Open
» Factory configured - do not alter	JP21	Closed

»	Factory configured - do not alter	JP22	Closed
»	Factory configured - do not alter	JP23	Closed

### DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(2) 256K x 36	(2) 256K x 36	None	None
4MB	(2) 512K x 36	None	None	None
6MB	(2) 256K x 36	(2) 256K x 36	(2) 256K x 36	None
8MB	(2) 256K x 36	(2) 256K x 36	(2) 256K x 36	(2) 256K x 36
8MB	(2) 512K x 36	(2) 512K x 36	None	None
8MB	(2) 1M x 36	None	None	None
12MB	(2) 512K x 36	(2) 512K x 36	(2) 512K x 36	None
16MB	(2) 512K x 36	(2) 512K x 36	(2) 512K x 36	(2) 512K x 36
16MB	(2) 1M x 36	(2) 1M x 36	None	None
16MB	(2) 2M x 36	None	None	None
24MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36	None
32MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36	None	None
32MB	(2) 4M x 36	None	None	None
48MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36	None
64MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36
64MB	(2) 4M x 36	(2) 4M x 36	None	None
64MB	(2) 8M x 36	None	None	None
96MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36	None
128MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36	None	None

192MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36	None
256MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36
512MB	(2) 16M x 36	None	None	None

CACHE CONFIGURATION		
Size	SL1	TAG
256KB	256KB module installed	8K x 8
512KB	512KB module installed	8K x 8

CACHE JUMPER CONFIGURATION					
Size	JP6	JP7	JP8	JP19	JP20
256KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
512KB	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)					
CPU speed	Clock speed	Multiplier	J1	JP11	JP12
75MHz	50MHz	1.5x	1 & 2	2 & 3	2 & 3
90MHz	60MHz	1.5x	1 & 2, 5 & 6	2 & 3	2 & 3
100MHz	66MHz	1.5x	1 & 2, 3 & 4, 5 & 6	2 & 3	2 & 3
120MHz	60MHz	2x	1 & 2, 5 & 6	2 & 3	1 & 2
133MHz	66MHz	2x	1 & 2, 3 & 4, 5 & 6	2 & 3	1 & 2
150MHz	60 MHz	2.5x	1 & 2, 5 & 6	1 & 2	1 & 2
166MHz	66 MHz	2.5x	1 & 2, 3 & 4, 5 & 6	1 & 2	1 & 2
180MHz	60 MHz	3x	1 & 2, 5 & 6	1 & 2	2 & 3
200MHz	66 MHz	3x	1 & 2, 3 & 4, 5 & 6	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

### CPU SPEED SELECTION (CX 6X86)

CPU speed	Clock speed	Multiplier	J1	JP11	JP12
120MHz	50MHz	2x	1 & 2	2 & 3	1 & 2
133MHz	55MHz	2x	1 & 2, 3 & 4	2 & 3	1 & 2
150MHz	60MHz	2x	1 & 2, 5 & 6	2 & 3	1 & 2
166MHz	66MHz	2x	1 & 2, 3 & 4, 5 & 6	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

### CPU SPEED SELECTION (AM K5)

CPU speed	Clock speed	Multiplier	J1	JP11	JP12
75MHz	50MHz	1.5x	1 & 2	2 & 3	2 & 3
90MHz	60MHz	1.5x	1 & 2, 5 & 6	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

### CPU TYPE SELECTION

Setting	JP21	JP22	JP23
AMD, Intel	Closed	Closed	Closed
Cyrix	Open	Open	Open

### CPU VOLTAGE SELECTION

Voltage		JP3
	3.3v	Pins 2 & 3 closed
»	3.45v	Pins 1 & 2 closed

### DMA CHANNEL SELECTION

Channel		JP26	JP27
»	1	Pins 1 & 2 closed	Pins 1 & 2 closed
	3	Pins 2 & 3 closed	Pins 2 & 3 closed

### PCI SLOT 4 SELECTION

Setting		JP24	JP25
»	PIO OR DMA mode	Pins 1 & 2 closed	Pins 1 & 2 closed
	PIO mode	Pins 2 & 3 closed	Pins 2 & 3 closed