DIAMOND FLOWER, INC.

G56VPS PRO (REV. B+)

Configuration
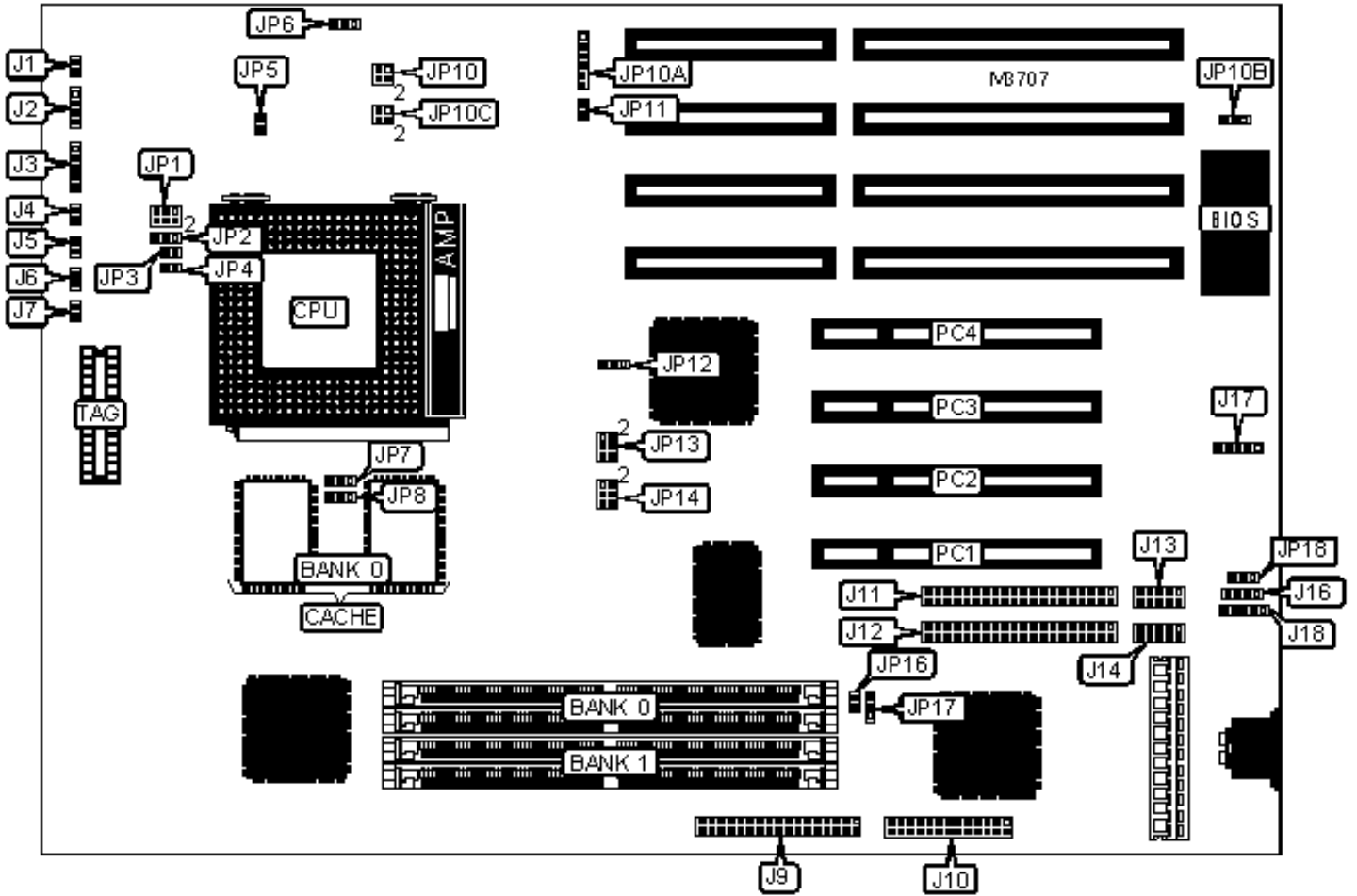
CONNECTIONS

Purpose	Location	Purpose	Location
Reset switch	J1	IDE interface 2	J11
Speaker	J2	IDE interface 1	J12
Power LED & keylock	J3	Serial port 2	J13
Green PC LED	J4	Serial port 1	J14
Green PC connector	J5	External battery	J16
IDE interface LED	J6	IR connector	J17
Floppy drive interface	J9	PS/2 mouse interface	J18
Parallel port	J10	32-bit PCI slots	PC1 - PC4

USER CONFIGURABLE SETTINGS

	Function	Label	Position
»	Factory configured - do not alter	J7	Open
»	Factory configured - do not alter	JP5	Closed
»	Factory configured - do not alter	JP6	Pins 1 & 2 closed
»	Factory configured - do not alter	JP10B	Pins 1 & 2 closed
»	Factory configured - do not alter	JP12	Pins 2 & 3 closed
»	Password normal operation	JP16	Open
	Password clear	JP16	Closed
»	Monitor type select color	JP17	Pins 1 & 2 closed
	Monitor type select monochrome	JP17	Pins 2 & 3 closed
»	Battery type select internal	JP18	Pins 1 & 2 closed
	Battery type select external	JP18	Pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36

DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory. Board also accepts x 32 SIMMs. Banks are interchangeable.		

CACHE CONFIGURATION

Size	Bank 0	TAG
256KB	(2) 32K x 32	(1) 32K x 8
512KB	(2) 64K x 32	(1) 32K x 8

CACHE JUMPER CONFIGURATION

Size	JP7
256KB	Pins 2 & 3 closed
512KB	Pins 1 & 2 closed

CPU SPEED SELECTION (CYRIX)

CPU speed	Clock speed	Multiplier	JP1	JP2	JP3
100MHz	50MHz	2x	2 & 4, 3 & 5	2 & 3	Open
110MHz	55MHz	2x	2 & 4, 3 & 5	2 & 3	Open
120MHz	60MHz	2x	2 & 4, 3 & 5	2 & 3	Open
133MHz	66MHz	2x	2 & 4, 3 & 5	2 & 3	Open
150MHz	75MHz	2x	2 & 4, 3 & 5	2 & 3	Open

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CYRIX, CON'T)

CPU speed	Clock speed	Multiplier	JP4	JP8	JP11	JP13
100MHz	50MHz	2x	Open	2 & 3	Open	3 & 4
110MHz	55MHz	2x	Open	2 & 3	Open	1 & 2, 3 & 4, 5 & 6
120MHz	60MHz	2x	Open	2 & 3	Open	1 & 2
133MHz	66MHz	2x	Open	2 & 3	Open	Open
150MHz	75MHz	2x	Open	2 & 3	Open	3 & 4, 5 & 6

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)

CPU speed	Clock speed	Multiplier	JP1	JP2	JP3
75MHz	50MHz	1.5x	1 & 3, 2 & 4	2 & 3	Closed
90MHz	60MHz	1.5x	1 & 3, 2 & 4	2 & 3	Closed
100MHz	66MHz	1.5x	1 & 3, 2 & 4	2 & 3	Closed
120MHz	60MHz	2x	3 & 5, 2 & 4	2 & 3	Closed
133MHz	66MHz	2x	3 & 5, 2 & 4	2 & 3	Closed
150MHz	60MHz	2.5x	3 & 5, 4 & 6	2 & 3	Closed
166MHz	66MHz	2.5x	3 & 5, 4 & 6	2 & 3	Closed
180MHz	60MHz	3x	1 & 3, 4 & 6	2 & 3	Closed
200MHz	66MHz	3x	1 & 3, 4 & 6	2 & 3	Closed

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL, CON'T)

CPU speed	Clock speed	Multiplier	JP4	JP8	JP11	JP13
75MHz	50MHz	1.5x	Closed	1 & 2	Closed	3 & 4
90MHz	60MHz	1.5x	Closed	1 & 2	Closed	1 & 2
100MHz	66MHz	1.5x	Closed	1 & 2	Closed	Open
120MHz	60MHz	2x	Closed	1 & 2	Closed	1 & 2
133MHz	66MHz	2x	Closed	1 & 2	Closed	Open
150MHz	60MHz	2.5x	Closed	1 & 2	Closed	1 & 2
166MHz	66MHz	2.5x	Closed	1 & 2	Closed	Open
180MHz	60MHz	3x	Closed	1 & 2	Closed	1 & 2
200MHz	66MHz	3x	Closed	1 & 2	Closed	Open

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION

Type	JP10	JP10C
CX 6X86/P54C	Open	Pins 1 & 2, 3 & 4 closed
CX 6X86L/P55C	Pins 1 & 2, 3 & 4 closed	Open

CPU VOLTAGE SELECTION (CYRIX)

Voltage	JP10A
2.5v	Open
2.7v	Pins 5 & 6 closed
2.9v	Pins 4 & 5 closed

CPU VOLTAGE SELECTION (INTEL)

Voltage	JP10A
VR (standard)	Pins 2 & 3 closed
VRE	Pins 1 & 2 closed
2.9v	Pins 4 & 5 closed

PCI SLOT 1 SELECTION

Setting	JP14
IDE master mode/PCI slot 1 slave mode	Pins 3 & 5, 4 & 6 closed
IDE PIO mode/ PCI slot 1 master mode	Pins 1 & 3, 2 & 4 closed