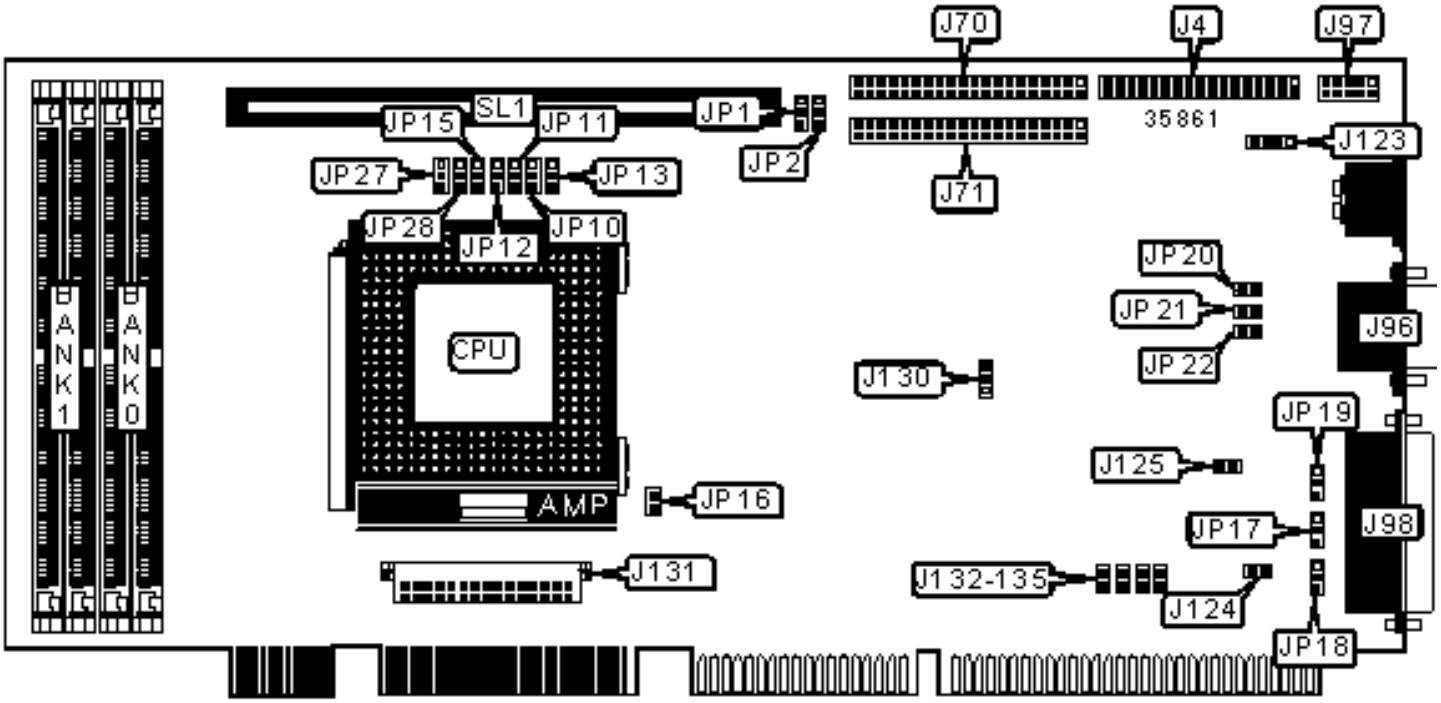
**CSS LABORATORIES, INC.**

**SB-5861**

**Configuration**



## CONNECTIONS

| Purpose | Location | Purpose | Location |
|---|---|---|---|
| Floppy drive interface | J4 | Parallel port | J98 |
| IDE interface 1 | J70 | External battery | J123 |
| IDE interface 2 | J71 | VRM connector | J131 |
| Serial port 1 | J96 | Cache slot | SL1 |
| Serial port 2 | J97 | | |

## USER CONFIGURABLE SETTINGS

| | Function | Label | Position |
|---|---|---|---|
| » | Watchdog reset output disabled | J124 | Open |
| | Watchdog reset output enabled | J124 | Closed |
| » | Watchdog IRQ15 disabled | J125 | Open |
| | Watchdog IRQ15 enabled | J125 | Closed |
| » | ISA bus speed select /4 | J130 | Pins 2 & 3 closed |
| | ISA bus speed select /3 | J130 | Pins 1 & 2 closed |
| » | Factory configured - do not alter | JP10 | Unidentified |
| » | Factory configured - do not alter | JP11 | Unidentified |
| » | Factory configured - do not alter | JP12 | Unidentified |
| » | Factory configured - do not alter | JP13 | Unidentified |
| » | Factory configured - do not alter | JP16 | Unidentified |
| » | Factory configured - do not alter | JP20 | Unidentified |
| » | Factory configured - do not alter | JP21 | Unidentified |
| » | Factory configured - do not alter | JP22 | Unidentified |

## SIMM CONFIGURATION

| Size | Bank 0 | Bank 1 |
|---|---|---|
| 8MB | (2) 1M x 36 | None |
| 16MB | (2) 2M x 36 | None |
| 16MB | (2) 1M x 36 | (2) 1M x 36 |
| 24MB | (2) 2M x 36 | (2) 1M x 36 |
| 32MB | (2) 4M x 36 | None |
| 32MB | (2) 2M x 36 | (2) 2M x 36 |
| 40MB | (2) 4M x 36 | (2) 1M x 36 |
| 48MB | (2) 4M x 36 | (2) 2M x 36 |
| 64MB | (2) 8M x 36 | None |

| **SIMM CONFIGURATION (CON'T)** | | |
|---|---|---|
| Size | Bank 0 | Bank 1 |
| 64MB | (2) 4M x 36 | (2) 4M x 36 |
| 72MB | (2) 8M x 36 | (2) 1M x 36 |
| 80MB | (2) 8M x 36 | (2) 2M x 36 |
| 96MB | (2) 8M x 36 | (2) 4M x 36 |
| 128MB | (2) 8M x 36 | (2) 8M x 36 |

| **CACHE CONFIGURATION** | |
|---|---|
| Size | SL1 |
| 256KB | 256KB module installed |
| 512KB | 512KB module installed |

| **CPU SPEED SELECTION (INTEL)** | | | | | | | |
|---|---|---|---|---|---|---|---|
| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP15 | JP27 | JP28 |

| | | | | | | | |
|---|---|---|---|---|---|---|---|
| 75MHz | 50MHz | 1.5x | 2 & 3 | 2 & 3 | Open | Open | Open |
| 90MHz | 60MHz | 1.5x | 1 & 2 | 2 & 3 | Open | Open | Open |
| 100MHz | 66MHz | 1.5x | 2 & 3 | 1 & 2 | Open | Open | Open |
| 120MHz | 60MHz | 2x | 1 & 2 | 2 & 3 | 2 & 3 | Open | Open |
| 133MHz | 66MHz | 2x | 2 & 3 | 1 & 2 | 2 & 3 | Open | Open |
| 150MHz | 60MHz | 2.5x | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | Open |
| 166MHz | 66MHz | 2.5x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | Open |
| 200MHz | 66MHz | 3x | 2 & 3 | 1 & 2 | 1 & 2 | 2 & 3 | Open |
| Note: Pins designated should be in the closed position. | | | | | | | |

## CPU SPEED SELECTION (INTEL MMX)

| CPU speed | Clock speed | Multiplier | JP1 | JP2 | JP15 | JP27 | JP28 |
|---|---|---|---|---|---|---|---|
| 166MHz | 66MHz | 2.5x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | Open |
| 200MHz | 66MHz | 3x | 2 & 3 | 1 & 2 | 1 & 2 | 2 & 3 | Open |
| 233MHz | 66MHz | 3.5x | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 | Open |
| Note: Pins designated should be in the closed position. | | | | | | | |

## WATCH DOG TIMER SELECTION

| Voltage | | J132 | J133 | J134 | J135 |
|---|---|---|---|---|---|
| | Enabled | Open | Closed | Open | Closed |
| » | Disabled | Closed | Open | Closed | Open |

## BIOS SELECTION

| Type | JP17 | JP18 | JP19 |
|---|---|---|---|
| EPROM | Pins 1 & 2 closed | Pins 1 & 2 closed | Pins 2 & 3 closed |
| Flash | Pins 1 & 2 closed | Pins 1 & 2 closed | Pins 1 & 2 closed |