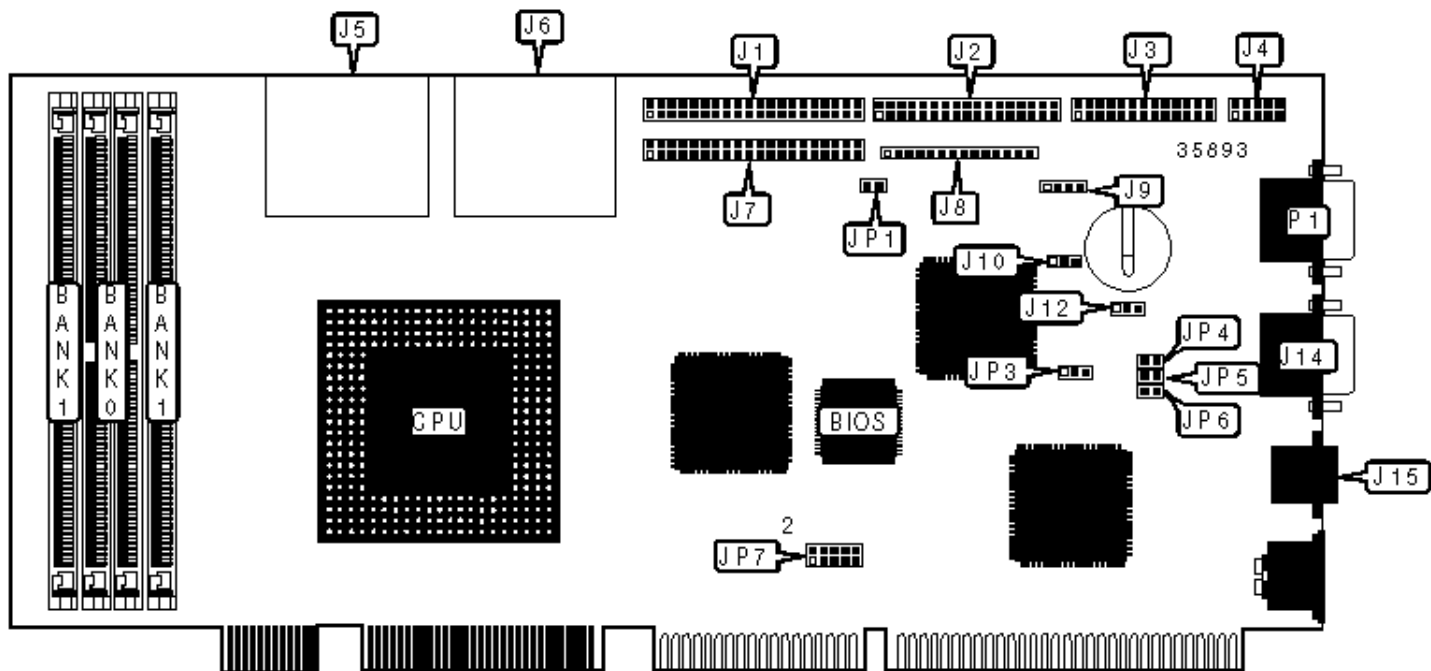


CSS LABORATORIES, INC.

MODEL GXI

Configuration



## CONNECTIONS

Purpose	Location	Purpose	Location
IDE interface 1	J1	Chassis fan power	J12
Floppy drive interface	J2	Serial port 1	J14
Parallel port	J3	Ethernet 10/100BaseT connector	J15
Serial port 2	J4	IDE interface LED	JP1
Primary compact flash connector	J5	Activity LED	JP4
Secondary compact flash connector	J6	Link LED	JP5
IDE interface 2	J7	Speed LED	JP6
Keyboard, speaker, reset	J8	VGA port	P1
PS/2 mouse interface	J9		

## USER CONFIGURABLE SETTINGS

Function	Label	Position
» CMOS memory normal operation	J10	Pins 2 & 3 closed
CMOS memory clear	J10	Pins 1 & 2 closed

## SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36

48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory.

### VIDEO MEMORY CONFIGURATION

Note: The location of the video memory is unidentified.

### WATCHDOG TIMER SELECTION

Setting	JP3
System reset	Pins 1 & 2 closed
I/O channel check	Pins 2 & 3 closed
Timer disabled	Open

### WATCHDOG TIMEOUT SELECTION

Timeout	JP7
.5 seconds	Pins 9 & 10 closed
1 seconds	Pins 7 & 8 closed
2 seconds	Pins 5 & 6 closed
4 seconds	Pins 3 & 4 closed
8 seconds	Pins 1 & 2 closed