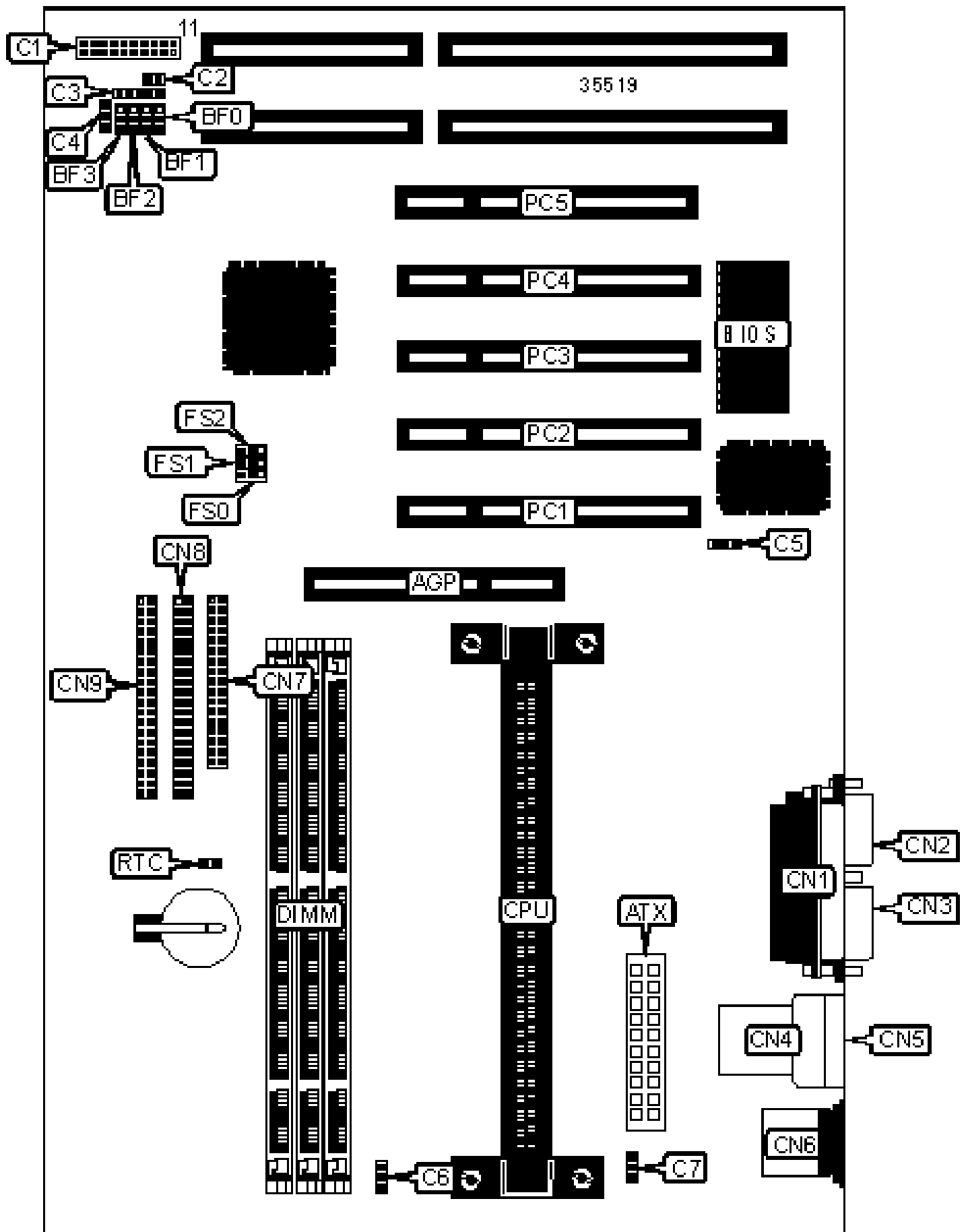


ASUS COMPUTER INTERNATIONAL

P2L97 (REV. 1.05)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	CPU fan power	C6
ATX power connector	ATX	Power fan	C7
Power LED & keylock	C1/pins 1 - 5	Parallel port	CN1
Speaker	C1/pins 7 - 10	Serial port 2	CN2
Message LED	C1/pins 12 & 13	Serial port 1	CN3
Green PC connector	C1/pins 14 & 15	USB connector 1	CN4
Soft off power supply	C1/pins 16 & 17	USB connector 2	CN5
Reset switch	C1/pins 19 & 20	PS/2 mouse port	CN6
IDE interface LED	C2	Floppy drive interface	CN7
IR connector	C3	IDE interface 2	CN8
CPU fan power	C4	IDE interface 1	CN9
Wake on LAN connector	C5	32-bit PCI slots	PC1 - PC5

USER CONFIGURABLE SETTINGS

Function	Label	Position
» CMOS memory normal operation	RTC	Open
CMOS memory clear	RTC	Closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
8MB	(1) 1M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
16MB	(1) 2M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64

32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64	None	None
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
64MB	(1) 8M x 64	None	None
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
128MB	(1) 16M x 64	None	None
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64

DIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64

Note: Board accepts EDO memory.

CPU SPEED SELECTION (PENTIUM)

Speed	Clock speed	Multiplier	FS0	FS1	FS2	BF0	BF1	BF2	BF3
233MHz	66MHz	3.5x	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
266MHz	66MHz	4.0x	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2	2 & 3
300MHz	66MHz	4.5x	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3
333MHz	66MHz	5.0x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3

Note: Pins designated are in the closed position.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU.