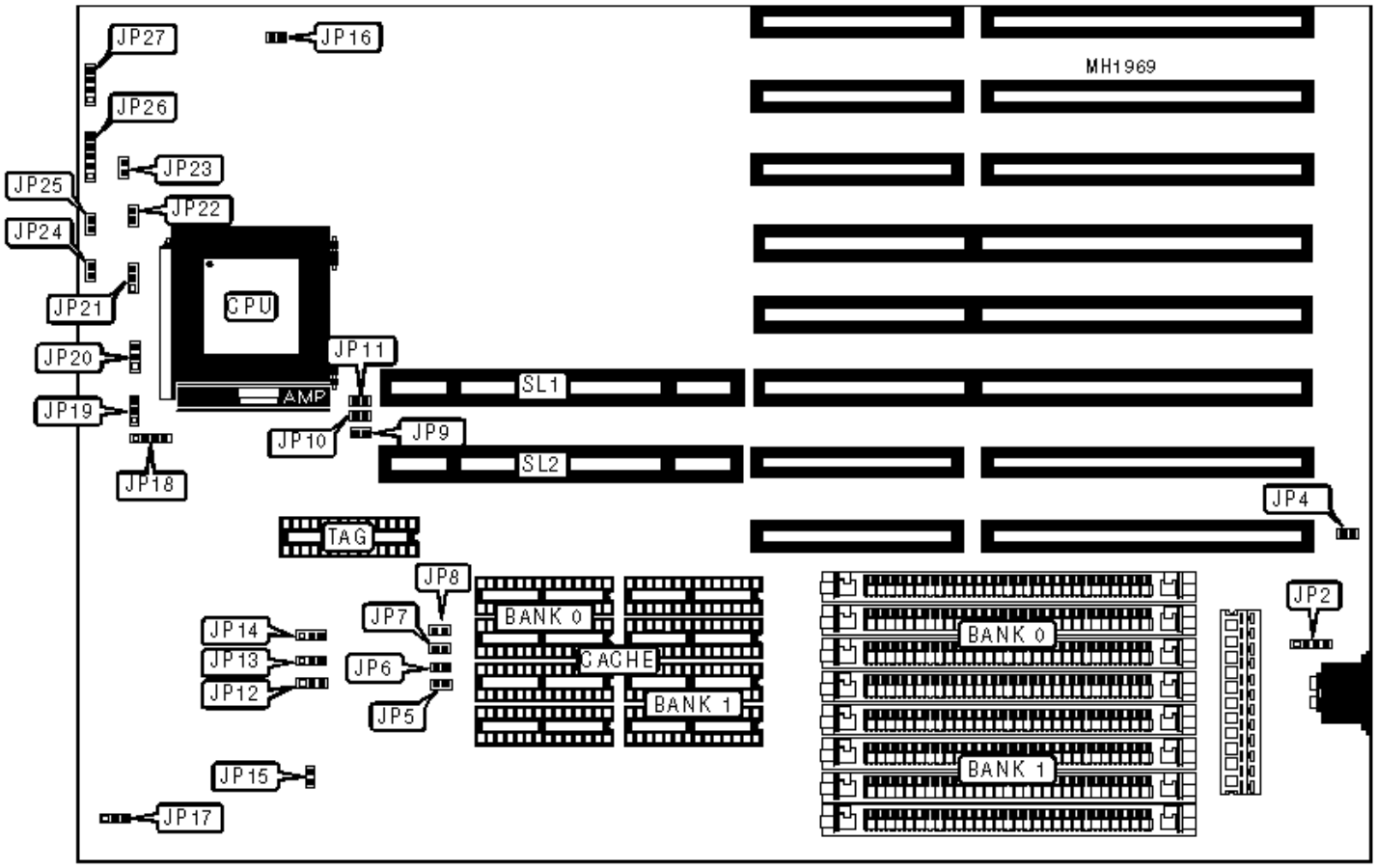
AMPTRON INTERNATIONAL. INC.

DX-8100C

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
External battery	JP2	Speaker	JP27
Reset switch	JP24	Turbo LED	JP23
Turbo switch	JP25	32-bit VESA Local bus slots	SL1, SL2
Keylock	JP26		

USER CONFIGURABLE SETTINGS

	Function	Jumper	Position
»	Enable on board battery	JP4	Closed
	Enable external battery	JP4	Open
»	Factory configured - do not alter	JP9	Closed
»	Factory configured - do not alter	JP15	Closed
»	Monitor type select color	JP16	Closed
	Monitor type select monochrome	JP16	Open
»	CPU speed select 50MHz	JP17	pins 1 & 2 closed
	CPU speed select 33MHz	JP17	pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9

16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
65MB	(4) 256K x 9	(4) 16M x 9
65MB	(4) 16M x 9	(4) 256K x 9
68MB	(4) 1M x 9	(4) 16M x 9
68MB	(4) 16M x 9	(4) 1M x 9
80MB	(4) 16M x 9	(4) 4M x 9
80MB	(4) 4M x 9	(4) 16M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP5	JP6	JP7	JP8
64KB	Open	Open	Open	Open
128KB	Closed	Open	Open	Closed
256KB	Closed	Closed	Closed	Closed

CACHE JUMPER CONFIGURATION (CON'T)			
Size	JP12	JP13	JP14
64KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
128KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed
256KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION			
CPU Type	JP20	JP21	JP22
80486SX	Open	pins 2 & 3 closed	Open
80487SX	pins 2 & 3 closed	pins 1 & 2 closed	Closed
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	Closed
80486DX2	pins 1 & 2 closed	pins 1 & 2 closed	Closed

VESA WAIT STATE/BUS SPEED CONFIGURATION			
CPU speed	Wait states	JP10	JP11
< 33MHz	0 wait states	Open	Open
> 33MHz	1 wait state	Closed	Closed

CLOCK CHIP CONFIGURATION		
Speed	JP18	JP19
33MHz	pins 1 & 2 closed	pins 2 & 3 closed
50MHz	pins 3 & 4 closed	pins 1 & 2 closed