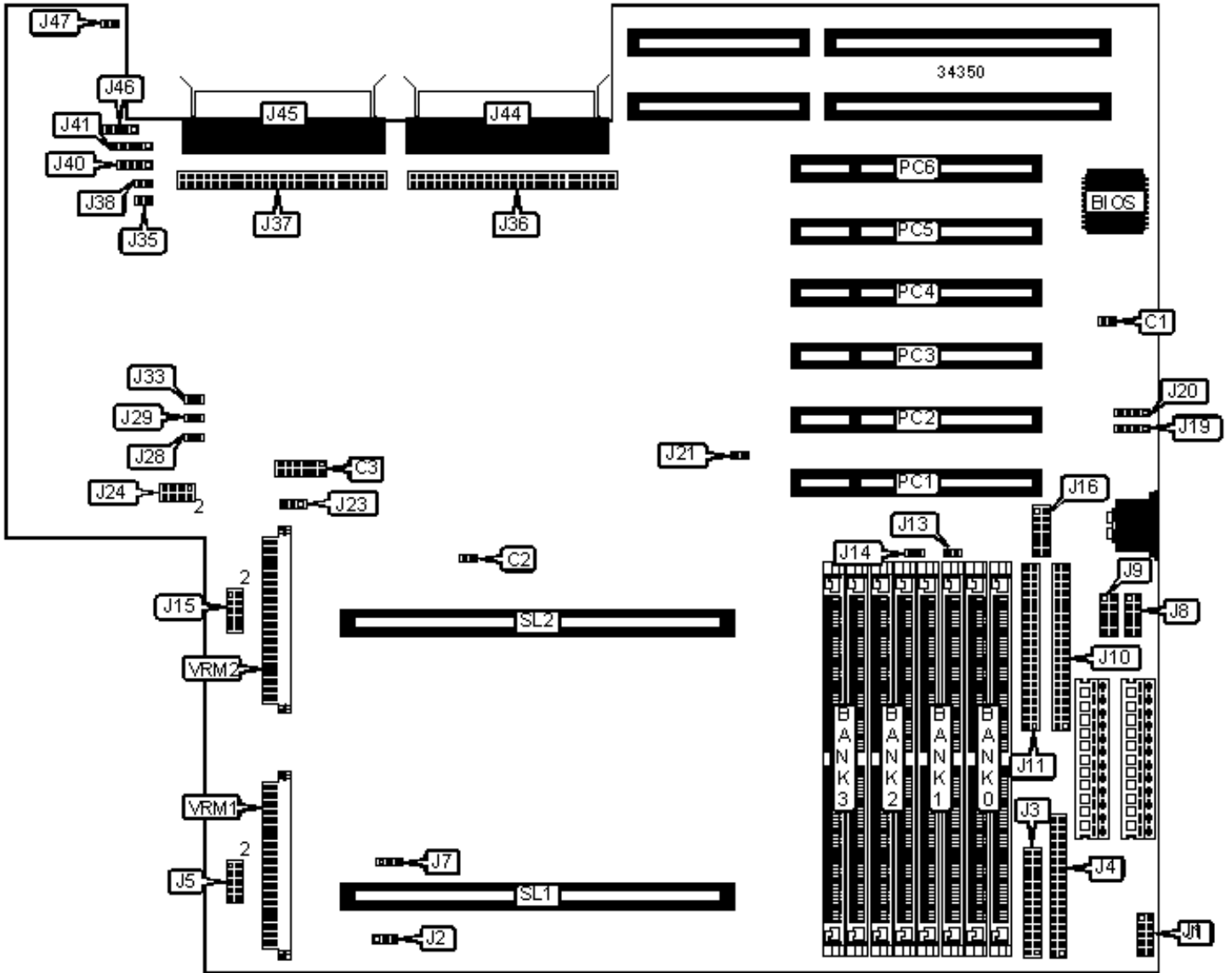


AMERICAN MEGATRENDS, INC.

MEGARUM 758

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
PS/2 mouse interface	J1	Turbo LED	J33
CPU fan power 1	J2	SCSI interface 1	J36
Parallel port	J3	SCSI interface 2	J37
Floppy drive interface	J4	IDE interface LED	J38
CPU fan power 2	J7	SCSI interface LED	J40
Serial port 2	J8	Power LED & keylock	J41
Serial port 1	J9	Wide SCSI interface 1	J44
IDE interface	J10	Wide SCSI interface 2	J45
IDE interface	J11	Speaker	J46
VGA interface	J16	System override connector	J47
Green PC connector	J18	32-bit PCI slots	PC1 - PC6
USB connector 1	J19	CPU Slot 1	SL1
USB connector 2	J20	CPU Slot 2	SL2
Chassis fan power	J23	VRM connector 1	VRM1
Reset switch	J28	VRM connector 2	VRM2
Chassis intrusion connector	J29		

Note: The location of J18 is unidentified.

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	C1	Unidentified
» Factory configured - do not alter	C2	Unidentified
» Factory configured - do not alter	C3	Unidentified
» PCI IRQ disabled for onboard PCI VGA	J13	Closed

	PCI IRQ enabled for onboard PCI VGA	J13	Open
»	On board video enabled	J14	Open
	On board video disabled	J14	Closed
»	CMOS memory normal operation	J35	Open
	CMOS memory clear	J35	Closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(2) 1M x 36	None	None	None
16MB	(2) 1M x 36	(2) 1M x 36	None	None
16MB	(2) 2M x 36	None	None	None
24MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36	None
32MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36	None	None
32MB	(2) 4M x 36	None	None	None
48MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36	None
64MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36
64MB	(2) 4M x 36	(2) 4M x 36	None	None

DRAM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2	Bank 3
64MB	(2) 8M x 36	None	None	None
96MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36	None
128MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36	None	None
128MB	(2) 16M x 36	None	None	None

192MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36	None
256MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36	None	None
256MB	(2) 32M x 36	None	None	None
384MB	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36	None
512MB	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36
512MB	(2) 32M x 36	(2) 32M x 36	None	None
768MB	(2) 32M x 36	(2) 32M x 36	(2) 32M x 36	None
1GB	(2) 32M x 36	(2) 32M x 36	(2) 32M x 36	(2) 32M x 36

Note: Board accepts EDO memory. Do not use 16M x 36 SIMMs that have more than 36 chips per SIMM.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU.

CPU SPEED SELECTION

Speed	J21	J24
120MHz	Open	Pins 1 & 2, 3 & 4, 5 & 6, 7 & 8 closed
133MHz	Closed	Pins 1 & 2, 3 & 4, 5 & 6, 7 & 8 closed
150MHz	Open	Pins 3 & 4, 5 & 6, 7 & 8 closed
166MHz	Closed	Pins 3 & 4, 5 & 6, 7 & 8 closed
180MHz	Open	Pins 1 & 2, 5 & 6, 7 & 8 closed
200MHz	Closed	Pins 1 & 2, 5 & 6, 7 & 8 closed
210MHz	Open	Pins 5 & 6, 7 & 8 closed
233MHz	Closed	Pins 5 & 6, 7 & 8 closed
240MHz	Open	Pins 1 & 2, 3 & 4, 7 & 8 closed
266MHz	Closed	Pins 1 & 2, 3 & 4, 7 & 8 closed

CPU VOLTAGE SELECTION

Voltage		J5	J15
	1.8v	1 & 2, 3 & 4, 7 & 8	1 & 2, 3 & 4, 7 & 8
	1.85v	1 & 2, 3 & 4, 7 & 8, 9 & 10	1 & 2, 3 & 4, 7 & 8, 9 & 10
	1.9v	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4, 5 & 6
	1.95v	1 & 2, 3 & 4, 5 & 6, 9 & 10	1 & 2, 3 & 4, 5 & 6, 9 & 10
	2.0v	1 & 2, 3 & 4, 5 & 6, 7 & 8	1 & 2, 3 & 4, 5 & 6, 7 & 8
	2.05v	1 & 2, 3 & 4, 5 & 6, 7 & 8, 9 & 10	1 & 2, 3 & 4, 5 & 6, 7 & 8, 9 & 10
	2.1v	9 & 10	9 & 10
	2.2v	7 & 8	7 & 8
	2.3v	7 & 8, 9 & 10	7 & 8, 9 & 10
	2.4v	5 & 6	5 & 6
	2.5v	5 & 6, 9 & 10	5 & 6, 9 & 10
	2.6v	5 & 6, 7 & 8	5 & 6, 7 & 8
	2.7v	5 & 6, 7 & 8, 9 & 10	5 & 6, 7 & 8, 9 & 10
	2.8v	3 & 4	3 & 4
	2.9v	3 & 4, 9 & 10	3 & 4, 9 & 10
	3.0v	3 & 4, 7 & 8	3 & 4, 7 & 8
	3.1v	3 & 4, 7 & 8, 9 & 10	3 & 4, 7 & 8, 9 & 10
	3.2v	3 & 4, 5 & 6	3 & 4, 5 & 6
»	3.3v	3 & 4, 5 & 6, 9 & 10	3 & 4, 5 & 6, 9 & 10
	3.4v	3 & 4, 5 & 6, 7 & 8	3 & 4, 5 & 6, 7 & 8
	3.5v	3 & 4, 5 & 6, 7 & 8, 9 & 10	3 & 4, 5 & 6, 7 & 8, 9 & 10

Note: Pins designated should be in the closed position. J5 controls Slot 1 & J15 controls Slot 2.