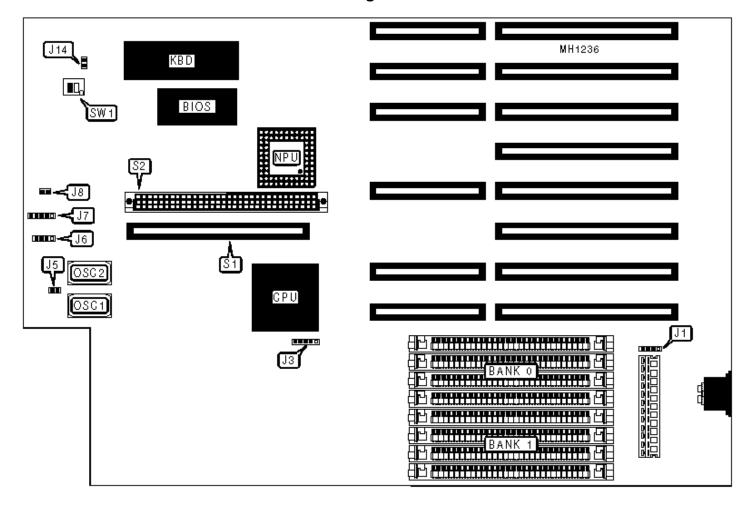
AMERICAN MEGATRENDS, INC.

JE 3025A/33A

Configuration



| CONNECTIONS | | | | | | | |
|---------------------|----------|-----------------------------|----------|--|--|--|--|
| Purpose | Location | Purpose | Location | | | | |
| External battery | J1 | Turbo switch | J8 | | | | |
| Turbo LED | J5 | Reset switch | J14 | | | | |
| Speaker | J6 | 32-bit external memory card | S1 | | | | |
| Power LED & keylock | J7 | CPU expansion card | \$2 | | | | |

| | USER CONFIGURABLE SETTINGS | | | | | | |
|----------|--|---------------|-------------------------------------|--|--|--|--|
| Function | | Jumper/Switch | Position | | | | |
| | DRAM address sequence select bank 0, then bank 1 | J3 | pins 1 & 2 closed pins 3 & 4 closed | | | | |
| | DRAM address sequence select bank 1, then bank 0 | J3 | pins 2 & 3 closed pins 4 & 5 closed | | | | |
| » | Factory configured - do not alter | SW1/switch 1 | open | | | | |
| » | Monitor type select color | SW1/switch 2 | closed | | | | |
| | Monitor type select monochrome | SW1/switch 2 | open | | | | |

ache memory is factory installed, and is not alterable. The size and location of the Cache and TAG chips are unknown.

DRAM CONFIGURATION Size Bank 1 Bank 0 1MB (4) 256K x 9 NONE 2MB (4) 256K x 9 (4) 256K x 9 NONE 4MB (4) 1M x 9 5MB (4) 1M x 9 (4) 256K x 9 8MB (4) 1M x 9 (4) 1M x 9 16MB (4) 4M x 9 NONE 20MB (4) 1M x 9 (4) 4M x 9 32MB (4) 4M x 9 (4) 4M x 9