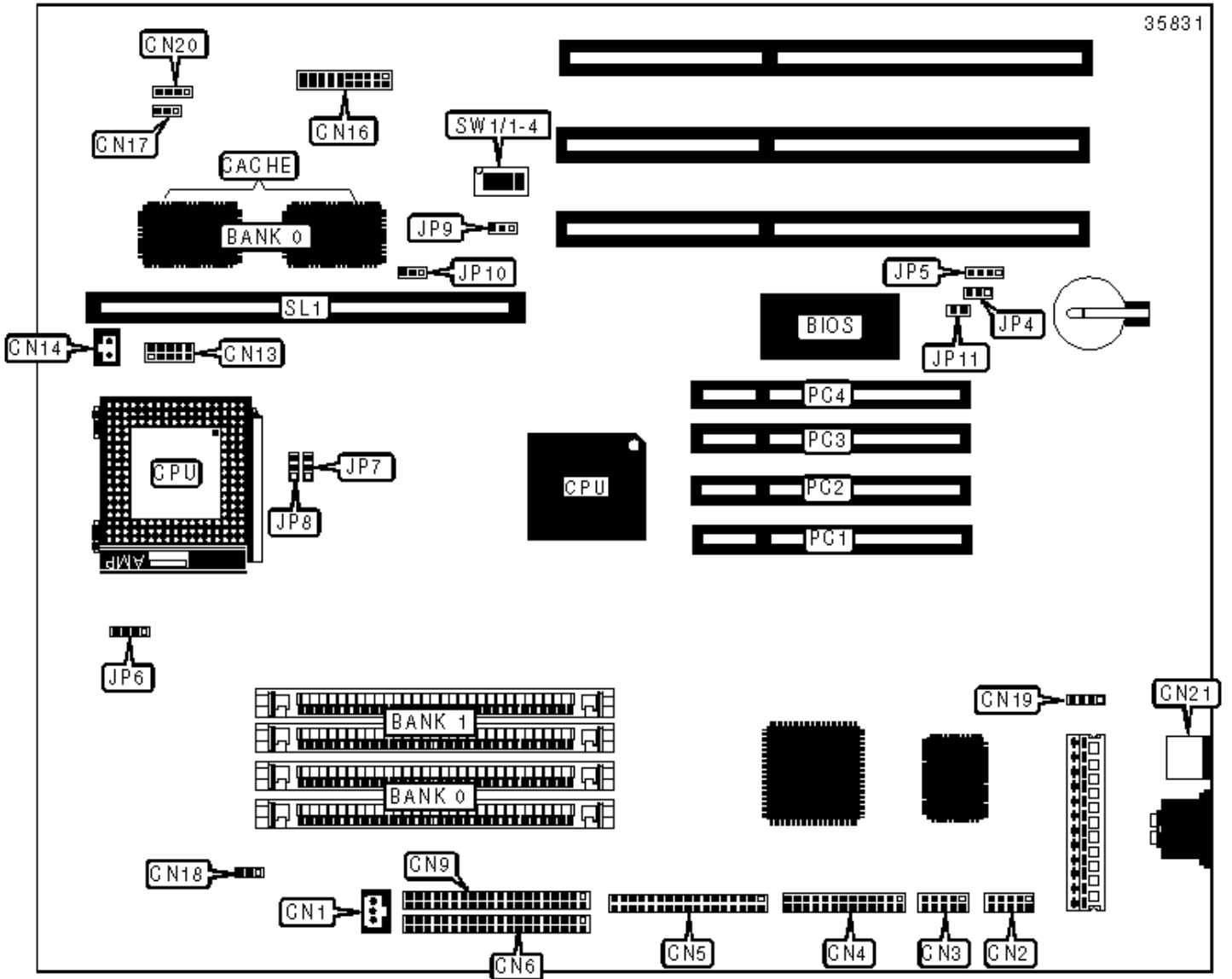


ACER, INC.

V55-2

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Standby power connector	CN1	Speaker	CN16/pins 7 - 10
Serial port 2	CN2	Turbo LED	CN16/pins 12 & 13
Serial port 1	CN3	Reset	CN16/pins 19 & 20
Parallel port	CN4	External suspend switch	CN17
Floppy drive interface	CN5	Software shutdown switch	CN18
IDE interface 2	CN6	IrDA connector	CN19
IDE interface 1	CN9	IDE interface LED	CN20
CPU power shorting block	CN13	PS/2 mouse port	CN21
CPU fan connector	CN14	32-bit PCI expansion slots	PC1-PC4
Keylock	CN16/pins 1 & 2	Cache slot	SL1
Power LED	CN16/pins 3 - 5		

USER CONFIGURABLE SETTINGS

Function	Label	Position
» ROM type select Flash ROM	JP4	Pins 2 & 3 closed
ROM type select EPROM	JP4	Pins 1 & 2 closed
» CPU core voltage select 2.8v	JP6	Pins 2 & 3 closed
CPU core voltage select 3.2v	JP6	Pins 1 & 2 closed
» CPU I/O voltage select 3.3v	JP7	Pins 2 & 3 closed
CPU I/O voltage select 3.5v	JP7	Pins 1 & 2 closed
» Factory configured - do not alter	JP10	Pins 2 & 3 closed
CMOS discharge normal	JP11	Open
CMOS discharge clear	JP11	Closed

ROM BRAND SELECTION

Brand		JP5
»	29EE010	Pins 2 & 3 closed
	NC	Pins 1 & 2 closed
	29F001	Pins 3 & 4 closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 32	None
16MB	(2) 2M x 32	None
16MB	(2) 1M x 32	(2) 1M x 32
24MB	(2) 2M x 32	(2) 1M x 32
32MB	(2) 4M x 32	None
32MB	(2) 2M x 32	(2) 2M x 32
40MB	(2) 4M x 32	(2) 1M x 32
48MB	(2) 4M x 32	(2) 2M x 32
64MB	(2) 8M x 32	None
64MB	(2) 4M x 32	(2) 4M x 32
72MB	(2) 8M x 32	(2) 1M x 32
80MB	(2) 8M x 32	(2) 2M x 32
96MB	(2) 8M x 32	(2) 4M x 32
128MB	(2) 8M x 32	(2) 8M x 32

CACHE CONFIGURATION

Size	SL1	Tag	Bank 0
256KB	(2) 32K x 32	(8) 32K x 32	(2) 32K x 32

512KB	(4) 32K x 32	(8) 32K x 32	(4) 32K x 32
512KB	(2) 64K x 32	(8) 32K x 32	(2) 64K x 32
Note: The location of Tag SRAM is unidentified.			

CACHE JUMPER CONFIGURATION			
Size		JP8	JP9
»	256KB	Pins 2 & 3 closed	Pins 2 & 3 closed
	512KB	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU FREQUENCY SELECTION (INTEL)						
CPU	Clock Speed	Clock Ratio	SW1/1	SW1/2	SW1/3	SW1/4
75MHz	50MHz	1.5x	Off	Off	On	On
90MHz	60MHz	1.5x	Off	Off	Off	On
100MHz	66MHz	1.5x	Off	Off	On	Off
120MHz	60MHz	2x	Off	On	Off	On
133MHz	66MHz	2x	Off	On	On	Off
150MHz	60MHz	2.5x	On	On	Off	On
166MHz	66MHz	2.5x	On	On	On	Off

CPU FREQUENCY SELECTION (INTEL MMX)						
CPU	Clock Speed	Clock Ratio	SW1/1	SW1/2	SW1/3	SW1/4
166MHz	66MHz	2.5x	On	On	On	Off
180MHz	60MHz	3x	On	Off	Off	On
200MHz	66MHz	3x	On	Off	On	Off

CPU FREQUENCY SELECTION (AM K5)
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CPU	Clock Speed	Clock Ratio	SW1/1	SW1/2	SW1/3	SW1/4
75MHz	50MHz	1.5x	Off	Off	On	On
90MHz	60MHz	1.5x	Off	Off	Off	On
100MHz	66MHz	1.5x	Off	Off	On	Off
120MHz	60MHz	2x	Off	On	Off	On
133MHz	66MHz	2x	Off	On	On	Off
150MHz	60MHz	2.5x	On	On	Off	On
166MHz	66MHz	2.5x	On	On	On	Off

CPU FREQUENCY SELECTION (CX 6X86)

CPU	Host Bus	Clock Ratio	SW1/1	SW1/2	SW1/3	SW1/4
120MHz	50MHz	2x	Off	On	On	On
150MHz	60MHz	2x	Off	On	Off	On
166MHz	66MHz	2x	Off	On	On	Off

CPU FREQUENCY SELECTION (IBM 6X86)

CPU	Host Bus	Clock Ratio	SW1/1	SW1/2	SW1/3	SW1/4
120MHz	50MHz	2x	Off	On	On	On
150MHz	60MHz	2x	Off	On	Off	On
166MHz	66MHz	2x	Off	On	On	Off