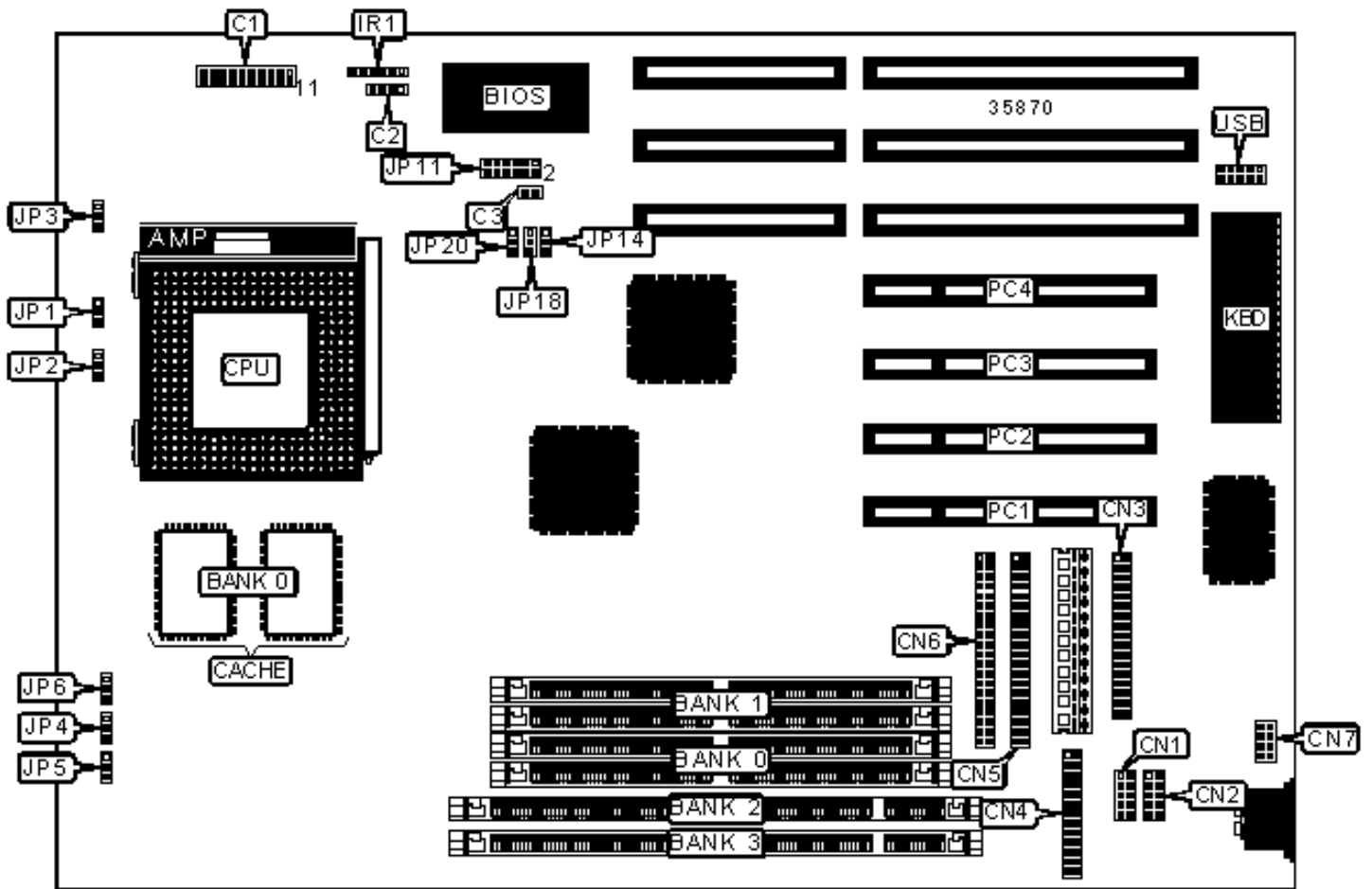


ACER, INC.

AP5T (REV. 3.0)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Power LED & keylock	C1/pins 1 – 5	Floppy drive interface	CN3
Speaker	C1/pins 7 – 10	Parallel port	CN4
Reset switch	C1/pins 19 & 20	IDE interface 1	CN5
Green PC LED	C1/pins 12 & 13	IDE interface 2	CN6
Green PC connector	C1/pins 15 - 17	PS/2 mouse interface	CN7
IDE interface LED	C2	IR connector	IR1
CPU fan power	C3	32-bit PCI slots	PC1 – PC4
Serial port 1	CN1	USB connector	USB
Serial port 2	CN2		

USER CONFIGURABLE SETTINGS

Function	Label	Position
» CMOS memory normal operation	JP14	Pins 1 & 2 closed
CMOS memory clear	JP14	Pins 2 & 3 closed
» On board I/O enabled	JP18	Pins 1 & 2 closed
On board I/O disabled	JP18	Pins 2 & 3 closed
» PS/2 mouse enabled	JP20	Pins 1 & 2 closed
PS/2 mouse disabled	JP20	Pins 2 & 3 closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36

24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36

SIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

Note: Board accepts EDO memory.

DIMM CONFIGURATION

Size	Bank 2	Bank 3
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None

16MB	(1) 1M x 64	(1) 1M x 64
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None
64MB	(1) 4M x 64	(1) 4M x 64
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 16M x 64	None
128MB	(1) 8M x 64	(1) 8M x 64
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
Note: Board accepts SDRAM memory.		

CACHE CONFIGURATION	
Size	Bank 0
256KB	(2) 32K x 32
512KB	(2) 64K x 32

CPU SPEED SELECTION (CX 6X86)

CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
150MHz	60MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
166MHz	66MHz	2x	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
200MHz	75MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CX 6X86L)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
150MHz	60MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
166MHz	66MHz	2x	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
200MHz	75MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CX MII)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
166MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
200MHz	66MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
233MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
266MHz	66MHz	3.5x	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (IDT C6)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
150MHz	60MHz	2.5x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
180MHz	60MHz	3x	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K5)

CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
90MHz	60MHz	1.5x	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
100MHz	66MHz	1.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
120MHz	60MHz	1.5x	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
133MHz	66MHz	1.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
166MHz	66MHz	1.75x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K6)

CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
166MHz	66MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
233MHz	66MHz	3.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)

CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
90MHz	60MHz	1.5x	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
100MHz	66MHz	1.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
120MHz	60MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
150MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
166MHz	66MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2

200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
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Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL MMX)

CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
150MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
166MHz	66MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
233MHz	66MHz	3.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION

Voltage	JP11
3.45v	Pins 1 & 2 closed
3.52v	Pins 3 & 4 closed
2.9v	Pins 5 & 6 closed
2.8v	Pins 7 & 8 closed
3.2v	Pins 9 & 10 closed