ACER, INC.

ACERMATE 486

Configuration
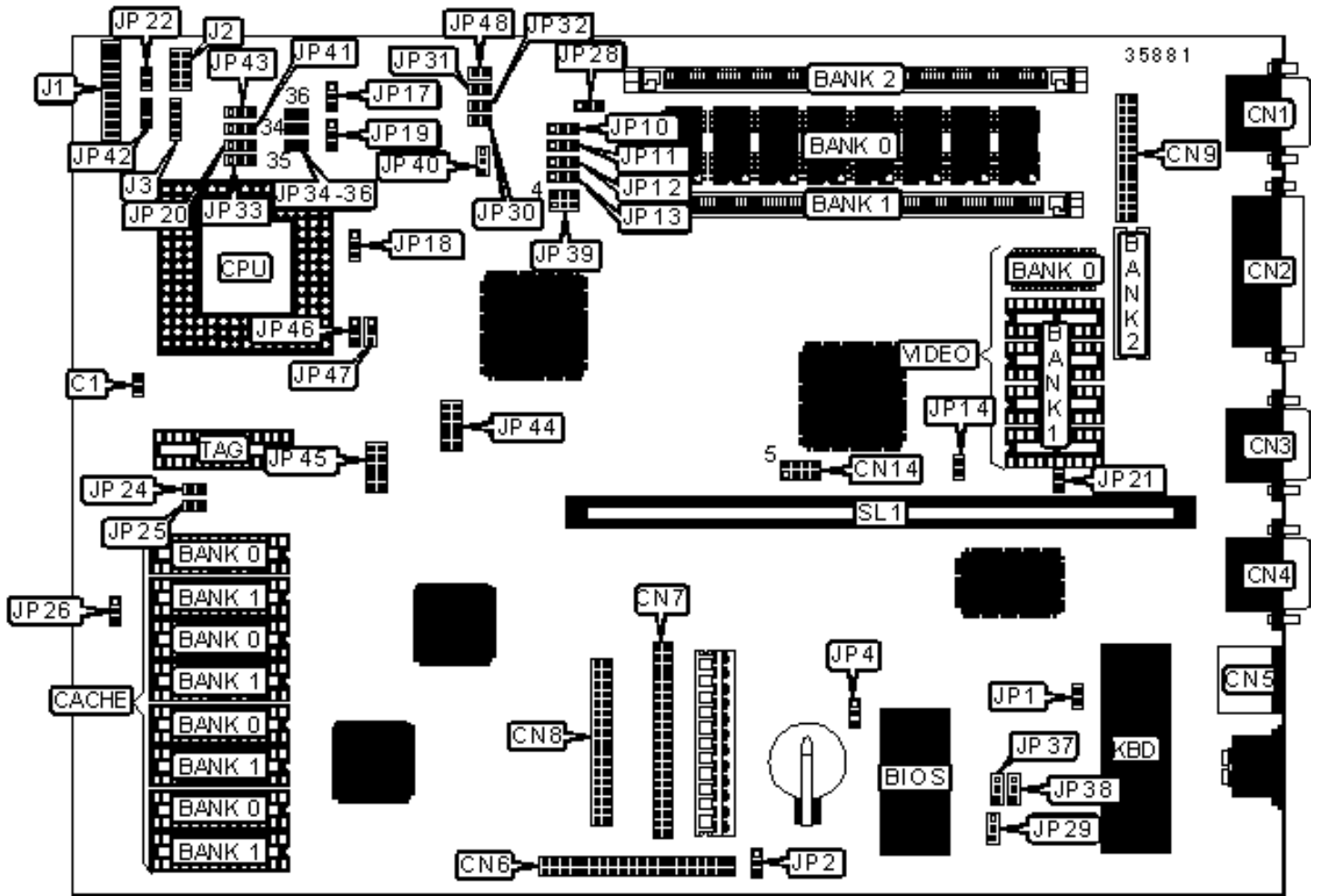
CONNECTIONS

Purpose	Location	Purpose	Location
Chassis fan power	C1	IDE interface 1	CN7
VGA port	CN1	Floppy drive interface	CN8
Parallel port	CN2	VGA feature connector	CN9
Serial port 2	CN3	Daughter board connector	JP44
Serial port 1	CN4	Daughter board connector	JP45
PS/2 mouse port	CN5	Riser slot	SL1
IDE interface 2	CN6		

USER CONFIGURABLE SETTINGS

	Function	Label	Position
»	Factory configured - do not alter	J1	Unidentified
»	Factory configured - do not alter	J2	Unidentified
»	Factory configured - do not alter	J3	Unidentified
»	Factory configured - do not alter	JP1	Unidentified
	Password enabled	JP2	Pins 1 & 2 closed
	Password disabled	JP2	Pins 2 & 3 closed
»	Factory configured - do not alter	JP4	Unidentified
	On board video enabled	JP14	Closed
	On board video disabled	JP14	Open
	IDE interface enabled	JP16	Open
	IDE interface disabled	JP16	Closed
	On board I/O enabled	JP21	Closed
	On board I/O disabled	JP21	Open
»	Factory configured - do not alter	JP22	Unidentified

»	Factory configured - do not alter	JP29	Unidentified

Note: The location of JP16 is unidentified.

SIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
4MB	4MB	None	None
5MB	4MB	(1) 256K x 36	None
6MB	4MB	(1) 256K x 36	(1) 256K x 36
7MB	4MB	(1) 256K x 36	(1) 512K x 36
8MB	4MB	(1) 1M x 36	None
9MB	4MB	(1) 1M x 36	(1) 256K x 36

SIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2
10MB	4MB	(1) 1M x 36	(1) 512K x 36
12MB	4MB	(1) 1M x 36	(1) 1M x 36
16MB	4MB	(1) 1M x 36	(1) 2M x 36
20MB	4MB	(1) 4M x 36	None
21MB	4MB	(1) 4M x 36	(1) 256K x 36
22MB	4MB	(1) 4M x 36	(1) 512K x 36
24MB	4MB	(1) 4M x 36	(1) 1M x 36
28MB	4MB	(1) 4M x 36	(1) 2M x 36
36MB	4MB	(1) 4M x 36	(1) 4M x 36

Note: Bank 0 is factory installed and is not configurable.

DRAM CONFIGURATION

Setting	JP28
On board memory enabled	Pins 1 & 2 closed
On board memory disabled	Pins 2 & 3 closed

CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	None	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION

Size	JP24	JP25	JP26
128KB	Open	Closed	Pins 1 & 2 closed
256KB	Closed	Closed	Pins 2 & 3 closed

VIDEO MEMORY CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
512KB	512KB	None	None
1MB	512KB	(4) 256K x 4	None
2MB	512KB	(4) 256K x 4	(2) 256K x 16

CPU SPEED SELECTION

Speed	CN14	JP10	JP17	JP19
25MHz	Pins 4 & 8 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
33MHz	Pins 3 & 7 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
50iMHz	Pins 4 & 8 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
66iMHz	Pins 3 & 7 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed

100iMHz	Pins 3 & 7 closed	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU TYPE SELECTION

Type	JP11	JP12	JP13	JP18	JP20
CX486	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
IBM486	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
TI486	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
80486SX	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
AM486DX	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
80486DX	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
AM486DX2	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
80486DX2	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
80486DX2 (WB)	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3
AM486DX4	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
80486DX4	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
80486DX4 (WB)	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3
P24T	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION (CON'T)

Type	JP30	JP31	JP32	JP33	JP34
CX486	Closed	Closed	Closed	2 & 3	Open
IBM486	Closed	Closed	Closed	2 & 3	Open
TI486	Open	Open	Open	1 & 2	Open
80486SX	Open	Open	Open	1 & 2	Closed
AM486DX	Open	Open	Open	1 & 2	Open

80486DX	Open	Open	Open	1 & 2	Closed
AM486DX2	Open	Open	Open	1 & 2	Open
80486DX2	Open	Open	Open	1 & 2	Closed
80486DX2 (WB)	Open	Open	Open	1 & 2	Closed
AM486DX4	Open	Open	Open	1 & 2	Open
80486DX4	Open	Open	Open	1 & 2	Closed
80486DX4 (WB)	Open	Open	Open	1 & 2	Closed
P24T	Open	Open	Open	1 & 2	Closed
Note: Pins designated should be in the closed position.					

CPU TYPE SELECTION (CON'T)

Type	JP35	JP36	JP39	JP40	JP41
CX486	Open	Open	1 & 2, 4 & 5	2 & 3	2 & 3
IBM486	Open	Open	1 & 2, 4 & 5	2 & 3	2 & 3
TI486	Open	Open	2 & 3, 5 & 6	2 & 3	2 & 3
80486SX	Closed	Closed	1 & 2, 4 & 5	2 & 3	2 & 3
AM486DX	Open	Open	1 & 2, 4 & 5	1 & 2	1 & 2
80486DX	Closed	Closed	1 & 2, 4 & 5	2 & 3	2 & 3
AM486DX2	Open	Open	1 & 2, 4 & 5	1 & 2	1 & 2
80486DX2	Closed	Closed	1 & 2, 4 & 5	2 & 3	2 & 3
80486DX2 (WB)	Closed	Closed	1 & 2, 4 & 5	2 & 3	2 & 3
AM486DX4	Open	Open	1 & 2, 4 & 5	1 & 2	1 & 2
80486DX4	Closed	Closed	1 & 2, 4 & 5	2 & 3	2 & 3
80486DX4 (WB)	Closed	Closed	1 & 2, 4 & 5	2 & 3	2 & 3
P24T	Closed	Closed	1 & 2, 4 & 5	2 & 3	2 & 3
Note: Pins designated should be in the closed position.					

CPU TYPE SELECTION (CON'T)

Type	JP42	JP43	JP46	JP47	JP48
CX486	2 & 3	Open	1 & 2	2 & 3	Open
IBM486	2 & 3	Open	1 & 2	2 & 3	Open
TI486	1 & 2	Open	1 & 2	1 & 2	Open
80486SX	2 & 3	Open	1 & 2	1 & 2	Open
AM486DX	2 & 3	Open	1 & 2	1 & 2	Closed
80486DX	2 & 3	Open	1 & 2	1 & 2	Open
AM486DX2	2 & 3	Open	1 & 2	1 & 2	Closed
80486DX2	2 & 3	Open	1 & 2	1 & 2	Open
80486DX2 (WB)	2 & 3	Open	2 & 3	1 & 2	Open
AM486DX	2 & 3	Open	1 & 2	1 & 2	Open
80486DX4	2 & 3	Open	1 & 2	1 & 2	Open
80486DX4 (WB)	2 & 3	Open	2 & 3	1 & 2	Open
P24T	2 & 3	Open	2 & 3	1 & 2	Open

Note: Pins designated should be in the closed position.

RESET/RESUME SELECTION

Setting	JP37	JP38
Reset button	Pins 1 & 2 closed	Pins 1 & 2 closed
Suspend/reset button	Pins 2 & 3 closed	Pins 2 & 3 closed