ACER, INC.

ACERALTOS 700ID (M3), 700SD (M3),M3

Configuration
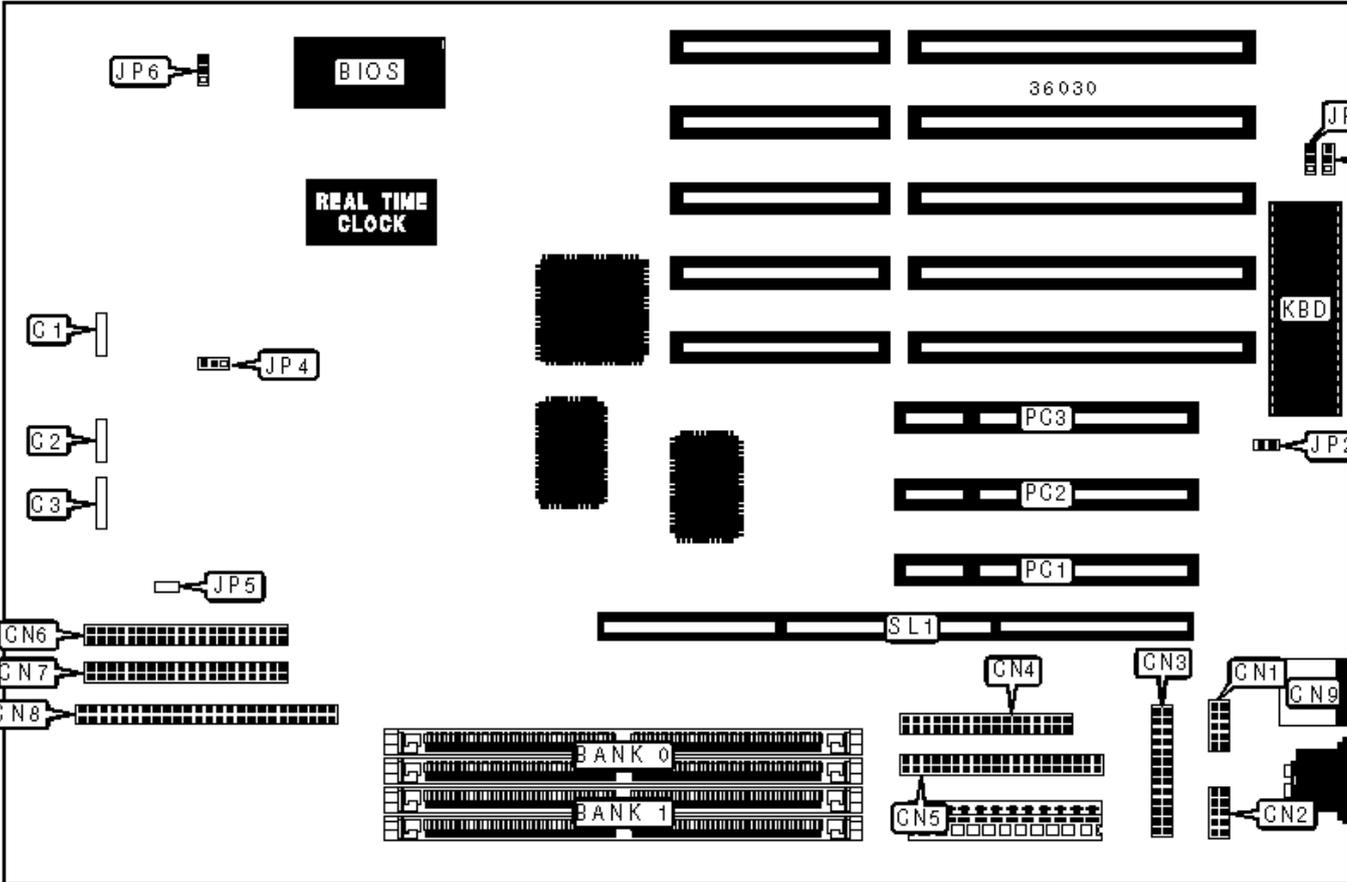
CONNECTIONS

Purpose	Location	Purpose	Location
Reset/green PC connector	C1	IDE interface	CN5
IDE interface LED	C2	IDE interface	CN6
Power LED & keylock	C3	IDE interface	CN7
Serial port 1	CN1	SCSI-2 interface	CN8
Serial port 2	CN2	PS/2 mouse port	CN9
Parallel port	CN3	32-bit PCI slots	PC1 - PC3
Floppy drive interface	CN4	CPU slot	SL1

USER CONFIGURABLE SETTINGS

	Function	Label	Position
»	Password disabled	JP1	Pins 2 & 3 closed
	Password enabled	JP1	Pins 1 & 2 closed
»	Reset button enabled	JP2	Closed
	Reset button disabled	JP2	Open
»	Factory configured - do not alter	JP3	Pins 1 & 2 closed
»	RTC select 128KB/256KB	JP4	Pins 1 & 2 closed
	RTC select 4KB	JP4	Pins 2 & 3 closed
»	Factory configured - do not alter	JP5	Unidentified
»	Buzzer enabled	JP6	Pins 1 & 2 closed
	Speaker enabled	JP6	Pins 2 & 3 closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None

16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION
Note: The location of the cache is unidentified.