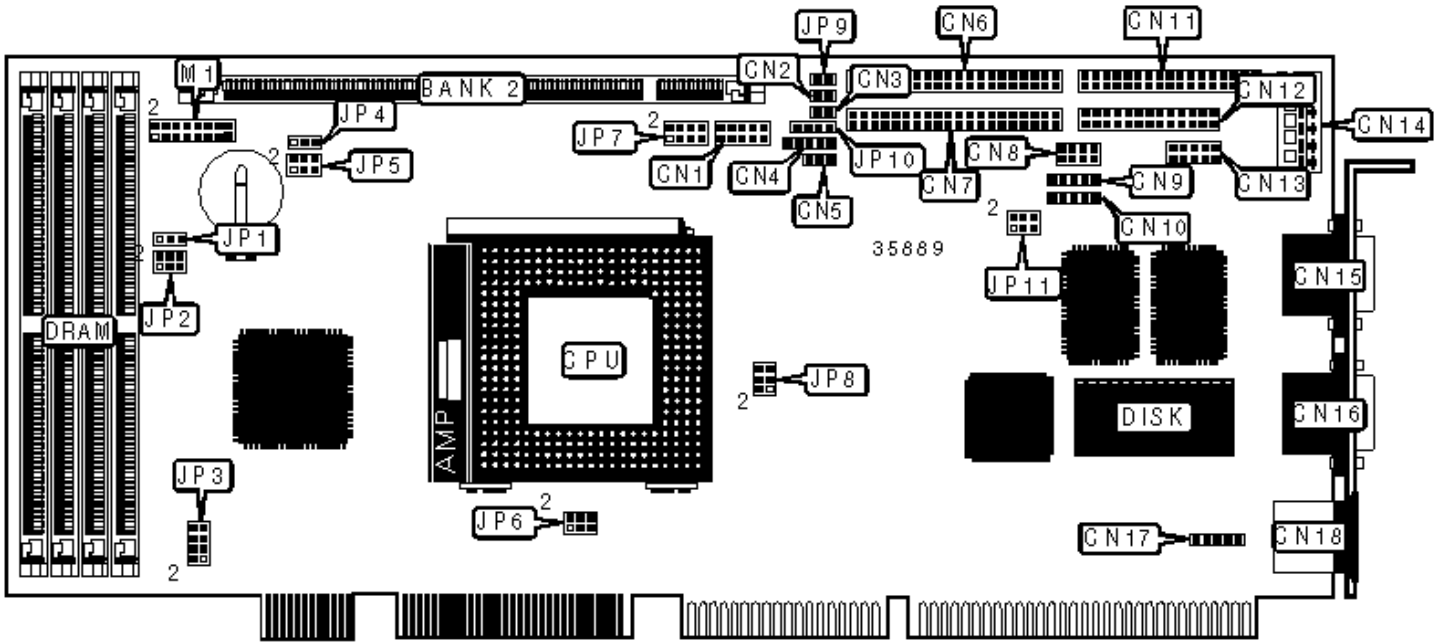


AAEON TECHNOLOGY, INC.

SBC-598

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
USB connector	CN1	Floppy drive interface	CN11
IDE interface LED	CN2	Parallel port	CN12
Thermal alarm LED	CN3	Serial port 2	CN13
Power LED & keylock	CN4	5v power	CN14
Chassis fan power	CN5	Serial port 1	CN15
IDE interface 1	CN6	VGA port	CN16
IDE interface 2	CN7	Auxiliary keyboard connector	CN17
Digital I/O connector	CN8	PS/2 mouse port	CN18
IR connector	CN10		

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	CN9	Unidentified
» CMOS memory normal operation	JP1	Pins 1 & 2 closed
CMOS memory clear	JP1	Pins 2 & 3 closed
» On board video enabled	JP2	Pins 1 & 3, 2 & 4 closed
On board video disabled	JP2	Pins 3 & 5, 4 & 6 closed
» PCI CLK asynchronous	JP4	Pins 1 & 2 closed
PCI CLK synchronous	JP4	Pins 2 & 3 closed
» Reset enabled	JP9	Open
Reset disabled	JP9	Closed
» Buzzer enabled	JP10	Pins 2 & 3 closed
Speaker enabled	JP10	Open

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36

SIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

Note: Board accepts EDO memory.

DIMM CONFIGURATION

Size	Bank 2
8MB	(1) 1M x 36
16MB	(1) 2M x 36
32MB	(1) 4M x 36
64MB	(1) 8M x 36
128MB	(1) 16M x 36
256MB	(1) 32M x 36

Note: Board accepts EDO memory.

DIMM VOLTAGE CONFIGURATION

Voltage	JP3
3.3v	Pins 5 & 6, 7 & 8 closed
5v	Pins 1 & 2, 3 & 4 closed

MEMORY BANK SELECTION

Setting	M1
SIMMs used	Pins 1 & 2, 3 & 4, 5 & 6, 7 & 8, 9 & 10, 11 & 12, 13 & 14, 15 & 16 closed
DIMM used	Pins 1 & 3, 2 & 4, 5 & 7, 6 & 8, 9 & 11, 10 & 12, 13 & 15, 14 & 16 closed

CACHE CONFIGURATION

Note: The location of the cache is unidentified.

CPU SPEED SELECTION (CX 6X86MX)

CPU speed	Clock speed	Multiplier	JP5	JP6
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166MHz	60MHz	2.5x	3 & 4, 5 & 6	1 & 2, 3 & 4
200MHz	66MHz	2.5x	1 & 2, 5 & 6	1 & 2, 3 & 4
233MHz	66MHz	3x	1 & 2, 5 & 6	3 & 4
266MHz	66MHz	3.5x	1 & 2, 5 & 6	Open

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K6)

CPU speed	Clock speed	Multiplier	JP5	JP6
166MHz	66MHz	2.5x	1 & 2, 5 & 6	1 & 2, 3 & 4
200MHz	66MHz	3x	1 & 2, 5 & 6	3 & 4
233MHz	66MHz	3.5x	1 & 2, 5 & 6	Open

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)

CPU speed	Clock speed	Multiplier	JP5	JP6
166MHz	66MHz	2.5x	1 & 2, 5 & 6	1 & 2, 3 & 4
200MHz	66MHz	3x	1 & 2, 5 & 6	3 & 4

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL MMX)

CPU speed	Clock speed	Multiplier	JP5	JP6
166MHz	66MHz	2.5x	1 & 2, 5 & 6	1 & 2, 3 & 4
200MHz	66MHz	3x	1 & 2, 5 & 6	3 & 4
233MHz	66MHz	3.5x	1 & 2, 5 & 6	Open

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION

Type	JP8
Single voltage	Pins 3 & 5, 4 & 6 closed
» Dual voltage	Pins 1 & 3, 2 & 4 closed

CPU VOLTAGE SELECTION

Voltage	JP7
2.0v	Open
2.1v	Pins 7 & 8 closed
2.2v	Pins 5 & 6 closed
2.3v	Pins 5 & 6, 7 & 8 closed
2.4v	Pins 3 & 4 closed
2.5v	Pins 3 & 4, 7 & 8 closed
2.6v	Pins 3 & 4, 5 & 6 closed
2.7v	Pins 3 & 4, 5 & 6, 7 & 8 closed
» 2.8v	Pins 1 & 2 closed
2.9v	Pins 1 & 2, 7 & 8 closed
3.0v	Pins 1 & 2, 5 & 6 closed
3.1v	Pins 1 & 2, 5 & 6, 7 & 8 closed
3.2v	Pins 1 & 2, 3 & 4 closed
3.3v	Pins 1 & 2, 3 & 4, 7 & 8 closed
3.4v	Pins 1 & 2, 3 & 4, 5 & 6 closed
3.5v	Pins 1 & 2, 3 & 4, 5 & 6, 7 & 8 closed

DISK ADDRESS SELECTION

Address	JP11
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	C800	Pins 1 & 2, 3 & 4 closed
	CC00	Pins 1 & 2, 5 & 6 closed
	D000	Pins 1 & 2 closed
	D400	Pins 3 & 4, 5 & 6 closed
»	D800	Pins 3 & 4 closed
	DC00	Pins 5 & 6 closed
	E000	Open
	Disabled	Pins 1 & 2, 3 & 4, 5 & 6 closed