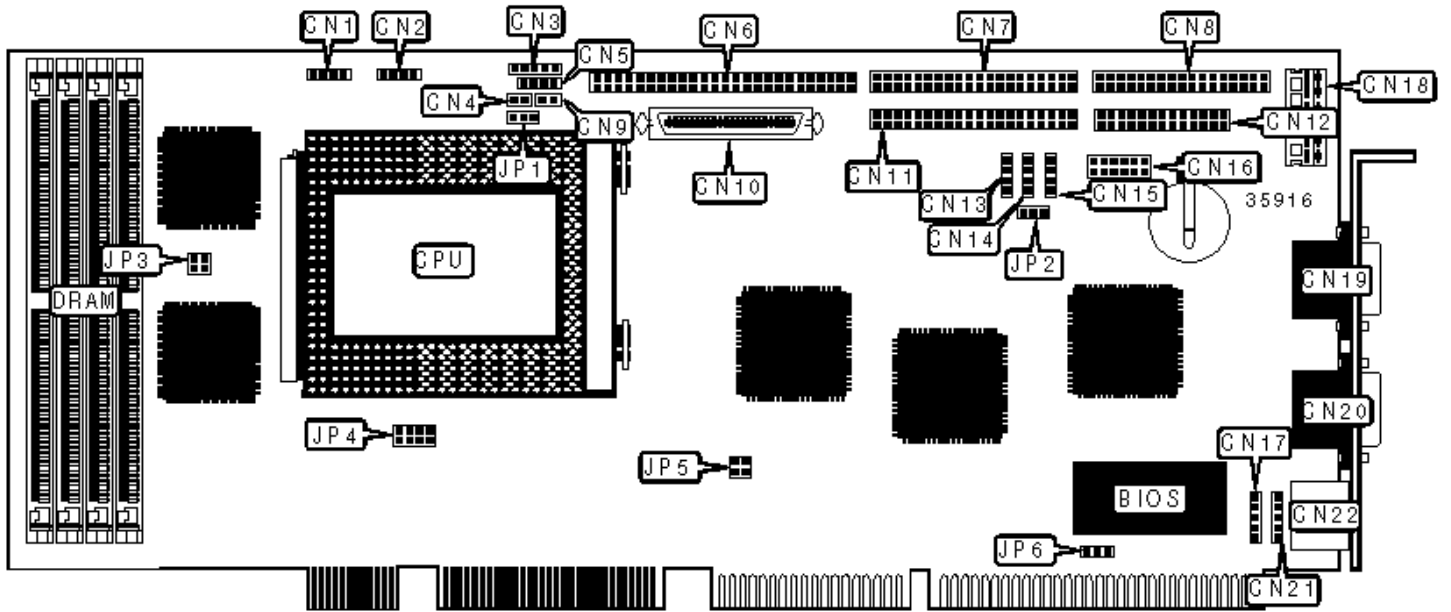


AAEON TECHNOLOGY, INC.

SBC-670S

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Power connector	CN1	Parallel port	CN12
Chassis fan power	CN2	USB connector 1	CN13
Power LED & keylock	CN3	USB connector 2	CN14
IDE interface LED	CN4	IR connector	CN15
Speaker	CN5	GPIO connector	CN16
SCSI-2 interface	CN6	PS/2 mouse interface	CN17
IDE interface 1	CN7	5v power	CN18
Floppy drive interface	CN8	Serial port 1	CN19
Reset switch	CN9	Serial port 2	CN20
Ultra Wide SCSI interface	CN10	Auxiliary keyboard connector	CN21
IDE interface 2	CN11	PS/2 mouse port	CN22

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	JP2	Unidentified

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36

40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
Note: Board accepts EDO memory.		

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium Pro CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP3	JP4	JP5
150MHz	60MHz	2.5x	1 & 2	3 & 4, 5 & 6, 7 & 8	1 & 2
166MHz	66MHz	2.5x	3 & 4	3 & 4, 5 & 6, 7 & 8	3 & 4
180MHz	60MHz	3x	1 & 2	1 & 2, 5 & 6, 7 & 8	1 & 2
200MHz	66MHz	3x	3 & 4	1 & 2, 5 & 6, 7 & 8	3 & 4

Note: Pins designated should be in the closed position.

FLASH BIOS SELECTION

Setting	JP6
5v	Open
12v	Pins 1 & 2 closed
2M	Pins 2 & 3 closed

