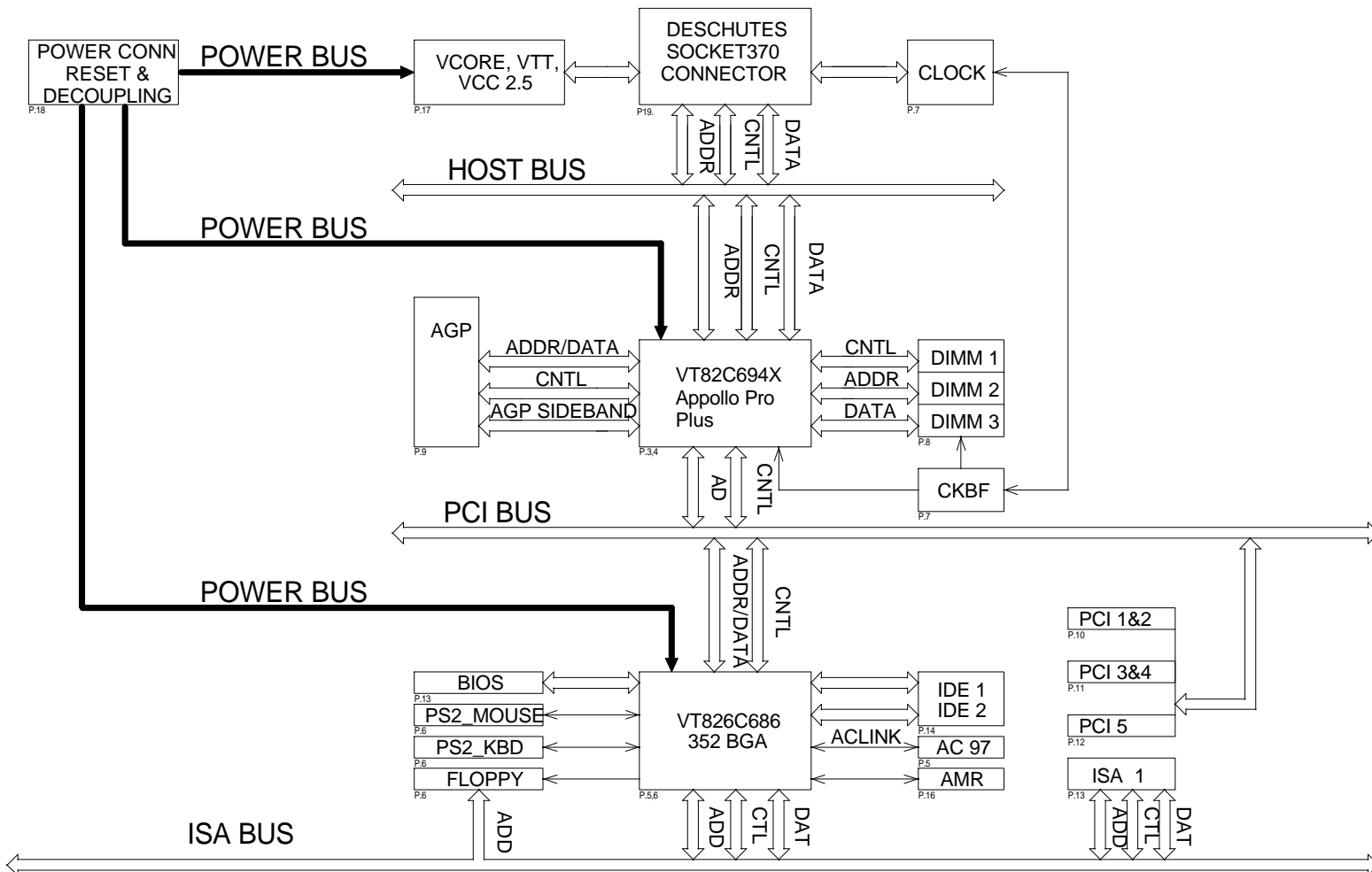


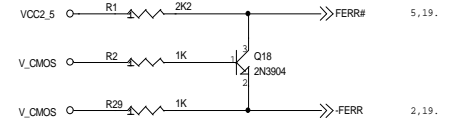
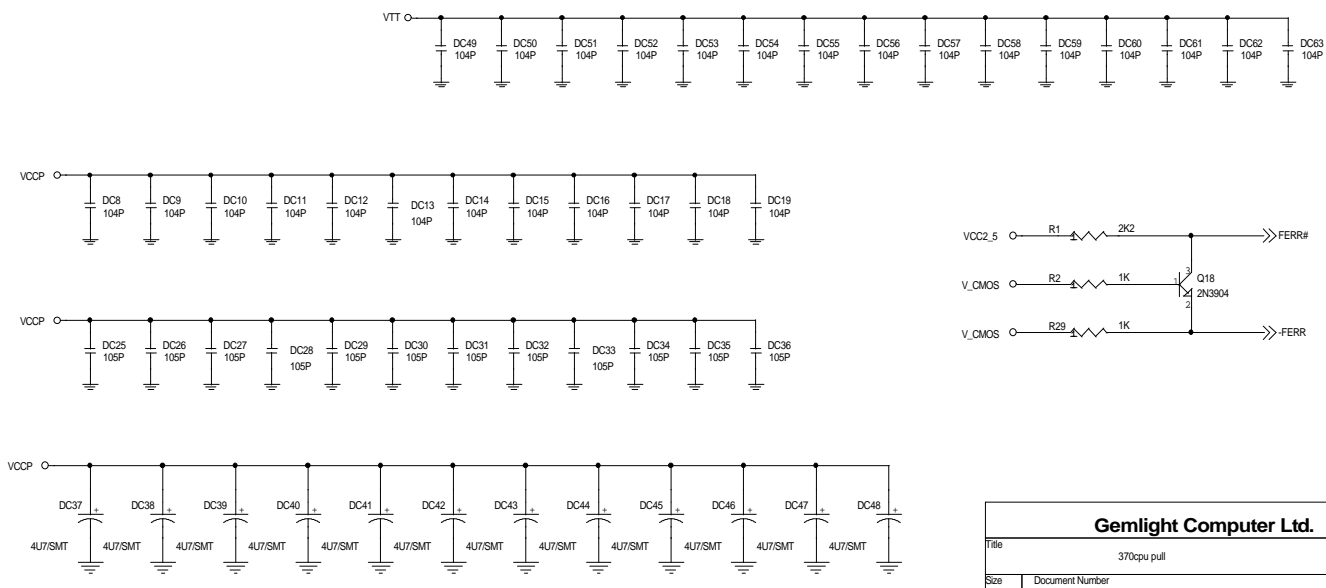
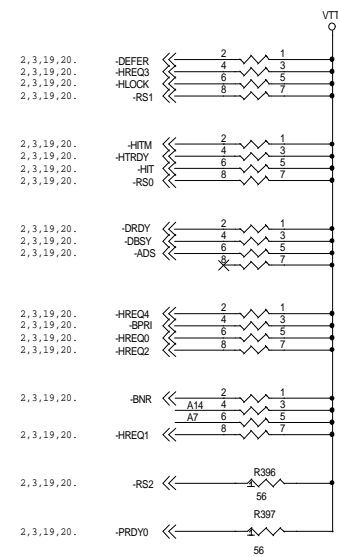
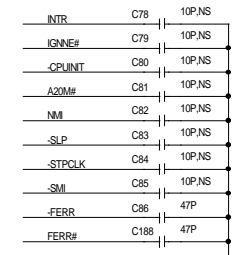
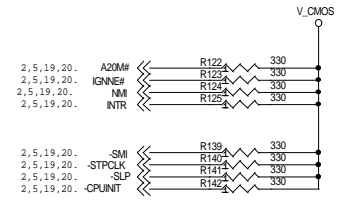
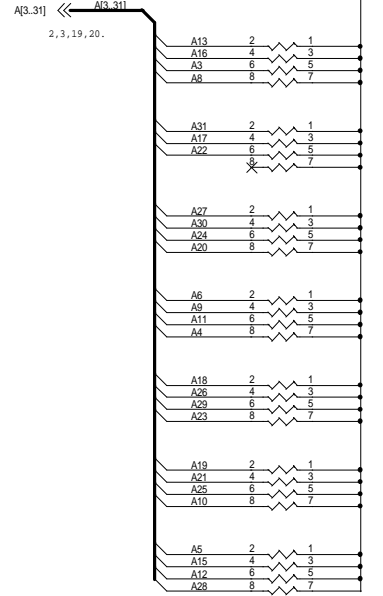
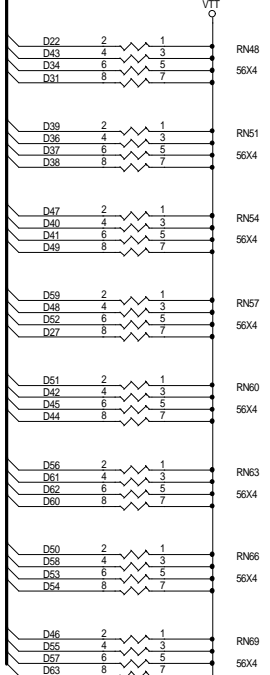
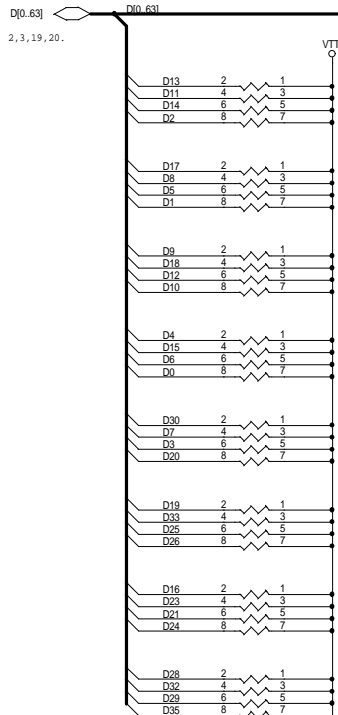
# GMB-6295 V0 SCHEMATIC DRAWINGS

Release RB 05 Apr. 2000



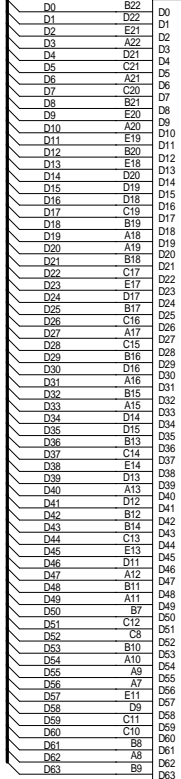
- | 01\_LINK
- | 02\_PULL\_R
- | 03\_694A
- | 04\_694B
- | 05\_686A
- | 06\_686B
- | 07\_CLK
- | 08\_DIMM
- | 09\_AGP
- | 10\_PCI12
- | 11\_PCI34
- | 12\_PCI5
- | 13\_ISA
- | 14\_PORT
- | 15\_AUDIO
- | 16\_AMR
- | 17\_CPUPWR
- | 18\_ATX
- | 19\_S370

<b>Gemlight Computer Ltd.</b>		
Title: BLOCK DIAGRAM AND VERSION CONTROL		
Size: B	Document Number: GMB-6295 V0	Rev: B
Date: Wednesday, April 05, 2000	Sheet: 1	of 19



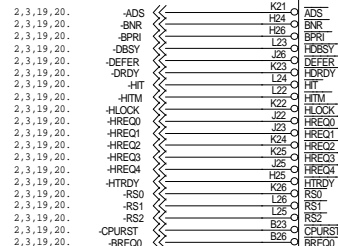
<b>Gemlight Computer Ltd.</b>		
Title		370cpu pull
Size	Document Number	GMB-6295
Date:	Wednesday, April 05, 2000	Sheet 2 of 19
		Rev B

D[0..63] 2, 3, 19, 20.



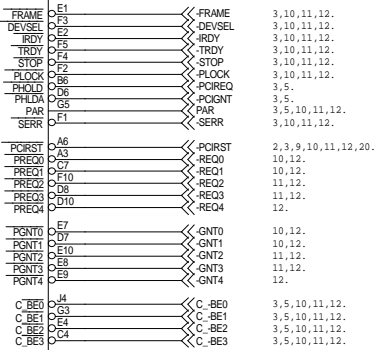
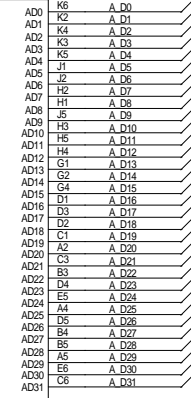
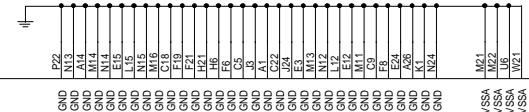
U2A

VT82C694X, S2A  
VT82C693A(A), S2B

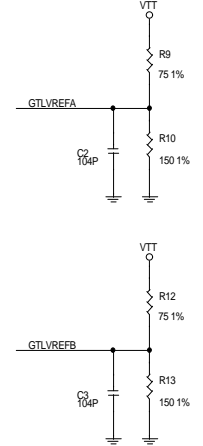


2, 3, 19, 20.

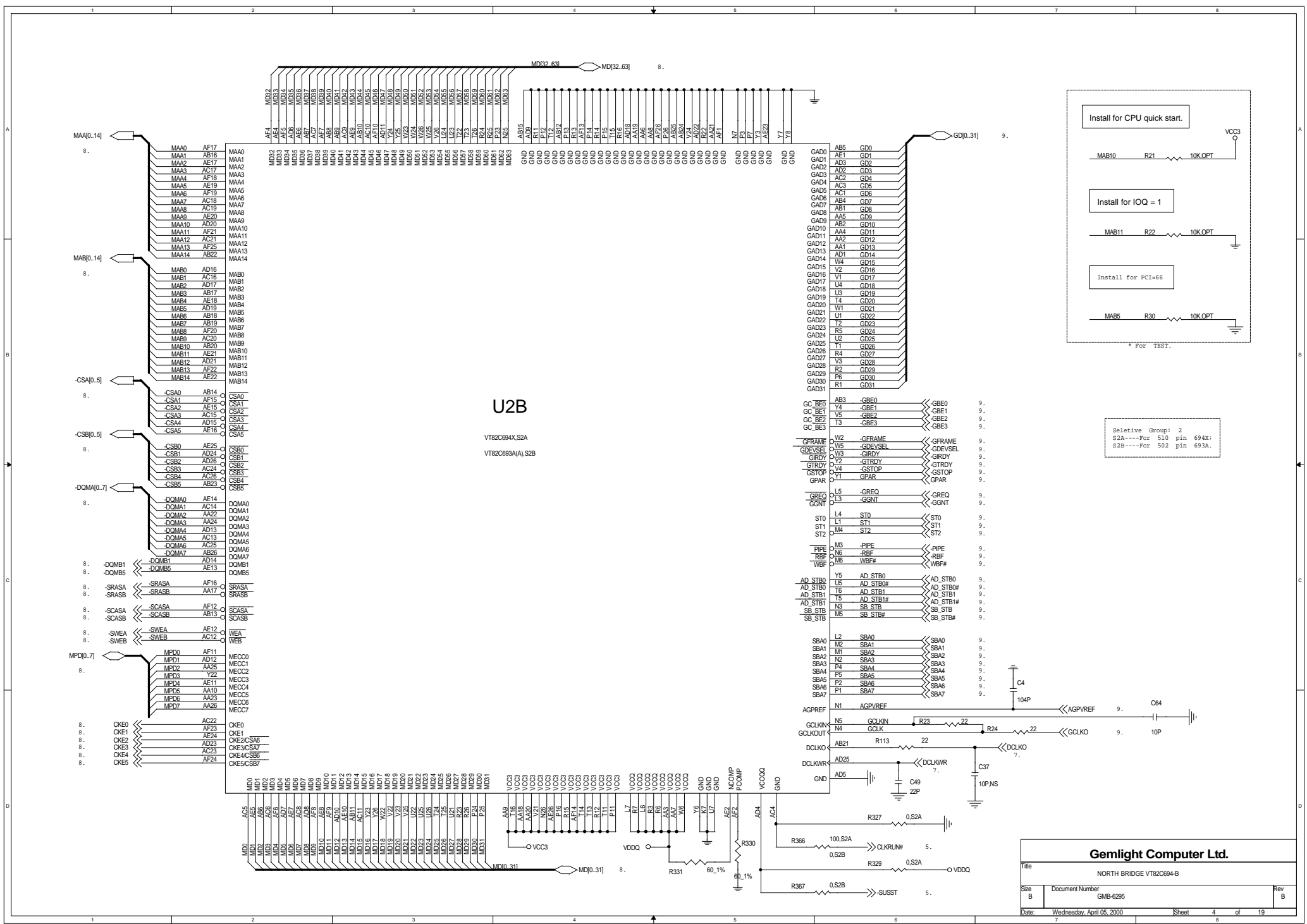
A[3..31] A[3..31]



Selective Group: 2  
S2A----For 510 pin 694X;  
S2B----For 502 pin 693A.



<b>Gemlight Computer Ltd.</b>			
Title NORTH BRIDGE VT82C694-A			
Size B	Document Number GMB-6295	Rev B	
Date: Wednesday, April 05, 2000	Sheet 3	of 19	



Install for CPU quick start.

MAB10 R21 10K OPT

Install for IOQ = 1

MAB11 R22 10K OPT

Install for PCI=66

MAB5 R30 10K OPT

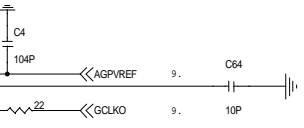
\* For TEST.

**U2B**

VT82C694X, S2A  
VT82C693A(A), S2B

GC_BE0	AB3	GBE0	GBE0	9.
GC_BE1	Y4	GBE1	GBE1	9.
GC_BE2	V5	GBE2	GBE2	9.
GC_BE3	T3	GBE3	GBE3	9.
GFRAME	W2	GFRAME	GFRAME	9.
GDEVSEL	W5	GDEVSEL	GDEVSEL	9.
GIRDY	W3	GIRDY	GIRDY	9.
GTRDY	Y2	GTRDY	GTRDY	9.
GSTOP	Y4	GSTOP	GSTOP	9.
GPAR	Y1	GPAR	GPAR	9.
GREQ	L5	GREQ	GREQ	9.
GGNT	L3	GGNT	GGNT	9.
ST0	L4	ST0	ST0	9.
ST1	L1	ST1	ST1	9.
ST2	M4	ST2	ST2	9.
PIPE	M3	PIPE	PIPE	9.
RBF	N6	RBF	RBF	9.
WBF	M6	WBF#	WBF#	9.
AD_STB0	Y5	AD_STB0	AD_STB0	9.
AD_STB0W	U5	AD_STB0W	AD_STB0W	9.
AD_STB1	T6	AD_STB1	AD_STB1	9.
AD_STB1#	Y5	AD_STB1#	AD_STB1#	9.
SB_STB	N3	SB_STB	SB_STB	9.
SB_STB#	M5	SB_STB#	SB_STB#	9.

SBA0	L2	SBA0	SBA0	9.
SBA1	M2	SBA1	SBA1	9.
SBA2	M1	SBA2	SBA2	9.
SBA3	N2	SBA3	SBA3	9.
SBA4	P4	SBA4	SBA4	9.
SBA5	P5	SBA5	SBA5	9.
SBA6	P2	SBA6	SBA6	9.
SBA7	P1	SBA7	SBA7	9.

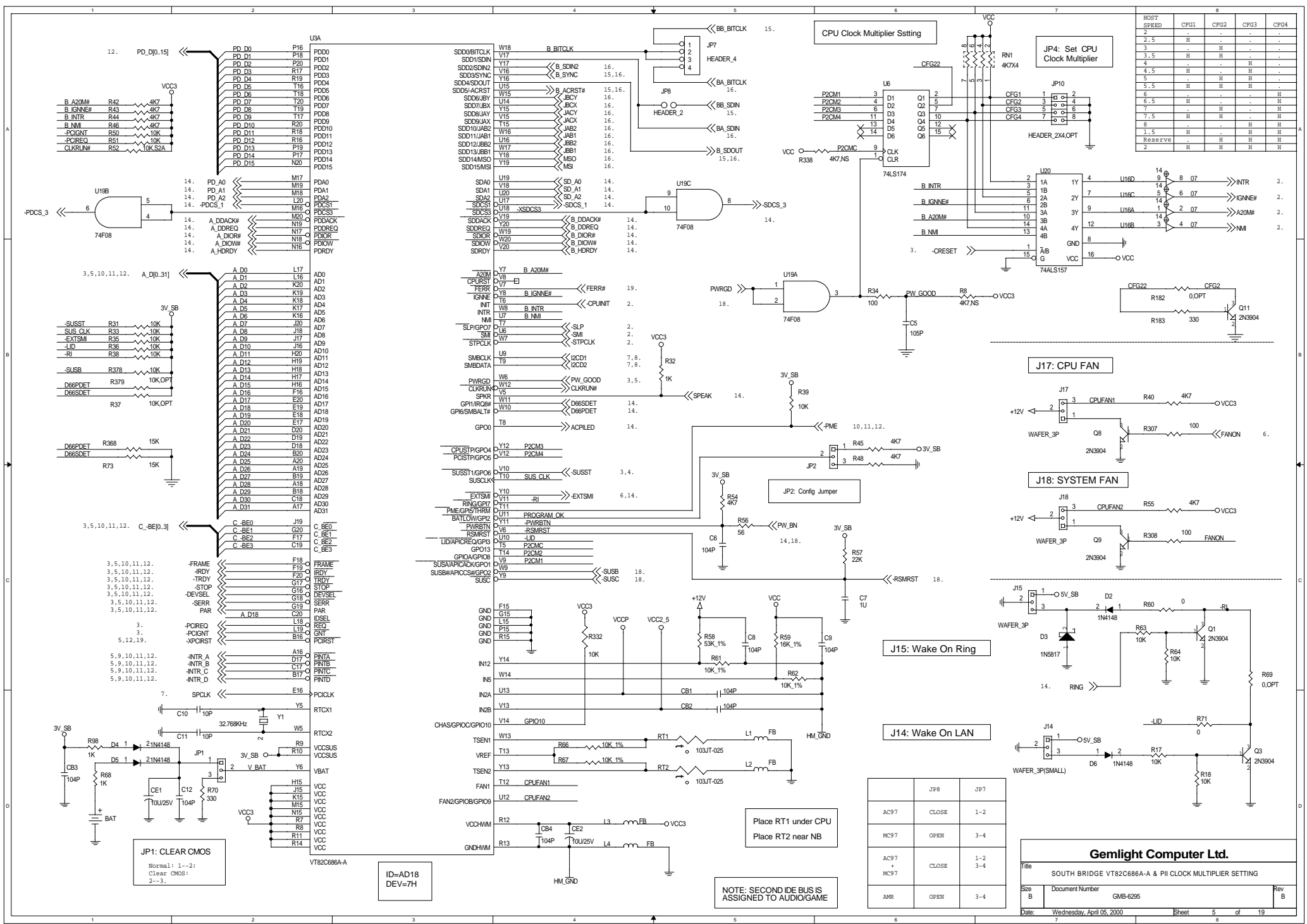


**Gemlight Computer Ltd.**

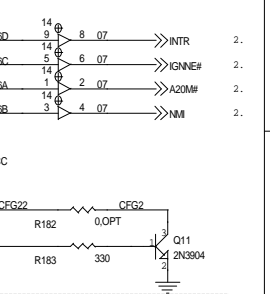
Title: NORTH BRIDGE VT82C694-B

Size B Document Number GMB-6295 Rev B

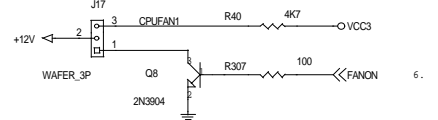
Date: Wednesday, April 05, 2000 Sheet 4 of 19



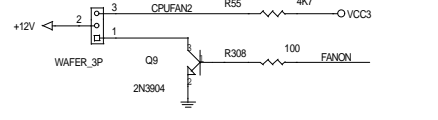
HOST SPEED	CPG1	CPG2	CPG3	CPG4
2	.	.	.	.
2.5	H	.	.	.
3	.	H	.	.
3.5	H	H	.	.
4	.	.	H	.
4.5	H	.	H	.
5	.	H	H	.
5.5	H	H	H	.
6	.	.	.	H
6.5	H	.	.	H
7	.	H	.	H
7.5	H	H	.	H
8	.	.	H	H
1.5	H	.	H	H
2	H	H	H	H
Reserve	.	H	H	H
2	H	H	H	H



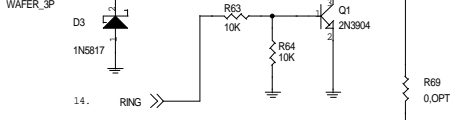
**J17: CPU FAN**



**J18: SYSTEM FAN**



**J15: Wake On Ring**



**J14: Wake On LAN**



	JP8	JP7
AC97	CLOSE	1-2
MC97	OPEN	3-4
AC97 +	CLOSE	1-2
MC97	CLOSE	3-4
AMR	OPEN	3-4

**Gemlight Computer Ltd.**

Title: SOUTH BRIDGE VT82C686A-A & PII CLOCK MULTIPLIER SETTING

Size B Document Number GMB-6295 Rev B

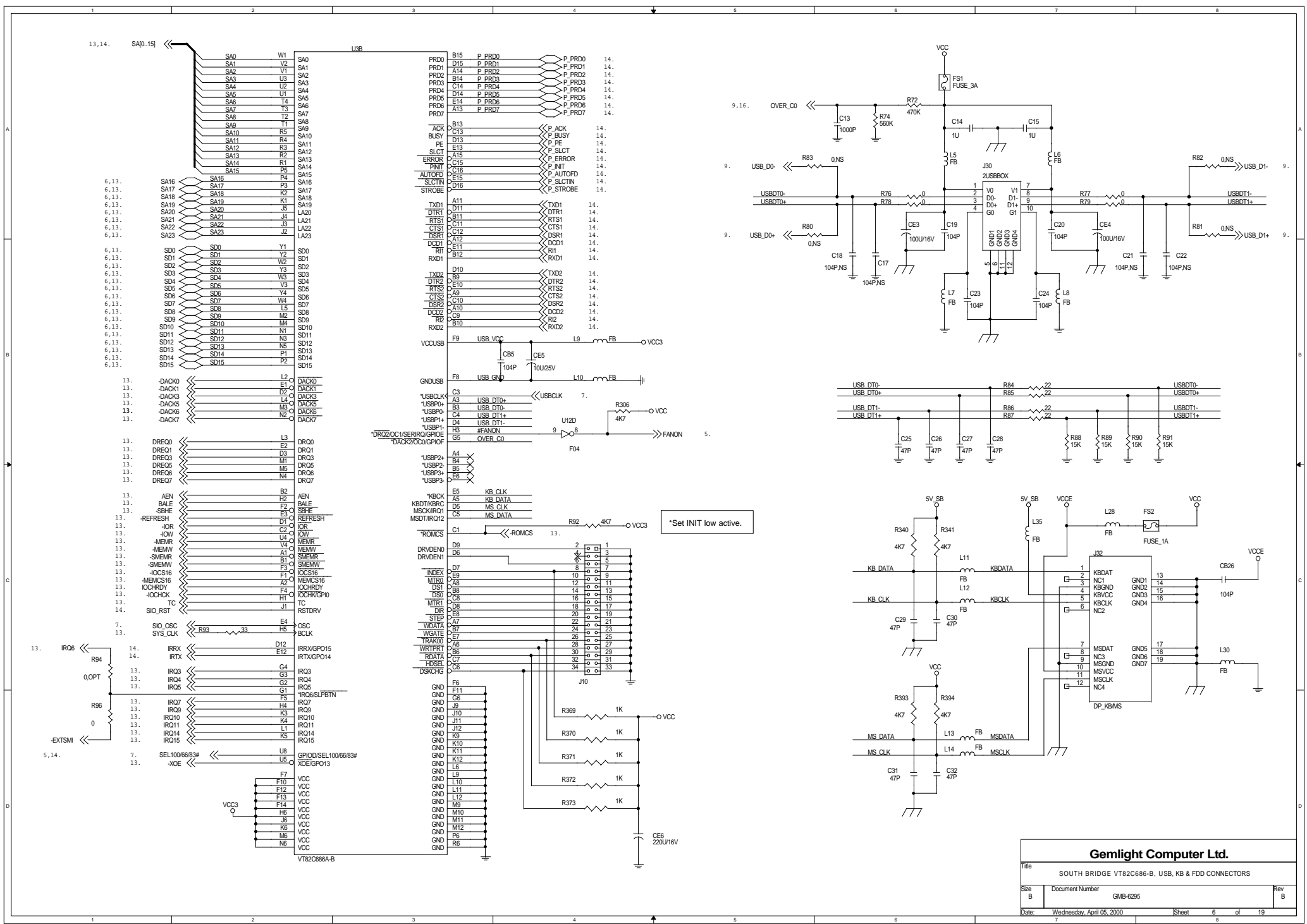
Date: Wednesday, April 05, 2000 Sheet 5 of 19

**JP1: CLEAR CMOS**  
 Normal: 1--2;  
 Clear CMOS: 2--3.

ID=AD18  
 DEV=7H

NOTE: SECOND IDE BUS IS  
 ASSIGNED TO AUDIO GAME

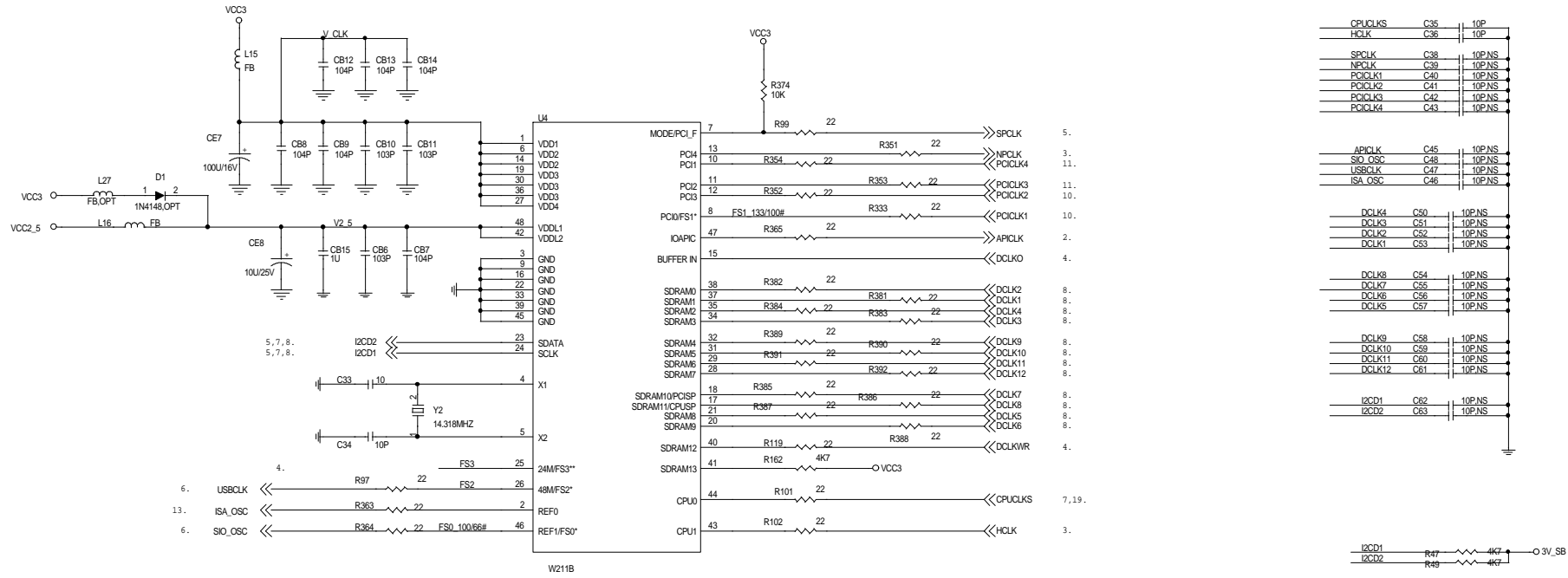
Place RT1 under CPU  
 Place RT2 near NB



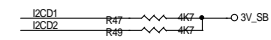
13.14.	SA0	W1	SA0	B15	P_PRD0	P_PRD0	14.
	SA1	V2	SA1	D15	P_PRD1	P_PRD1	14.
	SA2	V1	SA2	PRD1	P_PRD2	P_PRD2	14.
	SA3	U3	SA3	PRD3	B14	P_PRD3	14.
	SA4	U2	SA4	C14	P_PRD4	P_PRD4	14.
	SA5	U1	SA5	PRD4	D14	P_PRD5	14.
	SA6	T4	SA6	PRD5	E14	P_PRD6	14.
	SA7	T3	SA7	PRD6	A13	P_PRD7	14.
	SA8	T2	SA8	B13	B13	P_ACK	14.
	SA9	T1	SA9	BUSY	C13	P_BUSY	14.
	SA10	R5	SA10	PE	D13	P_PE	14.
	SA11	R4	SA11	SLCT	E13	P_SLCT	14.
	SA12	R3	SA12	ERROR	C15	P_ERROR	14.
	SA13	R2	SA13	FINIT	C16	P_FINIT	14.
	SA14	R1	SA14	AUTOFD	E15	P_AUTOFD	14.
	SA15	P5	SA15	SLCTIN	D16	P_SLCTIN	14.
6.13.	SA16	SA16	SA16	STROBE	D16	P_STROBE	14.
6.13.	SA17	SA17	SA17				
6.13.	SA18	SA18	SA18				
6.13.	SA19	SA19	SA19				
6.13.	SA20	SA20	SA20				
6.13.	SA21	SA21	SA21				
6.13.	SA22	SA22	SA22				
6.13.	SA23	SA23	SA23				
6.13.	SD0	SD0	SD0				
6.13.	SD1	SD1	SD1				
6.13.	SD2	SD2	SD2				
6.13.	SD3	SD3	SD3				
6.13.	SD4	SD4	SD4				
6.13.	SD5	SD5	SD5				
6.13.	SD6	SD6	SD6				
6.13.	SD7	SD7	SD7				
6.13.	SD8	SD8	SD8				
6.13.	SD9	SD9	SD9				
6.13.	SD10	SD10	SD10				
6.13.	SD11	SD11	SD11				
6.13.	SD12	SD12	SD12				
6.13.	SD13	SD13	SD13				
6.13.	SD14	SD14	SD14				
6.13.	SD15	SD15	SD15				
13.	-DACK0	DACK0	DACK0				
13.	-DACK1	DACK1	DACK1				
13.	-DACK2	DACK2	DACK2				
13.	-DACK5	DACK5	DACK5				
13.	-DACK6	DACK6	DACK6				
13.	-DACK7	DACK7	DACK7				
13.	DREQ0	DREQ0	DREQ0				
13.	DREQ1	DREQ1	DREQ1				
13.	DREQ2	DREQ2	DREQ2				
13.	DREQ3	DREQ3	DREQ3				
13.	DREQ6	DREQ6	DREQ6				
13.	DREQ7	DREQ7	DREQ7				
13.	AEN	AEN	AEN				
13.	BALE	BALE	BALE				
13.	SBHE	SBHE	SBHE				
13.	REFRESH	REFRESH	REFRESH				
13.	IOR	IOR	IOR				
13.	IOW	IOW	IOW				
13.	MEMR	MEMR	MEMR				
13.	MEMW	MEMW	MEMW				
13.	SMEMR	SMEMR	SMEMR				
13.	SMEMW	SMEMW	SMEMW				
13.	IOCS16	IOCS16	IOCS16				
13.	MEMCS16	MEMCS16	MEMCS16				
13.	IOCHRDY	IOCHRDY	IOCHRDY				
13.	IOCHCK	IOCHCK	IOCHCK				
13.	TC	TC	TC				
14.	SIO_RST	SIO_RST	SIO_RST				
7.	SIO_OSC	SIO_OSC	SIO_OSC				
13.	SYS_CLK	SYS_CLK	SYS_CLK				
14.	IRRX	IRRX	IRRX				
14.	IRTX	IRTX	IRTX				
13.	IRQ3	IRQ3	IRQ3				
13.	IRQ4	IRQ4	IRQ4				
13.	IRQ5	IRQ5	IRQ5				
13.	IRQ7	IRQ7	IRQ7				
13.	IRQ9	IRQ9	IRQ9				
13.	IRQ10	IRQ10	IRQ10				
13.	IRQ11	IRQ11	IRQ11				
13.	IRQ14	IRQ14	IRQ14				
13.	IRQ15	IRQ15	IRQ15				
5.14.	SEL100/66/83#	SEL100/66/83#	SEL100/66/83#				
13.	-XOE	-XOE	-XOE				

<b>Gemlight Computer Ltd.</b>			
Title	SOUTH BRIDGE VT82C886-B, USB, KB & FDD CONNECTORS		
Size	Document Number	GMB-6295	Rev
B			B
Date:	Wednesday, April 05, 2000	Sheet	6 of 19

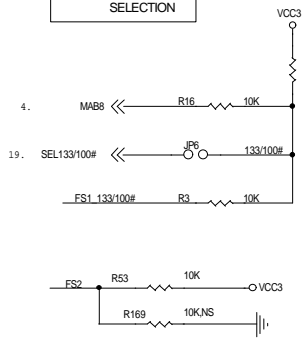
# CLOCK GENERATOR & BUFFER



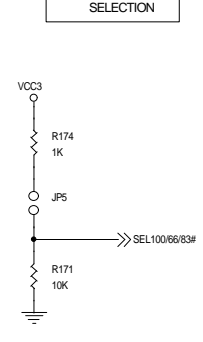
CPUCLK5	C35	10P
HCLK	C36	10P
SPCLK	C38	10P.NS
NPCLK	C39	10P.NS
PCICLK1	C40	10P.NS
PCICLK2	C41	10P.NS
PCICLK3	C42	10P.NS
PCICLK4	C43	10P.NS
APCLK	C45	10P.NS
SIO_OSC	C46	10P.NS
USBCLK	C47	10P.NS
ISA_OSC	C48	10P.NS
DCLK4	C50	10P.NS
DCLK3	C51	10P.NS
DCLK2	C52	10P.NS
DCLK1	C53	10P.NS
DCLK8	C54	10P.NS
DCLK7	C55	10P.NS
DCLK6	C56	10P.NS
DCLK5	C57	10P.NS
DCLK9	C58	10P.NS
DCLK10	C59	10P.NS
DCLK11	C60	10P.NS
DCLK12	C61	10P.NS
I2CD1	C62	10P.NS
I2CD2	C63	10P.NS



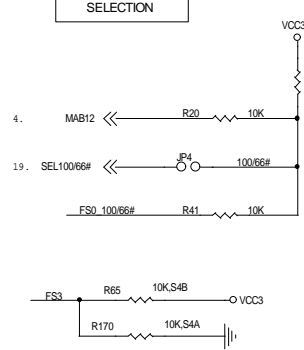
JP6: 133/100MHz SELECTION



JP5: 100/66/83MHz SELECTION



JP4: 100/66MHz SELECTION



FS3	FS2	8/FS1	12/FS0	JP6	JP5	JP4	Host Clock	AGP/HCLK	CPU
0	1	1	0	open	open	Close	83M	2/3	Force 66M to 83M
0	1	0	1	Close	Close	open	100M	2/3	Force 66M to 100M
0	1	1	1	open	open	open	133M	1/2	Force 100M to 133M
				Auto	Close	Close	66/100/133	Auto	Auto-detect 66/100/133M

Selective Group: 4  
S4A----83M HCLK Enable!  
S4B----83M CLK Disable!

**Gemlight Computer Ltd.**

Title: CLOCK SYNTHESIZER

Size B Document Number GMB-6295 Rev B

Date: Wednesday, April 05, 2000 Sheet 7 of 19

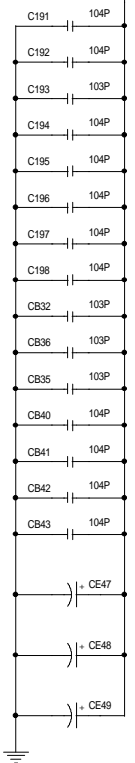
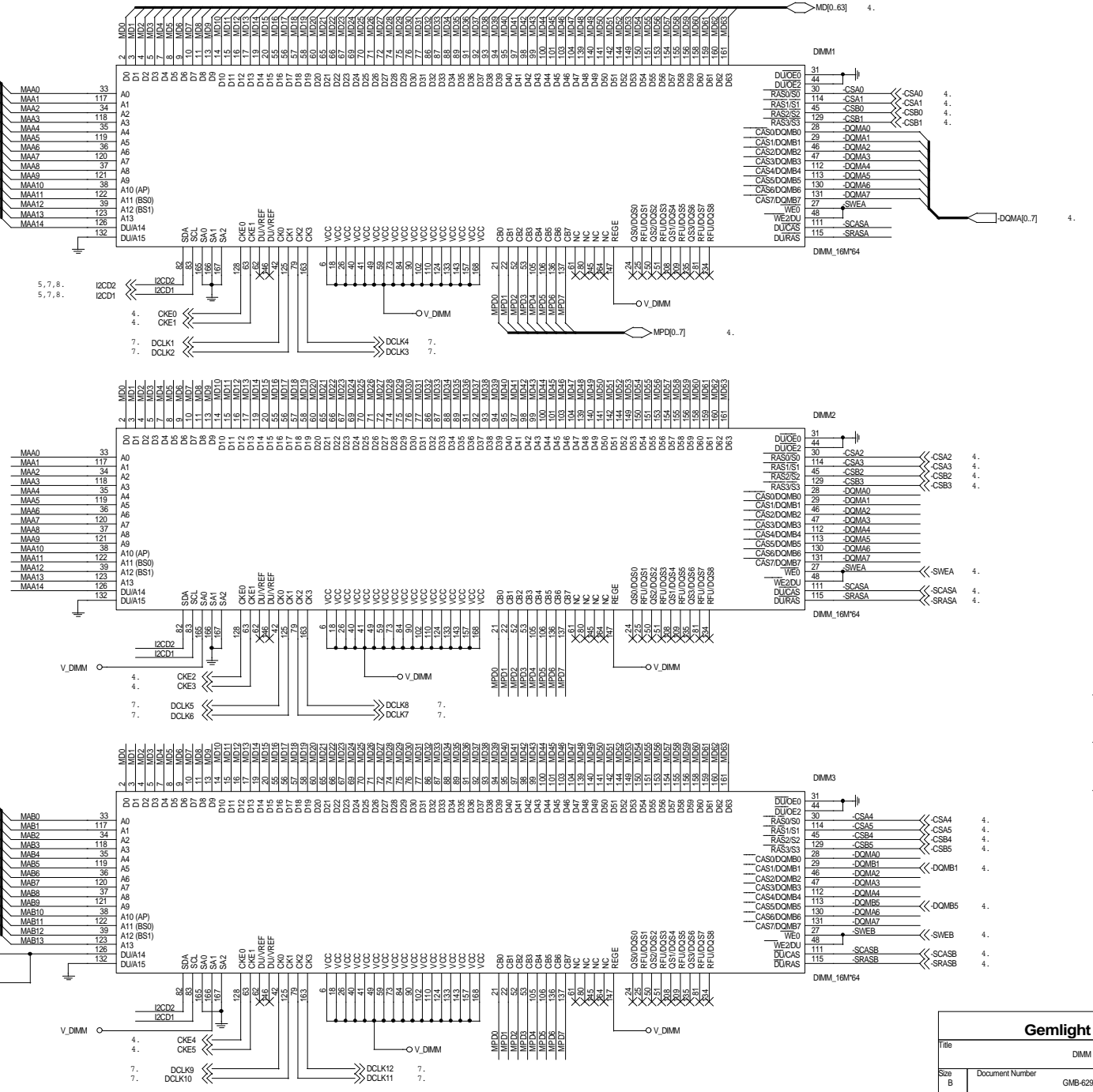
**DIMM1**  
ADDRESS: 1010000B

**DIMM2**  
ADDRESS: 1010001B

**DIMM3**  
ADDRESS: 1010010B

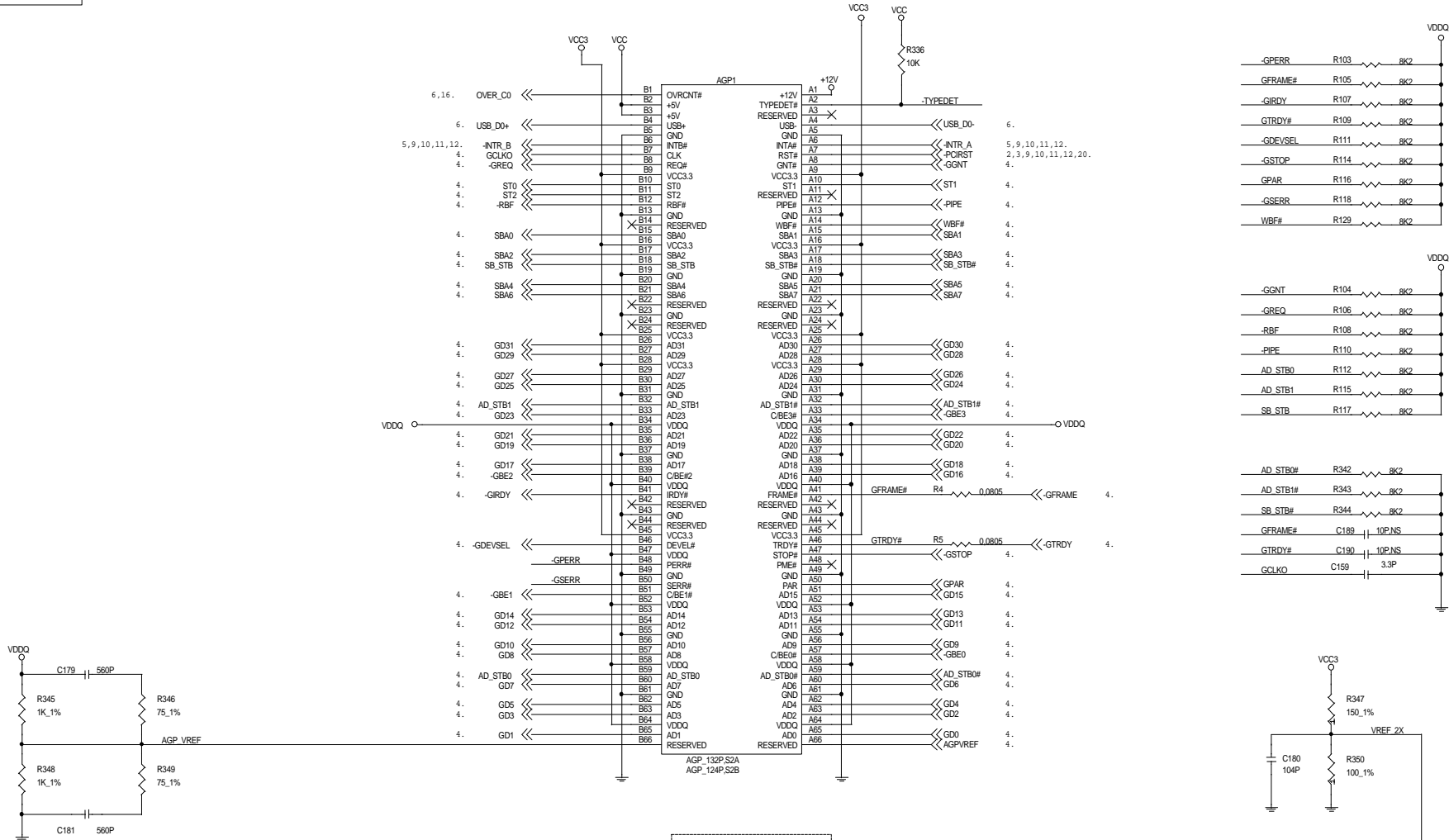
Selective Group: 2  
S2A----For 510 pin 694X;  
S2B----For 502 pin 693A.

MAB14 ← R334 ← 0.52A  
MAA14 ← R335 ← 0.52B

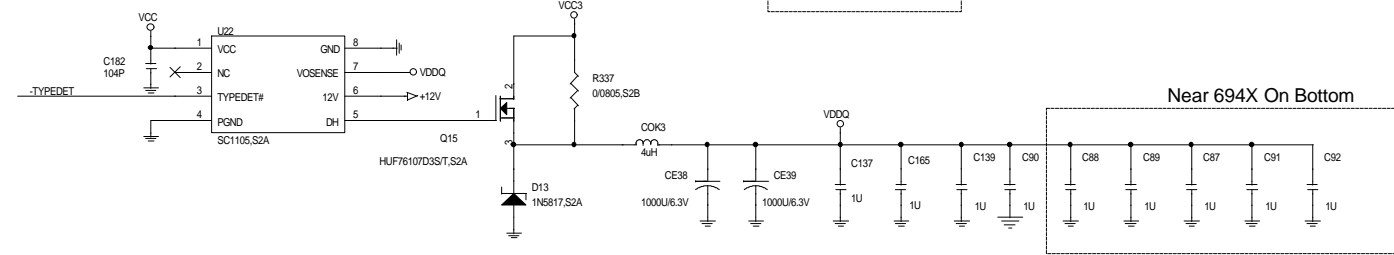
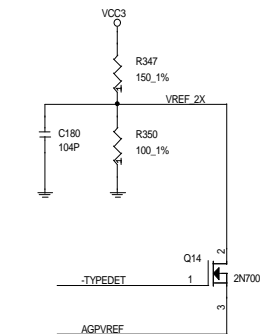




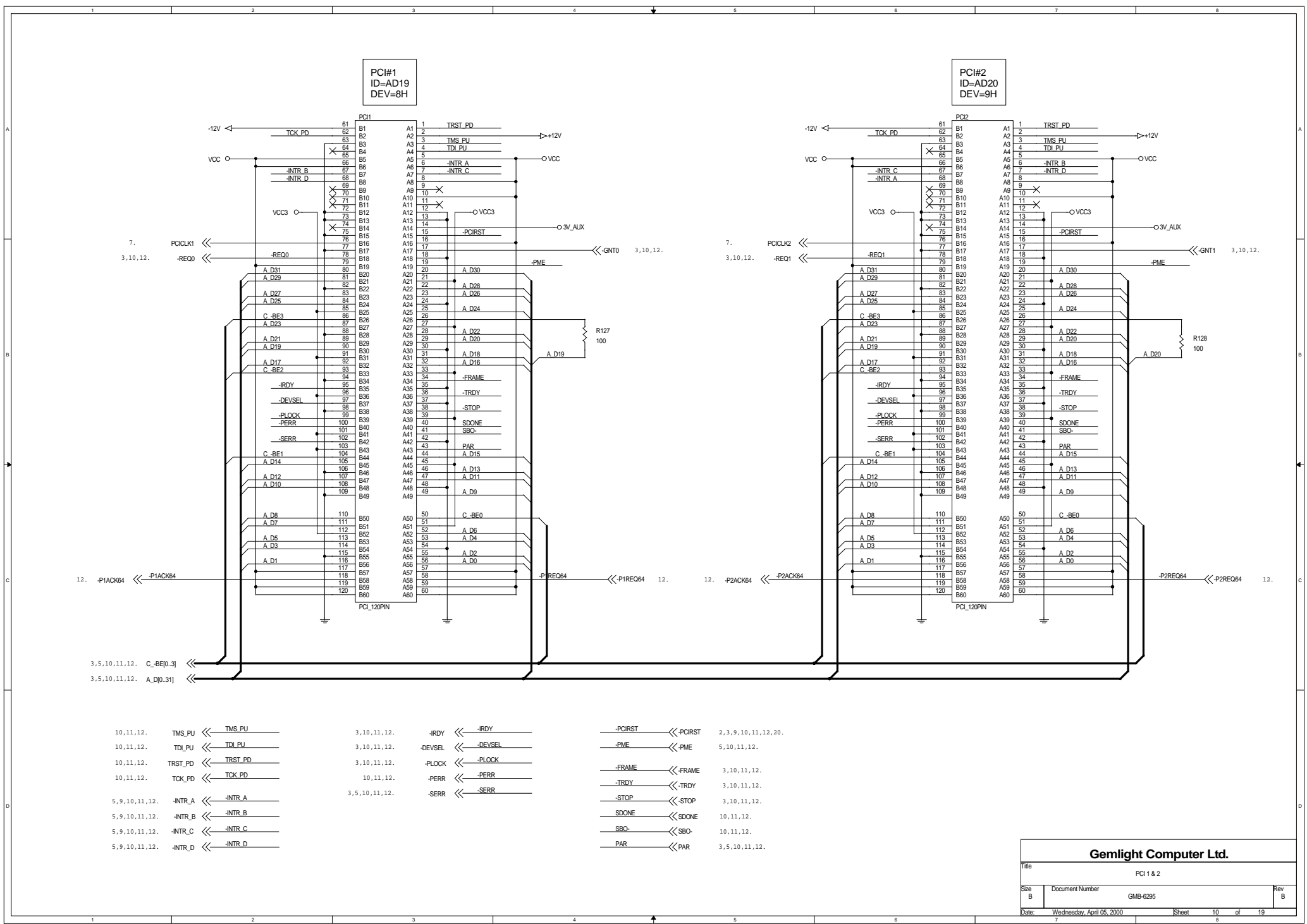
# AGP SLOT



Selective Group: 2  
 S2A----For 510 pin 694X;  
 S2B----For 502 pin 693A.



<b>Gemlight Computer Ltd.</b>		
Title: AGP		
Size B	Document Number: GMB-6295	Rev B
Date: Wednesday, April 05, 2000	Sheet 9	of 19



PCI#1  
ID=AD19  
DEV=8H

PCI#2  
ID=AD20  
DEV=9H

7. PCI CLK1 <<< -REQ0 <<< 3,10,12.

7. PCI CLK2 <<< -REQ1 <<< 3,10,12.

12. -P1ACK64 <<< -P1ACK64

12. -P2ACK64 <<< -P2ACK64

3,5,10,11,12. C\_BE[0..3] <<<  
3,5,10,11,12. A\_D[0..31] <<<

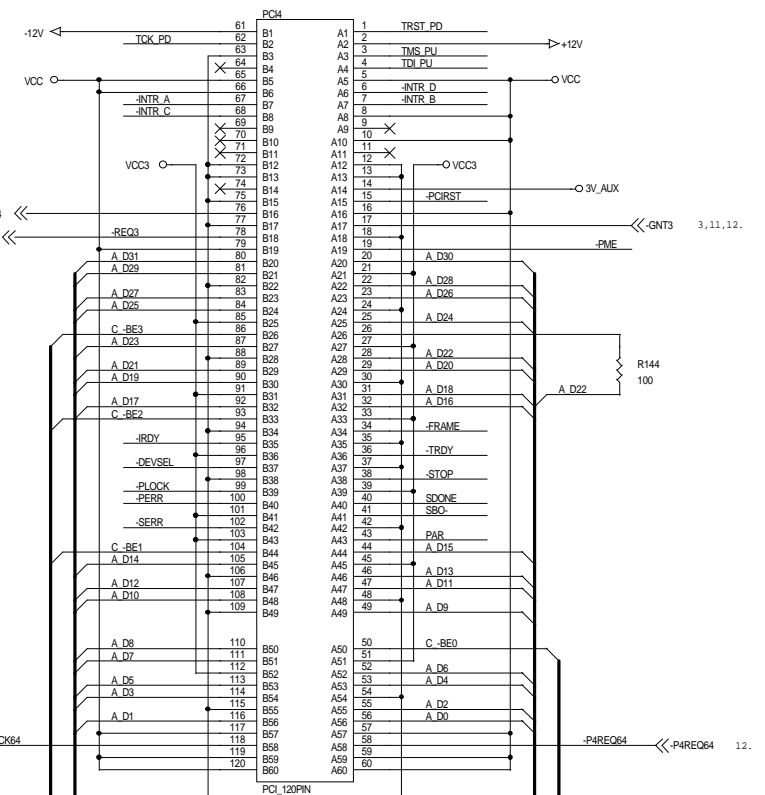
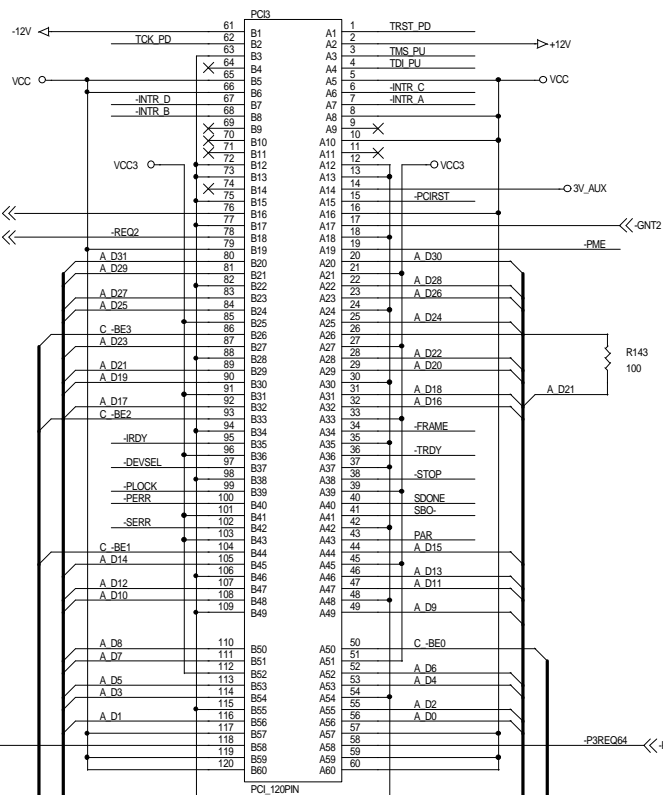
10,11,12. TMS\_PU <<< TMS\_PU 3,10,11,12. -IRDY <<< -IRDY  
10,11,12. TDI\_PU <<< TDI\_PU 3,10,11,12. -DEVSEL <<< -DEVSEL  
10,11,12. TRST\_PD <<< TRST\_PD 3,10,11,12. -PLOCK <<< -PLOCK  
10,11,12. TCK\_PD <<< TCK\_PD 10,11,12. -PERR <<< -PERR  
5,9,10,11,12. -INTR\_A <<< -INTR\_A 3,5,10,11,12. -SERR <<< -SERR  
5,9,10,11,12. -INTR\_B <<< -INTR\_B  
5,9,10,11,12. -INTR\_C <<< -INTR\_C  
5,9,10,11,12. -INTR\_D <<< -INTR\_D

2,3,9,10,11,12,20. -PCIRST <<< -PCIRST  
5,10,11,12. -PME <<< -PME  
3,10,11,12. -FRAME <<< -FRAME  
3,10,11,12. -TRDY <<< -TRDY  
3,10,11,12. -STOP <<< -STOP  
10,11,12. -SDONE <<< -SDONE  
10,11,12. -SBO- <<< -SBO-  
3,5,10,11,12. -PAR <<< -PAR

<b>Gemlight Computer Ltd.</b>			
Title PCI 1 & 2			
Size B	Document Number GMB-6295	Rev B	
Date: Wednesday, April 05, 2000	Sheet 10	of 19	

PCI#3  
ID=AD21  
DEV=AH

PCI#4  
ID=AD22  
DEV=BH



7. PCICLK3  
3,11,12. -REQ2

7. PCICLK4  
3,11,12. -REQ3

12. -P3ACK64  
-P3REQ64

12. -P4ACK64  
-P4REQ64

3,5,10,11,12. C\_BE[0..3]  
3,5,10,11,12. A\_D[0..31]

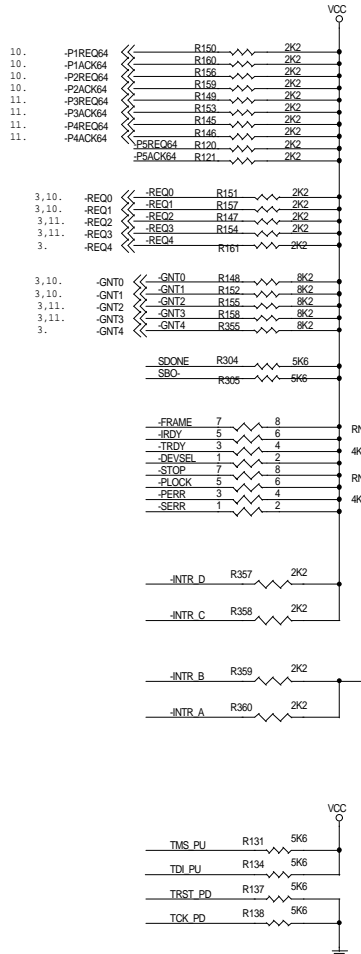
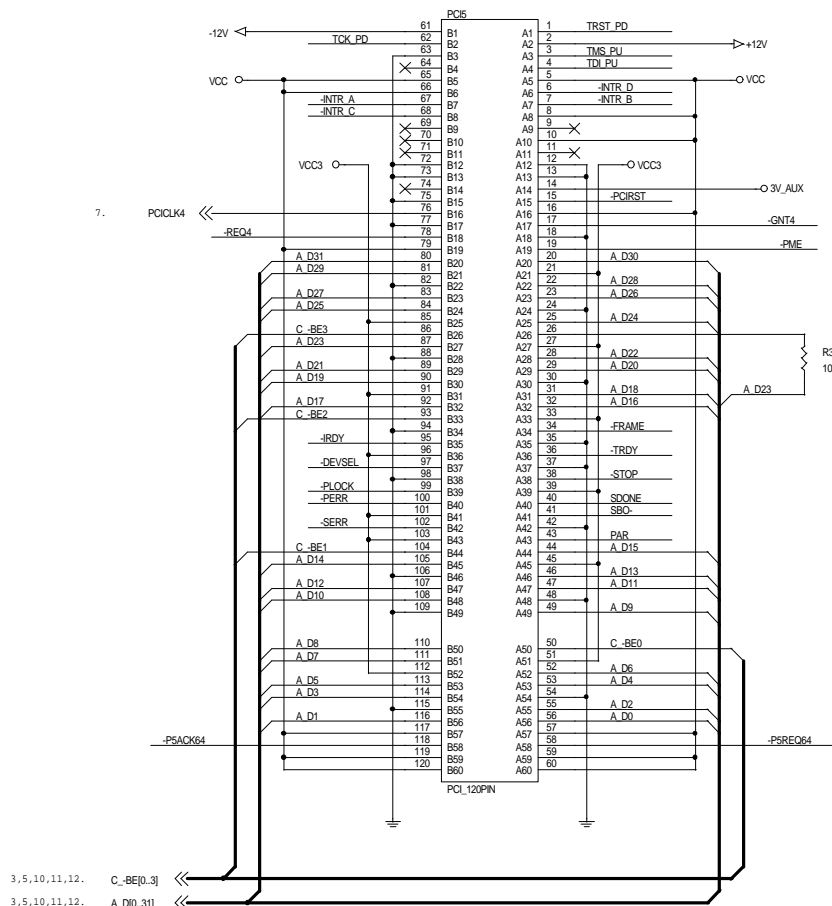
10,11,12. TMS\_PU << TMS\_PU  
10,11,12. TDI\_PU << TDI\_PU  
10,11,12. TRST\_PD << TRST\_PD  
10,11,12. TCK\_PD << TCK\_PD  
5,9,10,11,12. -INTR\_A << -INTR\_A  
5,9,10,11,12. -INTR\_B << -INTR\_B  
5,9,10,11,12. -INTR\_C << -INTR\_C  
5,9,10,11,12. -INTR\_D << -INTR\_D

3,10,11,12. -IRDY << -IRDY  
3,10,11,12. -DEVSEL << -DEVSEL  
3,10,11,12. -PLOCK << -PLOCK  
3,5,10,11,12. -PERR << -PERR  
3,5,10,11,12. -SERR << -SERR

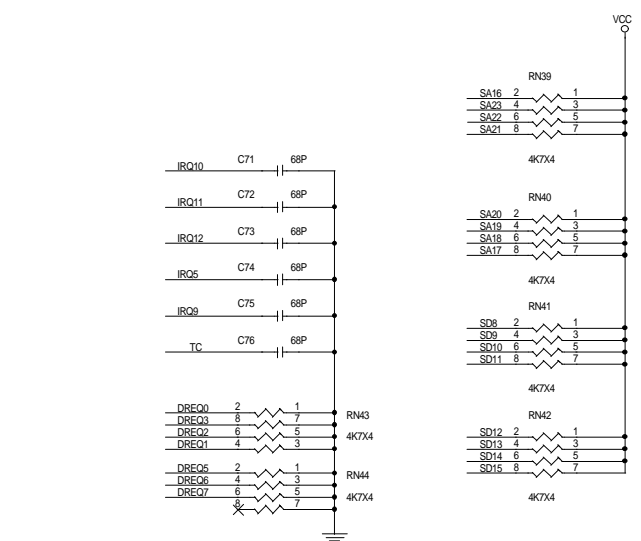
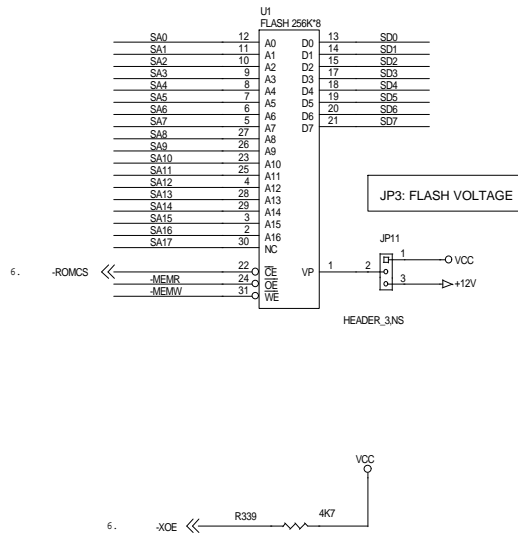
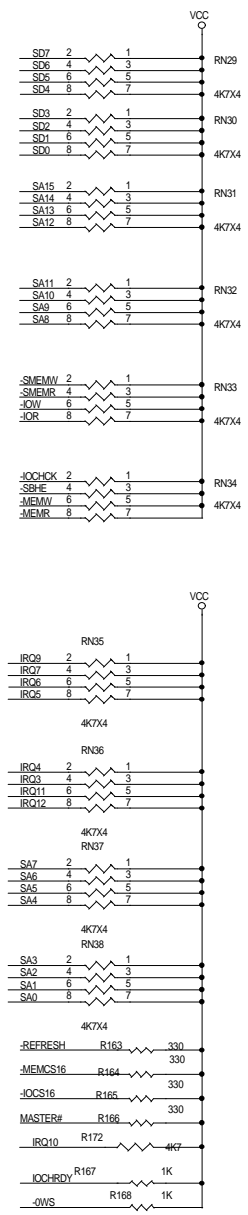
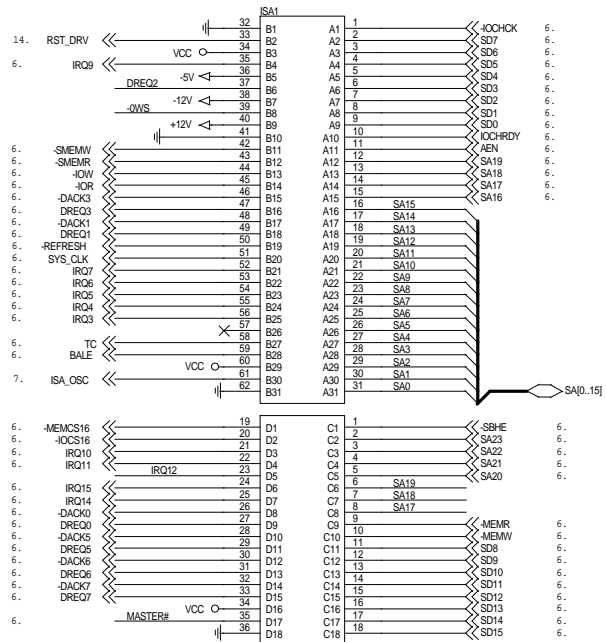
-PCIRST << -PCIRST 3,9,10,11,12,15.  
-PME << -PME 5,10,11,12.  
-FRAME << -FRAME 3,10,11,12.  
-TRDY << -TRDY 3,10,11,12.  
-STOP << -STOP 3,10,11,12.  
-SDONE << -SDONE 10,11,12.  
-SBO << -SBO 10,11,12.  
-PAR << -PAR 3,5,10,11,12.

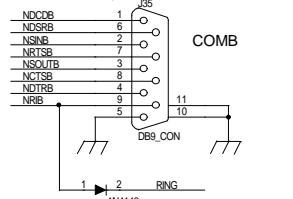
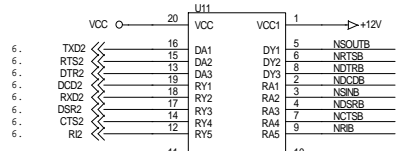
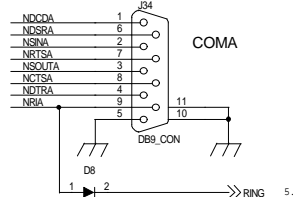
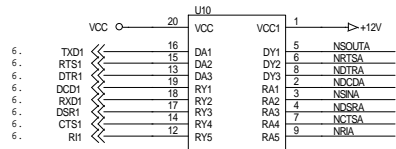
<b>Gemlight Computer Ltd.</b>		
Title	PCI 3 & 4	
Size	Document Number	Rev
B	GMB-6295	B
Date:	Wednesday, April 05, 2000	Sheet 11 of 19

PCI#5  
ID=AD23  
DEV=CH

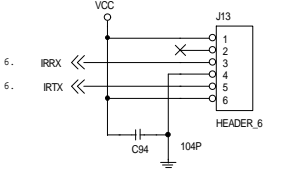


- |               |         |    |         |               |         |    |         |                    |         |    |         |          |
|---------------|---------|----|---------|---------------|---------|----|---------|--------------------|---------|----|---------|----------|
| 10,11,12.     | TMS_PU  | << | TMS_PU  | 3,5,10,11,12. | -IRDY   | << | -IRDY   | 2,3,9,10,11,12,20. | -PCIRST | << | -PCIRST | 5,12,20. |
| 10,11,12.     | TDI_PU  | << | TDI_PU  | 3,5,10,11,12. | -DEVSEL | << | -DEVSEL | 5,10,11,12.        | -PME    | << | -PME    |          |
| 10,11,12.     | TRST_PD | << | TRST_PD | 3,10,11,12.   | -PLOCK  | << | -PLOCK  | 3,5,10,11,12.      | -FRAME  | << | -FRAME  |          |
| 10,11,12.     | TCK_PD  | << | TCK_PD  | 10,11,12.     | -PERR   | << | -PERR   | 3,5,10,11,12.      | -TRDY   | << | -TRDY   |          |
| 5,9,10,11,12. | -INTR_A | << | -INTR_A | 3,5,10,11,12. | -SERR   | << | -SERR   | 3,5,10,11,12.      | -STOP   | << | -STOP   |          |
| 5,9,10,11,12. | -INTR_B | << | -INTR_B |               |         |    |         | 10,11,12.          | -SDONE  | << | -SDONE  |          |
| 5,9,10,11,12. | -INTR_C | << | -INTR_C |               |         |    |         | 10,11,12.          | -SBO    | << | -SBO    |          |
| 5,9,10,11,12. | -INTR_D | << | -INTR_D |               |         |    |         | 3,5,10,11,12.      | -PAR    | << | -PAR    |          |

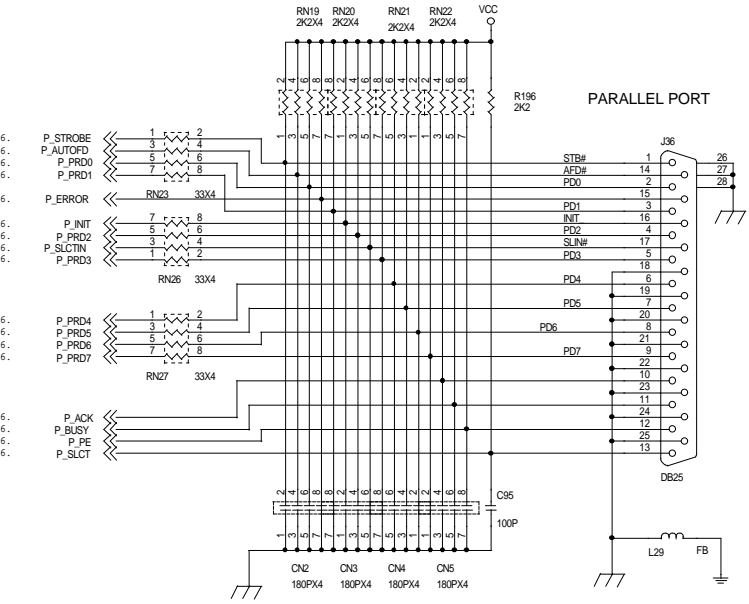




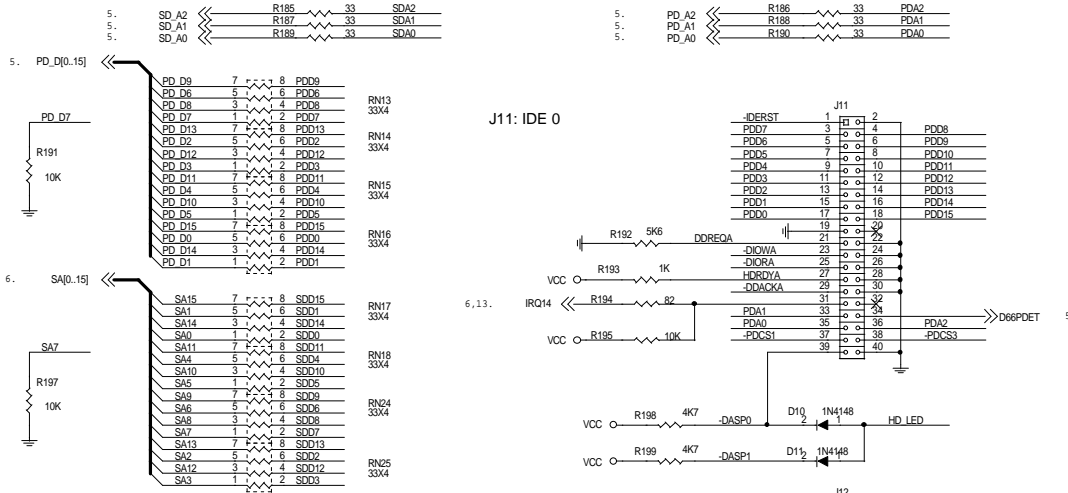
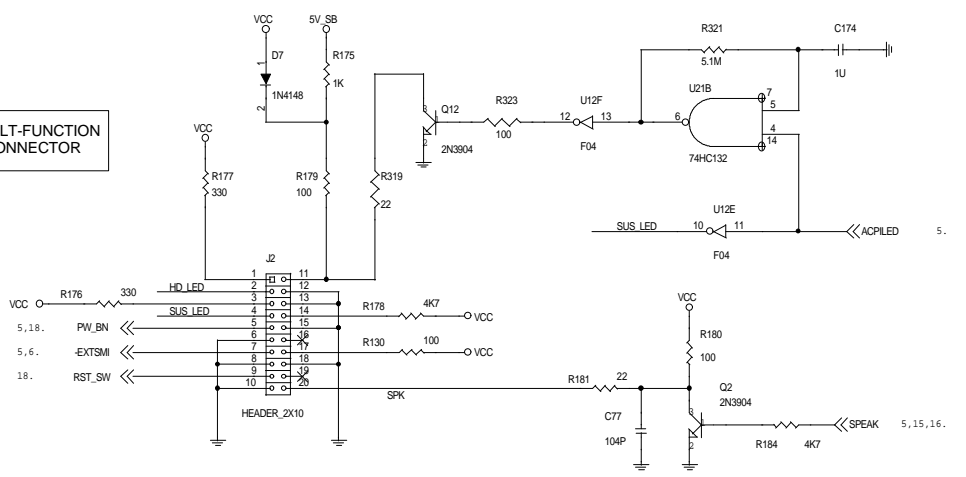
**IR CONNECTORS**



**PARALLEL PORT**



**J2: MULT-FUNCTION CONNECTOR**



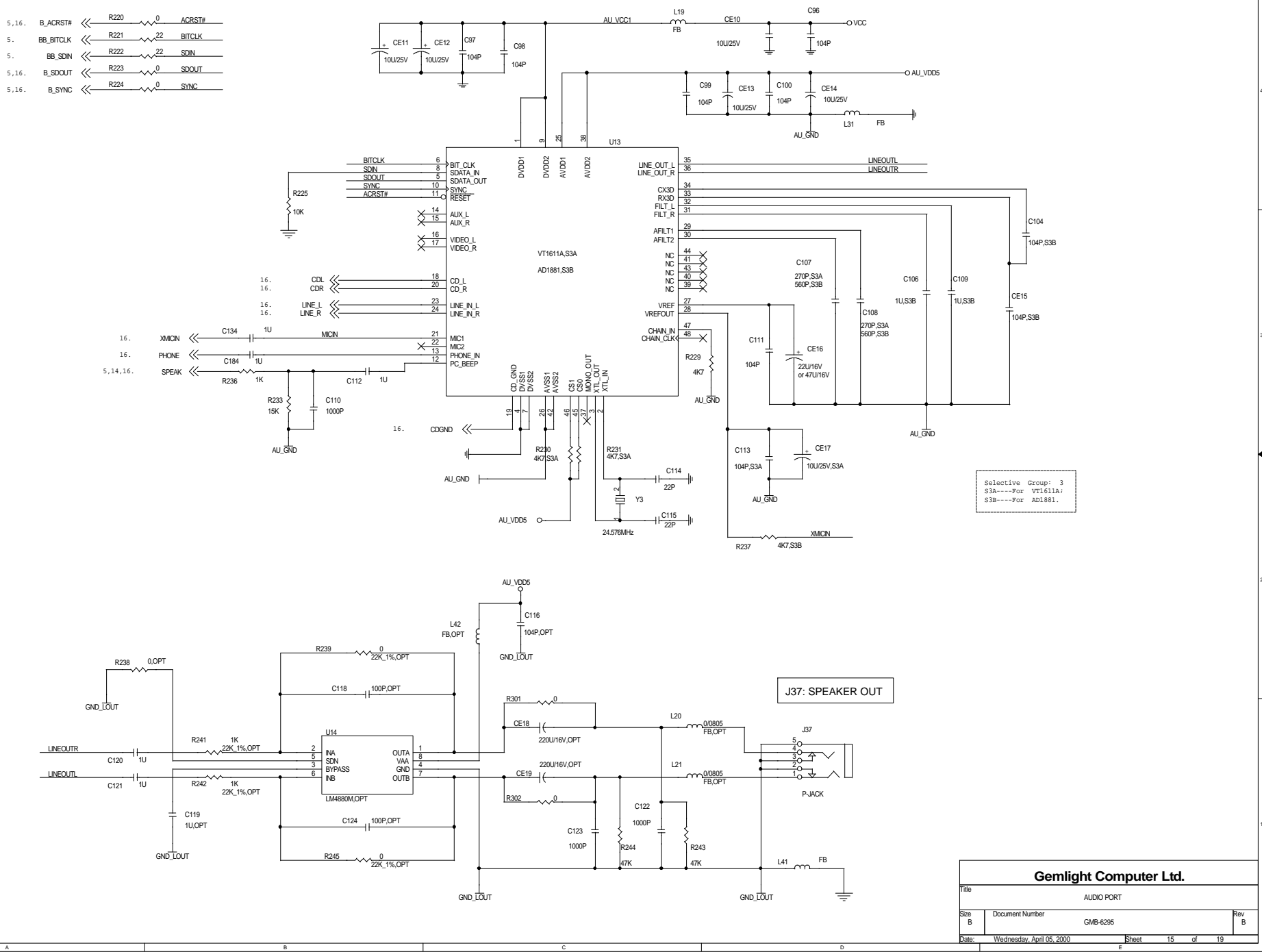
**Gemlight Computer Ltd.**

Title: COM, PR, RT, IRI, MULTI-FUNCTION CONNECTOR & IDE PORTS

Size: B Document Number: GMB-6295 Rev: B

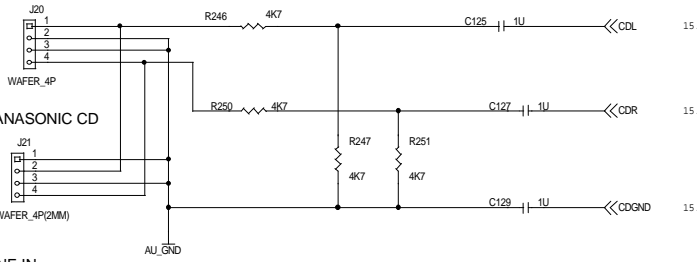
Date: Wednesday, April 05, 2000 Sheet: 14 of 19

- 5,16. B\_ACRST# << R220 0 ACRST#
- 5. BB\_BITCLK << R221 22 BITCLK
- 5. BB\_SDIN << R222 22 SDIN
- 5,16. B\_SDOUT << R223 0 SDOUT
- 5,16. B\_SYNC << R224 0 SYNC

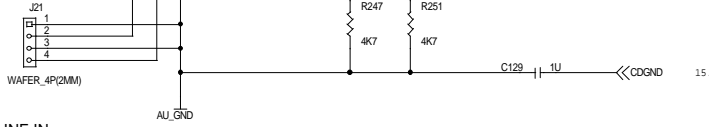


Selective Group: 3  
S3A---For VT1611A;  
S3B---For AD1861.

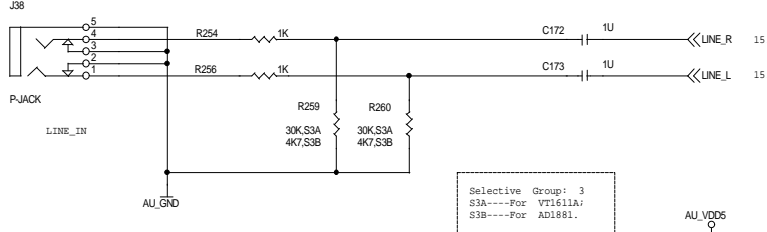
J20: SONY CD IN



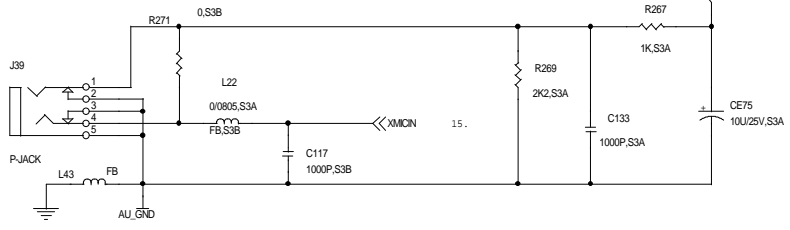
J21: PANASONIC CD



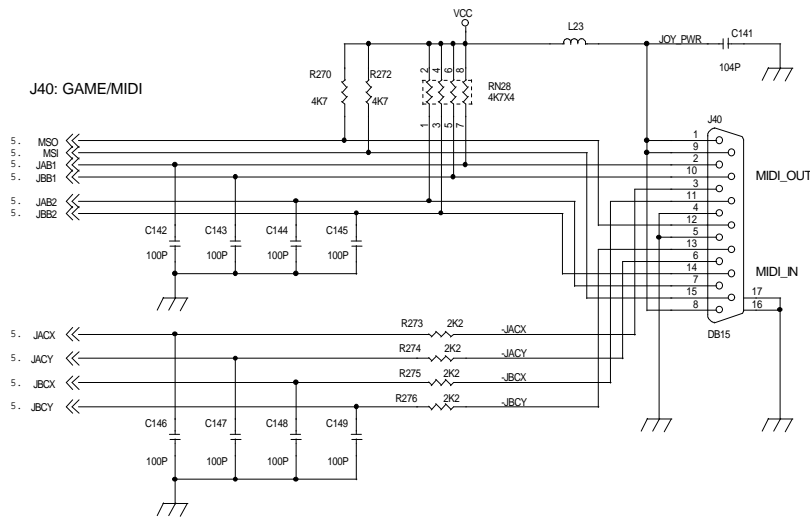
J38: LINE IN



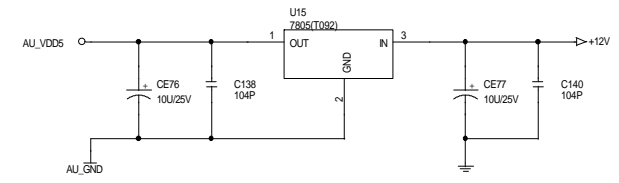
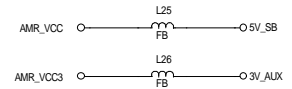
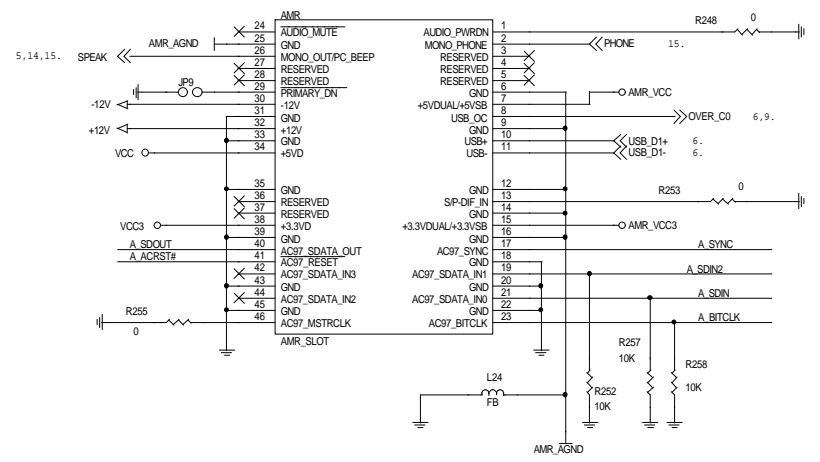
J39: MIC IN



J40: GAME/MIDI

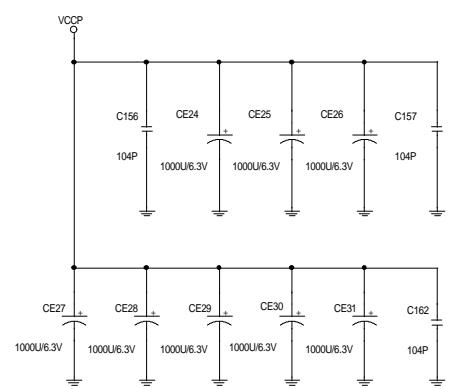
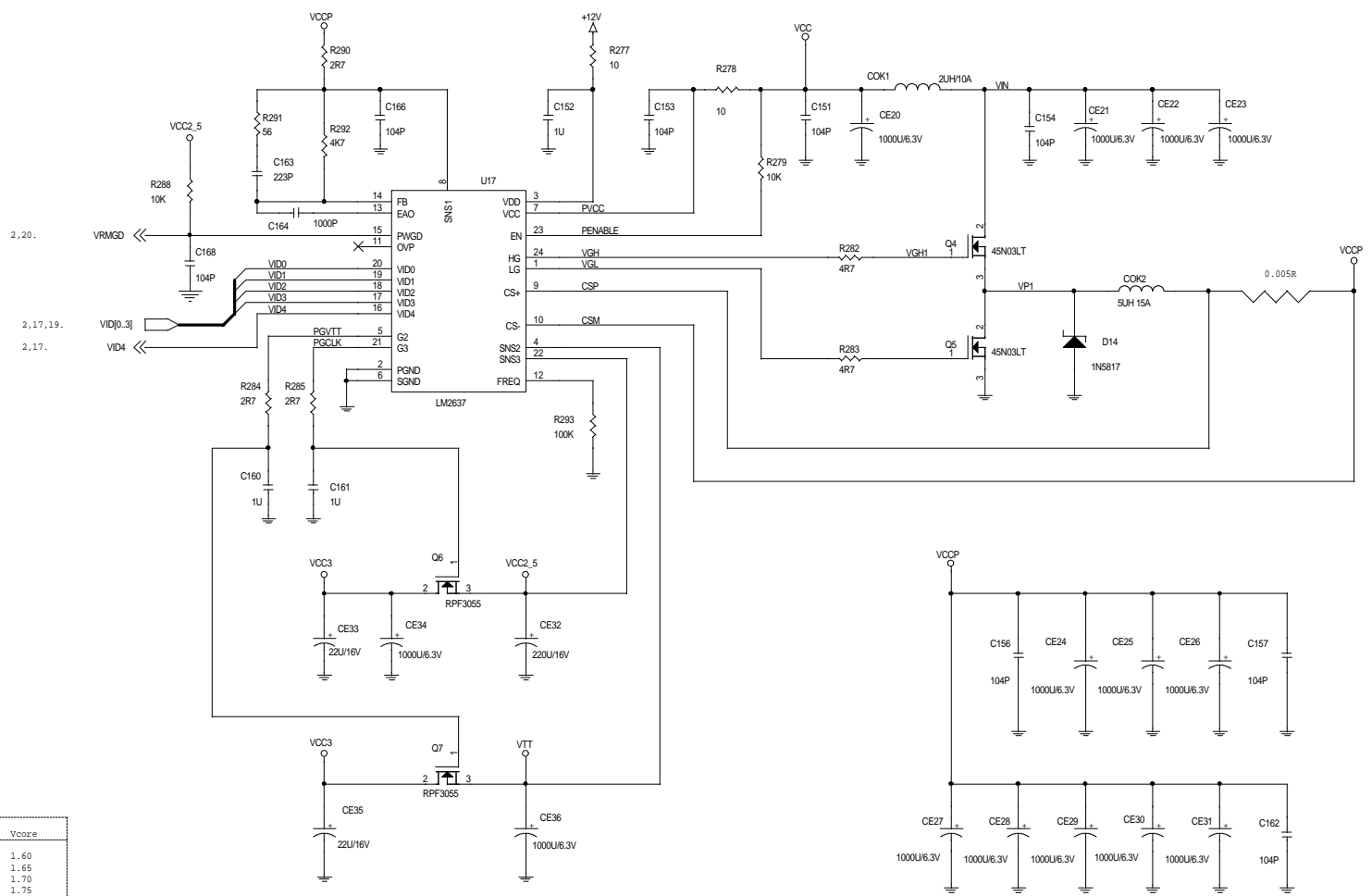


AUDIO/MODEM RISER SLOT



<b>Gemlight Computer Ltd.</b>		
Title	AUDIO IN & GAME PORT	
Size	Document Number	Rev
B	GMB-6295	B
Date:	Wednesday, April 05, 2000	Sheet 16 of 19





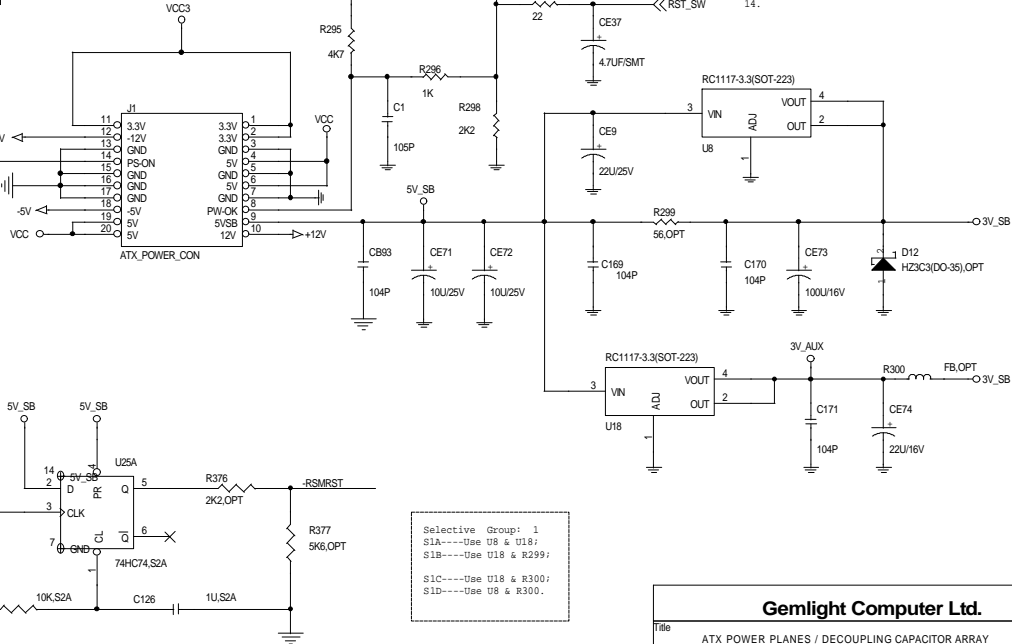
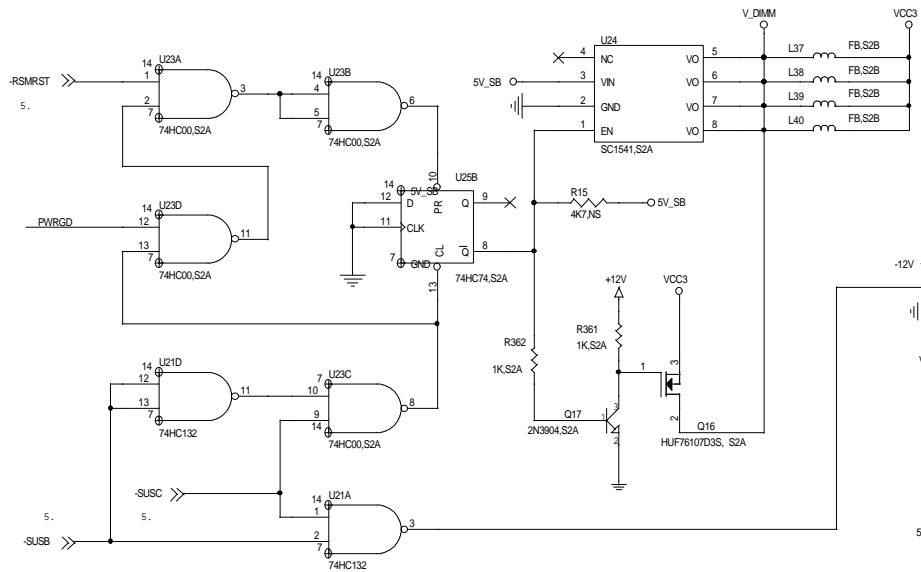
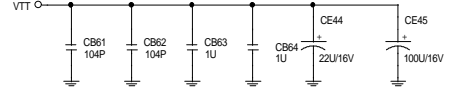
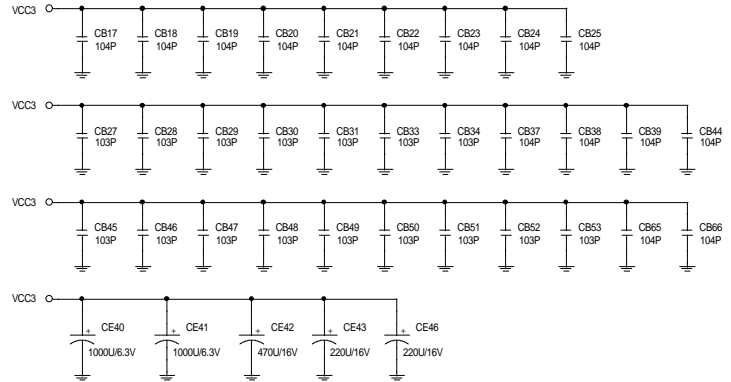
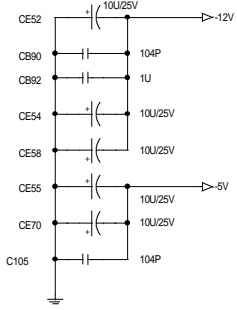
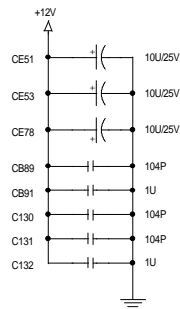
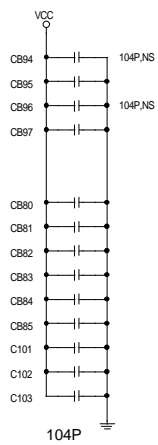
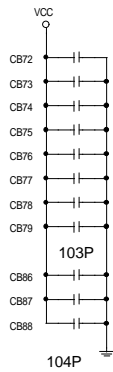
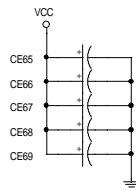
VID4	VID3	VID2	VID1	VID0	Vcore
0	1	0	0	1	1.60
0	1	0	0	0	1.65
0	0	1	1	1	1.70
0	0	1	1	0	1.75
0	0	1	0	1	1.80
0	0	1	0	0	1.85
0	0	0	1	1	1.90
0	0	0	1	0	1.95
0	0	0	0	1	2.00
0	0	0	0	0	2.05
1	1	1	1	1	2.00
1	1	1	1	0	2.10
1	1	1	0	1	2.20
1	1	1	0	0	2.30
1	1	0	1	1	2.40
1	1	0	1	0	2.50
1	1	0	0	1	2.60
1	1	0	0	0	2.70
1	0	1	1	1	2.80

**Gemlight Computer Ltd.**

Title: DC to DC CONVERTER CIRCUITS

Size B Document Number GMB-6295 Rev B

Date: Wednesday, April 05, 2000 Sheet 17 of 19



Selective Group: 4  
 S4A----For Support STR;  
 S4B----For Not Support.

Selective Group: 1  
 S1A----Use U8 & U18;  
 S1B----Use U18 & R299;  
 S1C----Use U18 & R300;  
 S1D----Use U8 & R300.

<b>Gemlight Computer Ltd.</b>		
Title ATX POWER PLANES / DECOUPLING CAPACITOR ARRAY		
Size B	Document Number GMB-6295	Rev B
Date: Wednesday, April 05, 2000	Sheet 18	of 19

