

V4P895GRN (4x72pin) MB CPU Jump Setting selection

CPU selection (JP8, JP9, JP15, JP16, JP17, JP18, JP19, JP20, JP23, JP26, JP27, JP28, JP29, JP30, JP31, JP32, JP36)

CPU TYPE	486SX	486DX	SL-486SX	SL-486DX	INTEL 486DX4	AMD 3.3V CPU
JP8	2-3	2-3	2-3	2-3	2-3	2-3
JP9	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN
JP15	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN
JP16	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN
JP17	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN
JP18	OPEN	OPEN	OPEN	OPEN	OPEN	1-2 : 3X CLK 2-3 : 2X CLK
JP19	1-2	1-2	1-2	1-2	1-2	1-2
JP20	OPEN	OPEN	OPEN	OPEN	OPEN: 3X CLK 1-2 : 2X CLK 2-3 : 2.5 CLK	OPEN
JP23	1-2	1-2	2-3	2-3	2-3	1-2
JP26	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN
JP27	OPEN	OPEN	CLOSE	CLOSE	CLOSE	OPEN
JP28	OPEN	OPEN	OPEN	OPEN	OPEN	OPEN
JP29	OPEN	1-2	OPEN	1-2	1-2	1-2
JP30	1-2	1-2	1-2	1-2	1-2	1-2
JP31	2-3	1-2	2-3	1-2	1-2	1-2
JP32	OPEN	2-3	OPEN	2-3	2-3	2-3
JP36	OPEN	OPEN	OPEN	OPEN	CLOSE	OPEN

CPU TYPE	AMD 3.3V Enhanced CPU	AMD 3.3V X5-133 CPU	P24D (Intel w/ write back cache)	CYRIX 486 DX CPU CYRIX 5X86 3.3V CPU CYRIX M1 3.3V CPU
JP8	2-3	2-3	2-3	5V CPU : 2-3 3V CPU : 1-2
JP9	OPEN	OPEN	OPEN	OPEN
JP15	CLOSE	CLOSE	CLOSE	OPEN
JP16	CLOSE	CLOSE	CLOSE	OPEN
JP17	2-3	2-3	2-3	1-2
JP18	1-2 : WB 2-3 : WT	1-2	1-2 : WB 2-3 : WT	OPEN
JP19	1-2	1-2	1-2	2-3
JP20	OPEN	1-2 : 4X CLK	OPEN	OPEN
JP23	2-3	2-3	2-3	1-2
JP26	CLOSE	CLOSE	CLOSE	OPEN
JP27	CLOSE	CLOSE	CLOSE	OPEN
JP28	OPEN	OPEN	OPEN	1-2 : M7 1X CLK 2-3 : M7 2X CLK
JP29	1-2	1-2	1-2	1-2
JP30	1-2	1-2	1-2	1-2
JP31	1-2	1-2	1-2	1-2
JP32	2-3	2-3	2-3	2-3
JP36	OPEN	OPEN	OPEN	OPEN

"WB" stand for Write back, "WT" stand for Write Through