## 7KTAR

## **User's Manual Version 1.1**

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### Introduction

## System @rview

This amual was written to help you start using this product as quickly and snothly as possbile. Inside you will find adequate explanations to solve out problem In order for this reference aterial to be of greatest use, refer to the "ex panded table of contents" to find relevant topics. This board incorporates the systemoard, I/O, and PCI IDE into one board that provides a total PC solution. The ainboard, AMD Athlon and Duron processor based PC ATX systemupport single processors with ISA Bus, PCI Local Bus, and AGP Bus to support upgrades to your systemerforance. It is ideal for mti-tasking and fully supports MS-DOS, Windows, Windows NT, Windows 2000, Novell, OS/2, Windows95/98, UNIX, SCO UNIX etc. This **am**ual also explains how to install the **ai**nboard for operation, and how to setup your CMOS configuration with the BIOS setup program

## 1.Motherboard Description

### 1.1 Features

### 1.1.1 Hardare

#### CPU

- -Single AMD Socket 462 Athlon/Duron.
- -200MHz SysterInterface speed.

### Speed

- -Supports 33MHZ PCI Bus speed.
- -Supports 4X AGP Bus.

### **DRAM Memory**

- -Supports 8/16/32/64....MB DIMM ordule socket.
- -Supports Synchronous DRAM(3.3V)
- -Supports a aximum size of 1.536GB with SDRAM.

#### Shadow RAM

-A mary controller provide shadow RAM and supports
 8-bit ROM BIOS.

#### Gen Function

- -Support power amageent operation via BIOS.
- -Power down tien from to 15 ins.
- -Wakes from ower saving sleep onde at the press of any key or any onuse activity.

#### **Bus Slots**

- -Provide one AGP slot and one AMR.
- -Five 32-bit PCI bus and one 16-bit ISA.

#### Universal Serial Bus

-Supports two back Universal Serial Bus(USB)Ports and two front Universal Serial Bus(USB)Ports.

#### Hdware Monitor Function

- -CPU Fan Speed Monitor.
- -System (Optional) and CPU Teperature Monitor.
- -System Voltage Monitor.

## 2 / Chapter 1 Motherboard Description

### Flash Memory

- -Support flash emry.
- -support ESCD Function.

#### IDE Bulit-in @Board

- -Supports four IDE hard disk drives.
- -Supports PIO Mode 4, Master Mode, high perforance hard disk drives.
- -Support Ultra DMA 33/66/100 Bus Master Mode.

### (Ultra DMA 100 only for 7KAP)

- -Supports IDE interface with CD-ROM.
- -Supports high capacity hard disk drives.
- -Support LBA mde.

PCI-Based AC 97 Digital Audio Processor (Optional)

- -AC 97 2.1 interface.
- -16 channels of high-quality same rate conversion.
- -16x8 channel digital inner.
- -Stereo 10 band graphic equalizer.
- -Sound Blaster and Sound Blaster Pro emation.
- -S/PDIF output (allows standard interface to consum electronics).
- -64-voice wavetable.

### I/Bulit-in @Board

- -Supports one miti-orde Parallel Port.
- (1)Standard &Bidirection Parallel Port
- (2)Enhanced Parallel Port(EPP)
- (3)Extended Capabilities Port
- -Supports two serial ports,16550 UART.
- -Supports one Infrared transition(IR).
- -Supports PS/2 muse and PS/2 Keyboard.
- -Supports 360KB, 720KB, 1.2MB, 1.44MB, and 2.88MB floppy disk drivers.

## 3 / Chapter 1 Motherboard Description

### 1.1.2 Softwre

### BIO

- -AWARD legal BIOS.
- -Supports APM 1.2.
- -Supports USB Function.
- -Supports ACPI

### **Deration System**

-Offers the highest perfor**an**ce for MS-DOS,Windows, Windows NT, Windows 2000, Novell, OS/2, Windows95/98, UNIX, SCO UNIX etc.

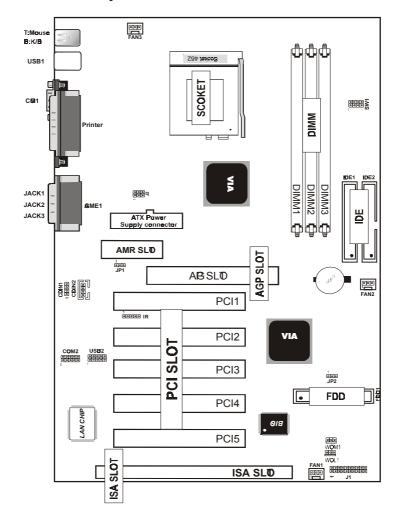
### 1.1.3 Attachments

- -HDD UDMA66/100 Cable.
- -FDD Cable.
- -Flash Meory Written for BIOS Update.
- -USB2 Cable (Optional) .
- -COM2 Cable.
- -Fully Setup Driver CD(Ghost, Anitivirus,

Adobe Acrobat...).

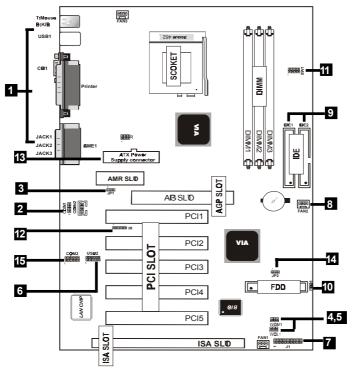
## 1.2 Motherboard Installation

## 1.2.1 Layout of Motherboard



## 5 / Chapter 1 Motherboard Description

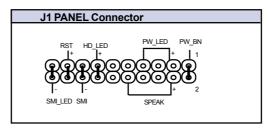
## 1.3 Motherboard Connectors



- 1.Back Pannel I/O Connectors 2.CD Audio-In Connector
- 3.AMR CODEC Function(JP1)
- 5. Wake-On-LAN Connector
- 7. Front Panel Connector
- 9. IDE Connectors
- 11.CPU Clock Selection(SW1) 12.IR Connector
- 13.ATX Power Connector
- 14.CMOS Function Select(JP2)
- 15. Front Two COM Connector(COM2)

- 4. Wake-On MODEM Connector
- 6. Front Two USB Connector
- 8.Fan connectors(Fan1/2/3)
- 10.Floppy Connector

## 1.3.1 Front Panel Connector(J1)



## **Speaker Connector (SPEAKR)**

An offboard speaker can be installed onto the otherboard as a amufacturing option. An offboard speaker can be connected to the otherboard at the front pannel connector. The speaker (onboard or offboard) provides error beep code information during the Power Self-Test when the coputer cannot use the vedio interface. The speaker is not connected to the audio subsystem and does not receive output fronthe audio subsystem

## Hrd Drive LED Connector (H LED)

This connector supplies power to the cabinet IDE activity LED. Read and write activity by devices connected to the Pri**ary** or Secondary IDE connectors will cause the LED to light up.

## SMI Suspend Switch Lead (SMI)

This allows the user to amually place the system to a suspend or or Green or where system ic activity will be instantly decreased to save electricity and expand the life of certain components when the systems not in use. This 2-pin connector (see the figure below) connects to the case-munted suspend switch. If you do not have a switch for the connector, you any use the Turbo Switch" instead since it does not have a function. SMI is activated when it detects a short to open ment and therefore leaving it shorted will not cause any problem It any require one or two pushes depending on the position of the switch. Wake-up can be controlled by settings in the BIOS but the keyboard will always allow wake-up (the SMI lead cannot wake-up the system If you want to use this connector, "Suspend Switch" in the Power Manageemt Setup of the BIOS SOFT-WARE section should be on the default setting of Enable.

## 7 / Chapter 1 Motherboard Description

## ATX Power Switch (PW\_BN)

The systemower is controlled by a mentary switch connected to this lead. Pushing the button once will switch the systemon. The systemower LED lights when the system power is on .

## Power LED Lead (PW LED)

The systemower LED lights when the systemower is on.

### SMI LED Lead (SMI LED)

The system IMI LED lights when the system uspend is on.

## Reset Switch Lead (RST)

The connector can be connected to a **num**tary SPST type switch that is normally open. When the switch is closed, the **ntherboard** resets and runs the POST.

## 1.3.2 Floppy Disk Connector(FDD1)

This connector supports the provided floppy drive ribbon cable. After connecting the single end to the board, connect the two plug on the other end to the floppy drives.

## 1.3.3 Hard Disk Connectors(IDE1/IDE2)

These connectors support the provided IDE hard disk ribbon cable. After connecting the single end to the board, connect the two plugs at the other end to your hard disk .

If you install two hard disks, you ust configure the second drive to Slave orde by setting its juper settings. BIOS now supports SCSI device or IDE CD-ROM boot up (see "HDD Sequence SCSI/IDE First" &Boot Sequence" in the BIOS Features Setup of the BIOS SOFTWARE) (Pin 20 is remved to prevent inserting in the wrong orientation when using rib bon cables with pin 20 plugged).

## 1.3.4 ATX 20-pin Powr Connector(PW1)

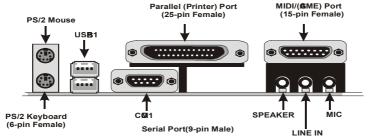
This connector supports the power button on-board. Using the ATX power supply, functions such as ModerRing Wake-Up and Soft Power Off are supported on this ntherboard. This power connector supports instant power-on functionality, which nans that the systemvill boot up instantly when the power connector is inserted on the board.

Pin	Signal	Pin	Signal
1	3.3V	2	3.3V
3	<b>IS</b> D	4	5V
5	<b>IS</b> D	6	5V
7	<b>IS</b> D	8	PW-Ø
9	5VSB	10	12V
11	3.3V	12	-12V
13	<b>IS</b> D	14	PS- <b>10</b>
15	<b>IS</b> D	16	<b>IS</b> D
17	<b>IS</b> D	18	-5V
19	5V	20	5V

## 1.3.5 Infrared Connector(IR)

After the IrDA interface is configured, files can be transferred from to protable devices such as laptops, PDAS, and printers using application software.

## 1.4 Back Pannel Connectors



## 1.4.1 PS/2 Mouse /Keyboard CON.

The otherboard provides a standard PS/2 ouse / Keyboard ini DIN connector for attaching a PS/2 ouse. You can plug a PS/2 ouse / Keyboard directly into this connector.

## 1.4.2 USB Connectors: USB1(RJ45+USB)

The otherboard provides a OHCI(Open Host Controller Interface)Universal Serial Bus Roots for attaching USB devices such as:keyboard, ouse and other USB devices. You can plug the USB devices directly into this connector.



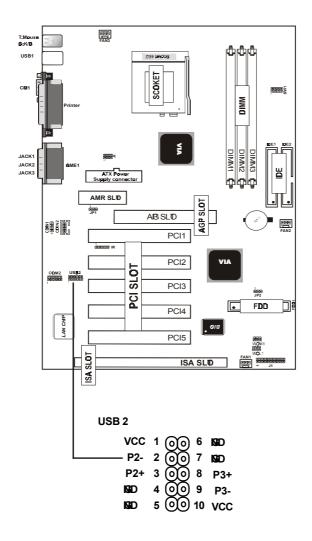
Pin	Signal
1	+5v
2	USBP0-(USBP1-)
3	USBP0+(USBP1+)
4	<b>S</b> D

**RJ-45** 



Pin	Signal
1	+5v
2	USBP0-(USBP1-)
3	USBP0+(USBP1+)
4	<b>Ø</b> D
RJ45	10/100M LAN Port (option)

## Front Two USB Connectors: USB2



## 1.5 Serial and Parallel Interface Ports

This systemons equipped with two serial ports and one parpllel port. Both types of interface ports will be explained in this chapter.

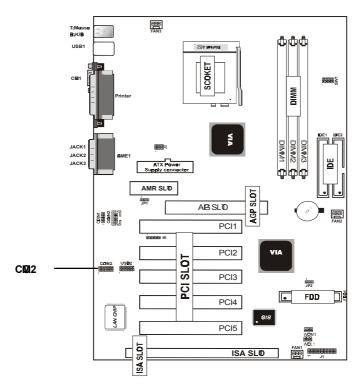
### The Serial Interfaces: CM1/CM2

The serial interface port is somtions referred to as an RS-232 port or an asynchronous commication port. Mice, printers, ordernand other peripheral devices can be connected to a serial port. The serial port can also be used to connect your coputer systemIf you wish to transfer the contents of your hard disk to another system can be accoplished by using each archine's serial port.



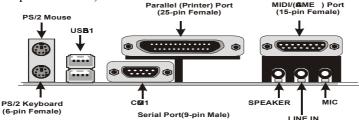
The serial port on this systemate one 9-pin connector. Somolder coputer systemand peripherals used to be equipped with only 25-pin connector. Should you need to connect your 9-pin serial port to an older 25-pin serial port, you can purchase a 9-to-25 pin adapter.

Signal	DB9 Pin	DB25 Pin
DCD	1	8
RX	2	3
TX	3	2
DTR	4	20
<b>⊗</b> D	5	7
DSR	6	6
RTS	7	4
CTS	8	5
RI	9	22



### **Parallel Interface Port**

Unlike the serial ports, parallel interface port has been standardized and should not present any difficulty interfacing peripherals to your systemSomins called Centronics port, the parallel port is alost exclusively used with printers. The parallel port on your systemas a 25-pin, DB 25 connector(see picture below).

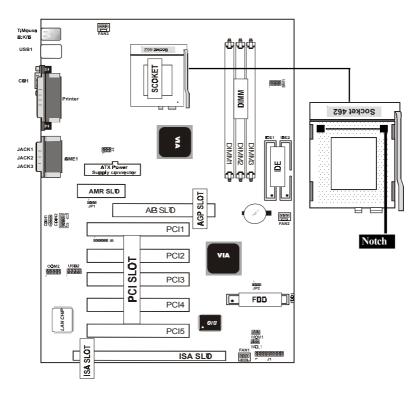


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## 1.6 CPU Installation

### 1.6.1 CPU Installation Procedure: Socket 462

- 1.Pull the lever sideways away fronthe socket then raise the lever to a 90-degree angle.
- 2.Locate Pin 1 in the socket and look for the white dot or cut edge in the CPU. Match Pin 1 with the white dot/cut edge then insert the CPU.
- 3. Press the lever down to coplete the installation.
- 4. Make sure the spec of heatsink is good enough.



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## 1.6.2 CPU Clock Selection:SW1

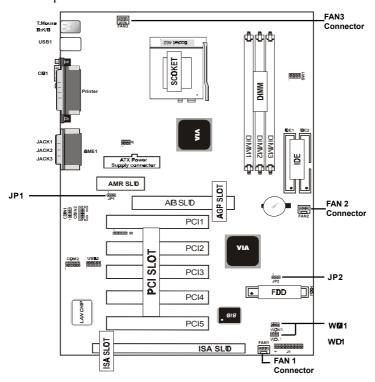
B:+ US	6B1	FANG	SCOKET	WIIG BHAM G		$\begin{array}{c} 1 \\ 2 \\ 0 \\ \end{array}$	w 4
JA	SW1	1	2	3	4	CPU(MHZ)	PCI(MHZ)
	1234	1	0	1	1	100	33.33
	1234	0	0	0	1	101	33.67
	1234	0	0	1	1	102	34.00
	1234	1	0	1	0	103	34.33
	1234	1	0	0	1	105	35.00
	1234	0	1	0	0	107	35.67
	1234	0	1	1	0	109	36.33
	123 <b>4</b>	0	1	0	1	110	36.67
	1234	0	1	1	1	111	37.00
	1234	1	1	0	0	113	37.67
	123 <b>4</b>	1	1	1	0	115	38.33
	123 <b>4</b>	1	1	0	1	117	39.00
	888 123 <b>4</b>	1	1	1	1	133.3	33.33

0:0N (Close) **1**:0FF (Open) **8** 

NOES:CPU clock speed over 100MHZ is not recommended.

## 1.7 Jumper Setting

A juper has two or ore pins that can be covered by a plastic juper cap, allowing you to select different system options.



## 1.7.1 CPU/System Fan Connectors:Fan1/3

Pin	Assignment
0 1	Ground
0 2 2	+12VDC
0 3 3	Signal

## 1.7.1 CPU/System Fan Connectors:Fan2

Pin	Assignment
0 1	Ground
0 2 2	+12VDC
<b>0</b> 3 3	Ground

## 1.7.2 Wake-@ Modem Header:WM1

Pin	Assignment
<b>ြ</b> ₁ 1	5VSB
<b> </b>    2	Ground
<b>©</b> ₃ 3	Signal

## 1.7.3 Wake-@LAN Header:WD1

Pin	Assignment
<u></u> 1	5VSB
<b> </b>    2	Ground
<b>©</b> ₃ 3	Signal

### 1.7.4 AMR Code Function: JP1

Pin	Assignment
1 - 2	On board CODEC
2 - 3	AMR Primary CODEC

## 1.7.5 CMO Function Select: JP2

Pin	Assignment		
1 - 2	Normal (Default)		
2 - 3	Clear CMOS		
ا آ	Crour Chieb		

### NOE:

## (Please followhe procedure as below clear CMS data.)

(1)Remove AC power line.(2)JP2(2-3)Closed.(3)Wait five seconds.(4)JP2(1-2) Closed.(5)AC Power on.(6)Reset your desired password or clear CMO data.

## 1.8 DRAM Installation

### 1.8.1 **DIMM**

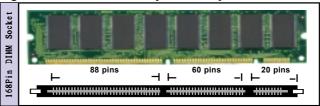
DRAM Access Tim3.3V Unbuffered SDRAM/ PC66/ PC100 and PC133 Type required.

DRAM Type:8MB,16MB,32MB,64MB,128MB,256MB ,512MBDIMM Module.(168 pin)

Bank	Memory module	
DIMM 1	16MB,32MB,64MB,128MB,256MB,512MB	
( Bank 0-1)	168 pin,3.3v SDRAM	
DIMM 2	16MB,32MB,64MB,128MB,256MB,512MB	
( Bank 2-3 )	168 pin 3.3v,SDRAM	
DIMM 3	16MB,32MB,64MB,128MB,256MB,512MB	
( Bank 4-5)	168 pin 3.3v,SDRAM	
	Total System Memory(Max 1.536MB)	

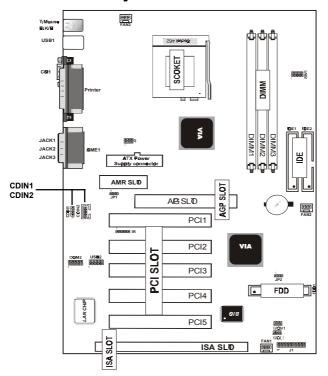
### 1.8.2 How install a DIMM Module

- 1. The DIMM socket has a "Plastic Safety Tab" and the DIMM mory ordule has an asymtrical notch", so the DIMM mory ordule can only fit into the slot in one direction.
- 2.Push the tabs out. Insert the DIMM **emry ord**ules into the socket at a 90-degree angle then push down vertically so that it will fit into place.
- 3. The Mounting Holes and plastic tabs should fit over the edge and hold the DIMM **nory** ordules in place.



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## 1.9 Audio Subsystem



## 1.9.1 CD Audio-In Connectors:CDIN1/CDIN2

Assignment
CD-R
NSD
<b>19</b> D
CD-L

Pin CDIN2	Assignment
1	CD-R
2	<b>IS</b> D
3	CD-L
4	NSD

## 2. BIO Setup

### Introduction

This amual discussed Award Setup programult into the ROM BIOS. The Setup programlows user to ordify the basic systemonfiguration. This special information is then stored in battery-backed RAM so that it retains the setup information when the power is turned off.

The Award BIOS installed in your coputer system ROM (Read Only Meory) is a customersion of an industry standard BIOS. This mans that it supports AMD-Athlon / Duron processors input/output system The BIOS provides critical low-level support for standard devices such as disk drives and serial and parallel ports.

The rest of this **am**ual is intended to guide you through the process of configuring your systemsing Setup.

## Plug and Play Support

These AWARD BIOS supports the Plug and Play Version 1.0A specification. ESCD(Extended SystemConfiguration Data)write is supported.

## EPA Gen PC Support

This AWARD BIOS supports Version 1.03 of the EPA Green PC specification.

## PCI Bus Support

This AWARD BIOS also supports Version 2.1 of the Intel PCI (Peripheral Coponent Interconnect)local bus specification.

### **APM Support**

These AWARD BIOS supports Version 1.1&2 of the Advanced Power Manageant (APM) specification. Power amageant features are inhemnted via the System Manageant Interrupt (SMI). Sleep and Suspend power amageant ordes are supported. Power to the hard disk drives and video unitors can be amaged by this AWARD BIOS.

## **DRAM Support**

SDRAM (Synchronous DRAM) are supported.

## **Support CPU**

This AWARD BIOS supports the AMD Athlon/Duron CPU.

## **Using Setup**

In general, you use the arrow keys to highlight itempress <Enter>to select, use the <PgUp>and <PgDn>keys to change entries, press<F1>for help and press <Esc>to quit. The following table provides or detail about how to navigate in the Setup programy using the keyboard.

### Note:

(BIO version 1.0 is for reference only. If there is a change in BIO version, please use the actual version on the BIO)

Kystroke	Function	
Up arrow	Move to previous item	
Down arrow	Move to next item	
Left arrow	Move to the item the left(mu bar)	
Right arrow	Move to the itemn the right(mu bar)	
Esc	Main Menu:Quit without saving changes	
	Subemus:Exit Current page to the next higher	
	level <b>em</b> u	
Move Enter	Move to itengou desired	
PgUp key	Increase the nueric value or rake changes	
PgDn key	Decrease the numeric value or make changes	
+Key	Increase the nueric value or rake changes	
-Key	Decrease the nueric value or ake changes	
Esc Key	Main emu-Quit and not save changes into	
	CMOS	
	Status Page Setup Menu and option Page Setup	
	Menu -Exit Current page and return to Main	
	Menu	
F1 Key	General help on Setup navigation keys.	
F5 Key	Load previous values from MOS	
F6 Key	Load the fail-safe defaults from IOS default	
	table	
F7 Key	Load the optimed defaults	
F10 Key	Save all the CMOS changes and exit	

### 2.1 Main Menu

Once you enter AWARD BIOS CMOS Set up Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from everal setup function. Use the arrow keys to select arms the item and press < Enter > to accept and enter the sub-enu.

## "WARNING"

The information about BIOS defaults on manual (Figure 1,2,3,4,5,6,7,8,9,10,11,12,13,14) is just for reference, please refer to the BIOS installed on the board for update information.

## **◎** Figure 1. Main Menu

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software

STANDARD CMO FEATURES	URES FREQENCY/VDTAG CNTRD	
ADVANCED BIO FEATURES	LØD FAIL-SAFE DEFAULTS	
ADVANCED CHIPSET FEATURES	LOOD OTIMIZED DEFAULTS	
INTE <b>G</b> ATED PERIPHERALS	SET SUPERVIS® PASSW®D	
PWER MANAGMENT SETUP	SET USER PASSW&D	
PNP/PCI CNIFICIRATINIS S	AVE & EXIT SETUP	
PC HEALTH STATUS	HEALTH STATUS EXIT WITHOT SAVING	
Esc: Quit F9: Menu in BIS ←→↑↓: Select Item		
F10 : Save & Exit Setup		
Time , Date , Hard Disk Type		

### Standard CM®Features

This setup page includes all the item standard copatible BIOS

## 24 / Chapter 2 BIOS Setup

### Advanced BIO Features

This setup page includes all the itemf BIOS special enchanced features.

## **Advanced Chipset Features**

This setup page includes all the itemf Chipset special enchanced features.

## **Integrated Peripherals**

This selection page includes all the itemf IDE hard drive and Programd Input/Output features.

## **Power Management Setup**

This setup page includes all the itemf power amageent features.

## PnP/PCI Configuration

This setup page includes IRQ Setting by user define or default.

### PC Halth Status

This page shows the hardware Monitor information of the system

## Frequency / Voltage Control

This setup page is control CPU's clock and frequency ratio.

#### Load Fail-Safed Defaults

Use this emu to load the BIOS default values for the ininh/stable perforance for your system operate.

### Load Otimized Defaults

These settings are **ore** likely to configure a workable coputer when so**o**thing is wrong. If you cannot boot the coputer seccessfully, select the BIOS Setup options and try to diagnose the problemater the coputer boots. These settings do not provide optional perforance.

### Set Supervisor Password

Change, set, or, disable password. It allows you to lim access to the system and Setup, or just to Setup.

### Set User Password

You can specify both a User and a Supervisor password. When you select either password option, you are propted for a 1-6 character password. Enter the password and then retype the password when propted.

## Save & Exit Setup

Save CMOS value changes to CMOS and exit setup.

## **Exit Without Saving**

Abandon all CMOS value changes and exit setup.

## 2.2 Standard CM®Features

This item Standard CMOS Setup Menu is divided into 10 categories. Each category includes no, one or ore than one setup item. Use the arrow keys to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item.

## **◎** Figure 2. Standard CM**®** Features

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software standard CMOS Features

Date(mm:dd:yy)	Tue,Jun 6 2000	Item Help
Time (hh:mm:ss)  IDE Primary Master IDE Primary Slave IDE Secondary Master	11:26:10  Press Enter None Press Enter None Press Enter None	Menu Level Change the day,
IDE Secondary Master	Press Enter None	and century.
Drive A Drive B	1.44M,3.5 in None	
Video Halt 🛈	E <b>ß</b> /V <b>ß</b> All,But Keyboard	
Base Memory Extended Memory	XXXX	
Total	XXXX	

<sup>←→↑↓:</sup> Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7:ptimized Defaults

## **Main Menu Selections**

This table shows the selections that you can **ak**e on the Main Menu.

Item	Otions	Description	
Date	Month DD YYYY	Set the systemate.note that the	
		'Day' autoanically changes	
		when you set the data.	
IDE Pri <b>an</b> y O	ptions are in its sub	Press <enter> to enter the sub emu</enter>	
Master	<b>em</b> u.	of detailed.	
IDE Pri <b>an</b> y O	ptions are in its sub	Press <enter> to enter the sub emu</enter>	
Slave	emu.	of detailed.	
IDE Secondary	Options are in its sub	Press <enter> to enter the sub emu</enter>	
Master	emu.	of detailed.	
IDE Secondary	Options are in its sub	Press <enter> to enter the sub emu</enter>	
Slave	emu.	of detailed.	
Drive A	None	Select the type of floppy disk drive	
Drive B	360K,5.25in	installed in your system	
	1.2M,5.25in		
	720K,3.5in		
	1.44M,3.5in		
	2.88M,3.5in		
Video	EGA/VGA	Select the default video device.	
	CGA 40		
	CGA 80		
	MONO		

Item	<b>O</b> tions	Description	
Halt On	All Errors	Select the situation in which you	
	No Errors	want the BIOS to stop the POST	
	All,but Keyboard	process and notify.	
	All,but Diskette		
	All,but Disk/Key		
Base Memry N	/A	Displays the amunt of conventional	
		emry detected during boot up.	
Extended	N/A	Displays the anunt of conventional	
Memry		emry detected during boot up.	
Total	N/A	Displays the total <b>cor</b> y	
Me <b>or</b> y		available in the system	

## 2.3 Advanced BIOFeatures

## **◎** Figure 3. Advanced BI**⊗** Features

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software advanced BIOS Features

Anti-Virus Protection	Disabled	Item Help
CPU Internal Cache External Cache CPU L2 Cache ECC Checking	Enabled Enabled Enabled	Menu Level
Qick Power @ Self Test First Boot Device	Enabled Floopy	Allows you to choose the
Second Boot Device Third Boot Device Boot Device	HDD-0 CD-R <b>Ø</b> l Enabled	VIRUS warning feature for IDE Hard Disk boot
Swap Floppy Drive Boot Up Floppy Seek	Disabled Disabled	sector protection. If this function
Boot Up NumLock Status Ste A20 Otion Typematic Rate Setting	0 Fast Disabled	is enabled and someone attempt to write data into
Typematic Rate (Chars/Sec) Typematic Delay (Msec)	6 250	this area,BIO will show a
Security Otion So Select For DRAM Video BIS Shadow	Setup Non- <b>6</b> 2 Enabled	warning message on screen and alarm beep
video bio oliadow	Liidbieu	alailii beep

←→↑↓: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@heral Help F5:Previous Values F6:Fail-Safe Defaults F7:ptimized Defaults

### **Anti-Virus Protection**

This option allows you to choose the VIRUS Warning feature for IDE Hard Disk boot sector protection. If this function is enabled and some attent to write data into this area, BIOS will show a warning essage on screen and alarmeep.

The Choices: Disabled (default), Enabled.

### **CPU Internal Cache**

These two categories speed up **enr**y access. However, it depends on CPU/chipset design.

**Enabled(default)** Enabled cache. **Disabled** Disabled cache.

#### External Cache

This fields allow you to Enable or Disable the CPU'S "Level 2" secondary cache. Caching allows better perforance.

**Enabled(default)** Enabled cache. **Disabled** Disabled cache.

### **CPU L2 Cache ECC Checking**

The iterallows you to enable/disable CPU L2 Cache ECC Checking.

The Choices: Enabled (default), Disabled.

### **Processor Number Feature**

The itenwill show up when you install the Pentium processor.

Enabled(default) PentiumProcessor Nuber

Feature.

**Disabled** Disabled.

## Qick Power @Self Test

This category seeds up Power on Self-Test(POST) after you power up the comuter. If it is set to Enable, BIOS will shorten or skip somcheck itemduring POST.

**Enabled(default)** Enabled quick POST.

**Disabled** Noral POST.

## First/Secondary/Third/Boot ther Device

These BIOS attepts to load the operating systerfrom the devices in the sequence selected in these item **The Choices:**Floppy, LS120, HDD-0, HDD-1, HDD-2, HDD-3, SCSI, CDROM, Enabled, ZIP, LAN, Disabled.

### **Swap Floppy Drive**

If the system as two floppy drives, you can swap the logical drive namassignents.

The Choices: Enabled, Disabled (default).

## **Boot Up Floppy Seek**

Seek disk drives during boot up. Disabled speeds boot-up. **The Choices:Enabled**(default).Disabled.

## **Boot Up NumLock Status**

Select power on state for Nulnck.

O(default)

Nupad is nuber keys.

Nupad is arrow keys.

## Lete A20 Oction

Select if chipset or keyboard controller should control Gate A20

**Normal** A pin in the keyboard

controller controls Gate A20.

**Fast(default)** Lets chipset control Gate A20.

## **Typematic Rate Setting**

**Enabled** Enabled this option to adjust

the keystroke repeat rate.

**Disabled(default)** Disabled.

## **Typematic Rate (Char/Sec)**

Range between 6(default) and 30 characters per second. This option controls the speed of repeating keystrokes.

## **Typematic Delay (Msec)**

This option sets the timinterval for displaying the first and the second characters.

The Choices:250(default),500,750,1000

## Security Otion

This category allows you to limaccess to the system and

Setup, or just to Setup.

**System** The system will not boot and

access to Setup will be defined if the correct password is not

entered in propt.

**Setup(default)** The systemvill boot,but

access to Setup will be defined if the correct password is not

entered in propt.

### **O**Select For DRAM

Select the operating system is running with greater than 64MB of RAM on the system

The Choices: Non-62 (default), 62

### Video BIO Shadow

Deterines whether video BIOS will be copied to RAM for faster execution

**Enabled(default)** Optional ROM is enabled. **Disabled** Optional ROM is disabled.

C8000-CFFFF Shadow / D0000-DFFFF Shadow

Deterines whether video BIOS will be copied to RAM

for faster execution.

**Enabled** Optional ROM is Shadowed.

**Disabled(default)** Optional ROM is not

Shadowed.

**Note:**For C8000-DFFFF option-ROM on PCI BIOS,BIOS will automatically enable the shadow RAM.User does not have to select the item

## 2.4 Advanced Chipset Features

This section allows you to configure the systemased on the specific features of the installed chipset. This chipset amages bus speeds and access to systematry resources, such as DRAM and external cache. It also coordinates commications the PCI bus. It must be stated that these item—should never need to be altered. The default settings have been chosen because they provide the best operating conditions for your systemThe only tienyou ight consider alking any changes would be if you discovered that data was lost while using your system

### **©** Figure 4. Advanced Chipset Features

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software advanced Chipset Features

DRAM Timary By SPD DRAM Clock SDRAM Cycle Length	Enabled 100 3	Item Help
Bank Interleave	Disabled	
	Disabled	Menu Level
Memory Hole	Dioabioa	Menu Level
PCI Master Pipeline Req	Enabled	
P2C/C2P Concurrency	Enabled	
Fast R-W Turn Around	Disabled	
System BI <b>6</b> Cache	Disabled	
Video RAM Cacheable	Disabled	
AB Aperture Size	64M	
AB-4X Mode	Enabled	
AB Driving Control	Auto	
AB Driving Value	DA	
K7 CLK-CTL Select	<b>©</b> timal	
⊕ Chip USB	Enabled	
⊕ Chip USB2	Enabled	
USB Keyboard Support	Disabled	
Chip Sound	Auto	
Chip Modem	Auto	
CPU to PCI Write Buffer	Enabled	
PCI Dynamic Bursting	Enabled	
PCI Master 0 WS Write	Enabled	
PCI Delay Transaction	Enabled	
PCI #2 Access #1 Retry	Enabled	
AB Master 1WS Write	Disabled	
AB Master 1WS Read	Disabled	
/ IN INIGOTOR TO TREAT	Disabled	

<sup>←→↑:</sup> Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@heral Help F5:Previous Values F6:Fail-Safe Defaults F7:fitimized Defaults

### **DRAM Timary by SPD**

The Choices: Enabled (default), Disabled

#### DRAM Clock

This itendeterines DRAM Clock following the CPU host clock.or.

The Choices: 100 (default), 133,66.

#### **SDRAM Cycle Length**

When synchronous DRAM is installed, the numer of clock cycle of CAS latency depends on the DRAM timg. Do not reset this field fronthe default value specified by the systemesigner.

The Choices:3(default),2,Auto.

#### Memory ble

In order to iprove perforace, certain space in energy can be reserved for ISA cards. This energy must be appead into the energy's space below 16MB.

The Choices: Diasbled (default), Enabled.

### P2C/C2P Concurrency

The iterallows you to enable/disable the PCI to CPU to PCI concurrency.

The Choices: Enabled (default), Disabled.

#### Fast R-W Turn Around

The itemontrols the DRAM timg. It allows you to enable/disable the fast read/write turn around.

The Choices: Disabled (default), Enabled.

### System BIO Cacheable

When enabled, the access to the system IOS ROM address at F0000H-FFFFFFH is cached

The Choices: Diasbled (default), Enabled.

#### Video RAM Cacheable

When enabled, the access to the system GA RAM address is cached.

The Choices: Diasbled (default), Enabled.

#### **AB** Aperture Size

Select the size of the Accelerated Graphic Port(AGP) aperture. The aperture is a portion of the PCI entry address range dedicated for graphics entry address space. Host cycle that hit the aperture range are forwarded to the AGP without any translation.

The Choices:64MB(default),32M,16M,8M,4M,128M.

#### AB-4X Mode

The itemallows you to enable/disable the AGP-4X Mode. **The Choices:Enabled**(default), Disabled.

### **AB** Driving Control

By choosing "Auto" the systerBIOS will the AGP output Buffer Drive strength P Ctrl by AGP Card. By choosing "Manual",it allows user to set AGP output Buffer Drive strength P Ctrl by anual.

The Choices: Auto (default), Manual.

### **KCLKCTL** Select

Use this item specify the clock control for ramrate. Select default for a defaulted timvalue, and optimim value which depends on different CPU ratio.

The Choices: Optim (default).

## Ochip USB/USB2

This should be enabled if your system as a USB installed on the system and you whish to use it. Even when so equipped, if you add a higher perforance controller, you will need to disable this feature.

The Choices: Enabled (default), Disabled.

### USB Kyboard Support

Select Enabled if your systemontains a Universal Serial Bus(USB) controller and you have a USB keyboard. **The Choices:Disabled**(default), Enabled.

#### Ochip Sound

The default setting of this itemnilizes an onboard sound chip for audio output. There is no need to buy and insert a sound card. If sound card is installed, disable this item

The Choices: Auto (default), Disabled

### Ochip Modem

The itemllows you to control the onboard MC97 Modemontroller.

The Choices: Auto(default), Disabled.

#### **CPU to PCI Write Buffer**

When this field is Enabled, write fronthe CPU to the PCI bus are buffered, to copensate for the speed defferences between the CPU and the PCI bus. When Disabled, the are not buffered and the CPU ust wait until the write is coplete before statring another write cycle.

The Choices: Enabled (default), Disabled.

## **PCI Dynamic Bursting**

The Choices: Enabled (default), Disabled.

#### PCI Master 0 WS Write

When this field is Enabled, write to the PCI bus are exected with zero wait states.

The Choices: Enabled (default), Disabled.

### **PCI Delay Transaction**

The Choices: Disabled (default), Enabled.

### PCI #2 Access #1 Retry

The Choices: Enabled (default), Disabled.

#### **AB** Master 1WS Write

When Enabled,write to the AGP (Accelerated Graphic Port) are executed with one wait states.

The Choices:Disabled(default), Enabled.

#### AB Master 1WS Read

When Enabled, read to the AGP (Accelerated Graphic Port) are executed with one wait states.

The Choices:Disabled(default), Enabled.

## 2.5 Integrated Peripherals

## **◎** Figure 5. Integrated Peripherals

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software Integrated Peripherals

<sup>←→↑↓:</sup> Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7:ptimized Defaults

**6** Chip IDE Channel 0

**Enabled(default)** Enabled onboard 1st channel

IDE port.

**Disabled** Disabled onboard 1st channel

IDE port.

**6**-Chip IDE Channel 1

Enabled (default) Enabled onboard 2nd channel

IDE port.

**Disabled** Disabled onboard 2nd channel

IDE port.

Primary Master PI(for onboard IDE 1st channel)

Auto(default) BIOS will automatically detect

the IDE HDD Accessing ode.

**Mode 0~4** Manually set the IDE

Accessing mde.

Primary Slave PI(for onboard IDE 2nd channel)

Auto(default) BIOS will automtically detect

the IDE HDD Accessing mde.

Mode 0~4 Manually set the IDE

Accessing mde.

Secondary Master PI(For onboard IDE 1st channel)

Auto(default) BIOS will autoatically detect

the IDE HDD Accessing ode.

**Mode 0~4** Manually set the IDE

Accessing ode.

Secondary Slave PI(for onboard IDE 2nd channel)

Auto(default) BIOS will automtically detect

the IDE HDD Accessing ode.

**Mode 0~4** Manually set the IDE

Accessing ode.

**Primary Master UDMA** 

Auto(default) BIOS will autoatically detect

the IDE HDD Accessing mde.

**Disabled** Disabled.

**Primary Slave UDMA** 

Auto(default) BIOS will automically detect

the IDE HDD Accessing ode.

**Disabled** Disabled.

**Secondary Master UDMA** 

Auto(default) BIOS will autoatically detect

the IDE HDD Accessing **m**de.

**Disabled** Disabled.

**Secondary Slave UDMA** 

Auto(default) BIOS will automatically detect

the IDE HDD Accessing mde.

**Disabled** Disabled.

**Init Display First** 

PCI Slot(default) Set Init Display First to PCI

Slot.

**Oboard AB** Set Init Display First to

onboard AGP.

**IDE HD Block Mode** 

**Enabled(default)** Enabled. **Disabled** Disabled.

**Oboard FDC Controller** 

**Enabled(default)** Enabled. **Disabled** Disabled.

**Oboard Serial Port1/Port2** 

Select an address and corresponding interrupt for the first and second serial ports.

**The Choices:Auto(default)**,(3F8/IRQ4),(2F8/IRQ3), (3E8/IRQ4),(2E8/IRQ3).

#### **UART 2 Mode**

This itemallows you decide which Infra Red(IR) function of the onboard I/O chip, you wish to use.

The Choices: Standard (default), SCR, ASKIR.

### **IR Function Duplex**

This itemallows you decide which Infra Red(IR) function of the onboard I/O chip.

The Choices: Haf (default). Full.

#### Parallel Port Mode

**SPP** Using Parallel port as Standard

Parallel Port.

**EPP** Using Parallel port as Ex-

hancedParallel Port.

**ECP** Using Parallel port as Ex-

tended Capabilites Port.

**ECP+EPP(default)** Using Parallel port as

ECP+EPP ode.

#### **O**board Parallel Port

This itemllows you decide access onboard parallel port controller with which I/O address.

**The Choices:378/IRQ** (default),Disabled,278/IR5,3BC/IR7

### **Oboard Legacy Audio**

The Choices: Enabled (default), Disabled.

#### **Sound Blaster**

The Choices: Disabled (default), Enabled.

MPU -401

The Choices: Disabled (default), Enabled.

### MPU -401 I/OAddress(330-333H

Change the SoundBlaster Pro MPU-401 I/O address.

### 6me Port (200-270H

Change the joystick connect address.

The Choices: Enabled (default), Disabled.

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## 2.6 Power Management Setup

The Power Managemnt Setup allows you to configure your system on effectively save energy while operating in a amner consistent with your own style of coputer use.

## **◎** Figure 6. Power Management Setup

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software Power Manage**en**t Setup

ACPI Function	Enabled	Item Help
Power Management ACPI Suspend Type PM Control by APM Video f Otion Video f Method Modem Use IRQ Soft-fb by PWRBTN Wake Up Events	Press Enter S1(P6) Yes Suspend->fb V/H SYNC+Blank 3 Instant-fb Press Enter	Menu Level

←→↑↓: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7:ptimized Defaults

#### ACPI Function

This iterdisplay status of the Advanced Configuration and Power Manageent (ACPI).

### **Power Management**

This category allows you to select the type (or degree) of power saving and is directly related to the following mles

- 1.HDD Power Down.
- 2.Doze Mode.
- 3. Suspend Mode.

If you highlight the literal "Press Enter" next to the "Power Manageunt" label and then press the enter key, it will take you a subunu with the following options:

### **Power Management**

This option allows you to set each orde individually. When not disabled, each of the ranges are from in. to 1 hr. except for HDD Power Down which ranges from in. to 15 in. and disable.

**The Choices: User Define** (default), Min Saving, Max Saving.

#### **HD** Power Down

By default, this is "Disabled", maning that no matter the orde the rest of system the hard drive will remain ready. Otherwise, you have a range of choices from to 15 imutes or Suspend. This mans that you can select to have your hard disk drive be turned off after a selected number of imutes or when the rest or the system goes into a suspend orde.

Disabled(default).

#### Doze Mode/Suspend Mode

The **Doze Mode**, and **Suspend Mode** fields set the Period of timafter each of these **odes** actives. At Max Saving, these **odes** activate sequentially (in the given order) after one **inute**; at Min Saving after one hour.

## **ACPI Suspend Type**

The iterallows you to select the suspend type under ACPI

operating system

S1(PS) (default) Power on Suspend. S3(STR) Suspend to RAM.

PM Control by APM

No SystemBIOS will ignore APM

when Power Manageent is

on.

Yes(default) SystemBIOS will wait for

APM'S propt before it enters

any PM orde.

### Video & Otion

This field determes when to activate the vedio off feature for omitor power anageant.

#### Video 6 Method

This deterines the anner in which the onitor is

blanked.

V/SYNC+Blank This selection will cause the default) system turn off the vertical

and horizontal.

Synchronization ports and write blanks to the video

buffer.

Blank Screen This option only writes blanks

to the video buffer.

**DPMS Support** Initial display power

amageent signaling.

### Modem Use IRQ

This deterines the IRQ, which can be applied in Modem use

3(default)

4/5/7/9/10/11/NA

### Soft-6 by PWRBTN

Pressing the power button for **n**re than 4 seconds forces the system enter the Soft-Off state when the system s"hung".

The Choices:Instant-6 (default), Delay 4 Sec.

### Wake Up Events

If you highlight the literal "Press Enter" next to the "Wake Up Events" label and then press the enter key, it will take you a subemu with the following options:

#### VA

When set to On, any event occurring at a VGA port will awaken a system hich has been powered down.

#### LPT & CM

When set to On,any event occurring at a COM(serial) /LPT (printer) port will awaken a system hich has been powered down.

#### HD & FDD

When set to On(default), any event occurring at a hard or floppy drive will awaken a system hich has been powered down.

### **PCI Master**

When set to On, any event occurring at a PCI port will awaken a system hich has been powered down.

### **Modem Ring Resume**

To use this function, you need a LAN add-on card which support power on function. It should also support the wake-up on LAN jup The Choices:Disabled(default).

#### RTC Alarm Resume

When "Enabled", you can set the date and timat which the RTC (real-timelock) alarmwakens the systerfrom Suspend orde.

#### **Primary INTR**

When set to On(default), any event occurring at Priary INTR will awaken a system hich has been powered down

The following is a list of IRQ, Interrupt ReQuests, which can be exepted not as the COM ports and LPT ports above can. When an I/O device wants to gain the attention of the operating system signals this by causing an IRQ to occur. When the operating system ready to respond to the request, it interrupt itself and perforanthe service. As above, the choices are On and Off.Off is the default. When set On, activity will neither prevent the system going into a power amagement orde nor awaken it.

IRQ3	(COM1)
IRQ4	(COM2)
IRQ5	(LPT2)
IRQ6	(Floppy Disk)
IRQ7	(LPT1)
IRQ8	(RTC Aları)n
IRQ9	(IRQ2 Redir)
IRQ10	(Reserved)
IRQ11	(Reserved)
IRQ12	(PS/2 Mouse)
IRQ13	(Coprocessor)
IRQ14	(Hard Disk)
IRQ15	(Reserved)

## 2.7 PnP/PCI Configurations

This section describes configuring the PCI bus systemPCI or Personal Coputer Interconnect, is a system which allows I/O devices to operate at speeds nearing the speed of the CPU itself uses when commicating with its own special coponents. This section covers somewry technical item and it is strongly recommed that only experienced user should also any changes to the default settings.

## **◎** Figure 7. PnP/PCI Configurations

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software PnP/PCI Configurations

PNP 6 Installed Reset Configuration Data	No Disabled	Item Help Menu Level
Resources Controlled By IRŒsesources DMA Resources	Auto(ESCD) Press Enter Press Enter	Select Yes if you are using a Plug and Play capable
PCI/VS Palette Snoop Assign IR@or VS Assign IR@or USB	Disabled Enabled Enabled	operating system select No if you need the BIO to configure non- boot devices

←→↑↓: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@heral Help F5:Previous Values F6:Fail-Safe Defaults F7:@timized Defaults

#### PNP **O** Installed

When set to YES,BIOS will only initialize the PnP cards used for booting (VGA,IDE,SCSI). The rest of the cards will be initialized by the PnP operating systerlike Windows 95. When set to No, BIOS will initialize all the PnP cards. Therefore for non-PnP operating syste(DOS, Netware), this option ust set to No.

### **Reset Configuration Data**

The systerBIOS supports the PnP feature so the system needs to record which resource is assigned and proceeds resources fromonflict. Every peripheral device has a node, which is called ESCD. This node records which resources are assigned to it. The systemeeds to record and update ESCD to the corry locations. These locations (4K) are reserved at the systerBIOS. If Disabled (Default) is chosen, the systerBIOS. If Disabled (Default) is chosen, the syster ESCD will update only when the new configuration varies frorthe last one. If Enabled is chosen, the systems forced to update ESCDs and then is automatically set to the "Disabled" rode.

assigned to:PCI/ISA PnP
_
assigned to:PCI/ISA PnP

The above settings will be shown on the screen only if "Manual" is chosen for the resources controlled by function.

Legacy is the ternwhich signifies that a resource is assigned to the ISA Bus and provides for non-PnP ISA add-on cards. PCI/ISA PnP signifies that a resource is assigned to the PCI Bus or provides for ISA PnP add-on cards and peripherals.

#### **Resources Controlled By**

By Choosing "Auto" (default), the system IOS will detect the system sources and automatcally assign the relative IRQ and DMA channel for each peripheral. By Choosing "Manual", the user will need to assign IRQ DMA for add-on cards. Be sure that there no IRQ/DMA and I/O port conflicts

### IR (Resources

When resources are controlled **am**ually, assign each system terrupt a type, depending on the type of device using the interrupt.

#### DMA Resources

When resources are controlled **am**ually, assign each DMA channel a type, depending on the type of device using the DMA channel.

### PCI / V& Palette Snoop

Choose Disabled or Enabled. Somgraphic controllers which are not VGA copatible take the output from VGA controller and app it to their display as a way to provide boot information and VGA copatibility.

However, the color inforation coing fronthe VGA controller is drawn fronthe palette table inside the VGA controller to generate the proper colors, and the graphic controller needs to know what is in the palette of the VGA controller. To do this, the non-VGA graphic controller watches for the write access to the VGA palette and registers the snoop data. In PCI based system where the bus, the Write Access to the palette will not show up on the ISA bus if the PCI VGA controller responds to the Write.

In this case, the PCI VGA controller should not respond to the Write, it should only snoop the data and perinthe access to be forward to the ISA bus. The non-VGA ISA graphic controller can then snoop the data on the ISA bus. Unless you have the above sistuation, you should disable this option.

**Disabled**(default) Disabled the function. **Enabled** Enabled the function.

### Assign IR For V&

Lets the user choose which IRQ to assign for the VGA.

### Assign IR For USB

Lets the user choose which IRQ to assign for the USB.

### 2.8 PC Halth Status

### **◎** Figure 8. PC Halth Status

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software PC Health Status

	Item Help
Current System Temp.	Menu Level
+12.0V	

←→↑↓: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7:0timized Defaults

## Current CPU Temp.

This field displays the current CPU teperature, if you coputer contain a mitoring system

### **Current System Temp.**

This field displays the current systemerature, if you coputer contain a mitoring system

## **Current CPU Fan Speed**

These field displays the current speed of up to CPU Fans, if you coputer contain a mitoring system

## **Current System Fan Speed**

These field displays the current speed of up to System Fans, if you coputer contain a mitoring system

### Current CPU Vcore VccSRAM,3.3V,5V,12V

Detect system voltage status automically.

## 2.9 Frequency / Voltage Control

## **◎** Figure 9. Frequency / Voltage Control

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software Frequency / Voltage Control

Auto Detect DIMM / PCI CLK Spread Spectrum Modulated	Disabled Disabled	Item Help
Clock By Slight Adjust	100	Menu Level

←→↑↓: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7:@timized Defaults

#### Auto Detect DIMM / PCI CLK

This itemallows you to enable/disable auto detect DIMM / PCI CLOCK.

The Choices:Disabled(default), Enabled.

### Clock By Slight Adjust 100

This itemallows you to select CPU Clock.

#### NOTE:

If unfortunately,the system frequency that you are selected is not functioning,there are two wthods od booting-up the system

Method1:Clear the COMS data by setting the JP2((2-3) closed) as "On" status.All the COMS data will be loaded as default setting.

Method2:Press the<Insert>key and Power button simtaneously, after that keep-on pressing the<Insert>key until the Power-on screen showed. This action will boot-up the systemccording to FSB of the processor.

### **Spread Spectrum Modulated**

This function id designed to EMI test only.

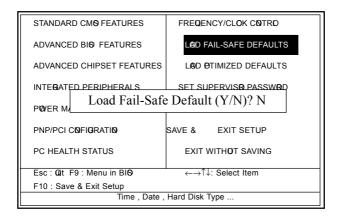
The Choices: Disabled (default), Enabled.

### 2.10 Load Fail-Safe Defaults

When you press <Enter> on this itemou get a confirmation dialog box with a passage sighar to:

### **◎** Figure 10. Load Fail-Safe Defaults

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software



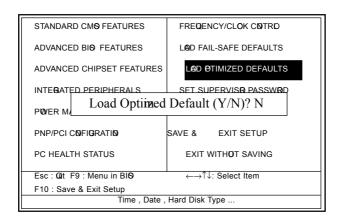
Pressing 'Y' loads the default values that are factory settings for optim performace systemperations.

## 2.11 Load Otimized Defaults

When you press <Enter> on this itemou get a confirmation dialog box with a passage sight to:

### **◎** Figure 11. Load **⊘**timized Defaults

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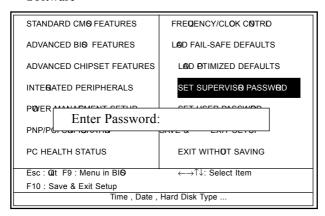


Pressing 'Y' loads the default values that are factory settings for optim perforance system perations.

## 2.12 Set Supervisor / User Password

## **◎** Figure 12. Set Supervisor / User Password

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When you select this function, the following **as**sage will appear at the center of the screen to assist you in creating a password.

#### Enter Password

Type the password, up to eight characters, and press <Enter>. The password you type now will clear any previously entered password from MOS emry. You will be asked to comm the password. Type the password again and press <Enter>. You may also press <ESC> to about the selection and not enter a password. To disable password, just press <Enter> when you are propried to enter password. A massage will commentate you wish to disable the password. Once the password is disabled, the system ill boot and you can enter setup freely.

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#### Password Disabled

If you select "Systemat the Security Option of BIOS Features Setup Menu, you willbe propted for the password every timewhen the systems rebooted, or any timewhen you try to enter Setup. If you select "Setup" at Security Option of BIOS Features Setup Menu, you willbe propted only when you try to enter Setup.

## 2.13 Save & Exit Setup

## **◎** Figure 13. Save & Exit Setup

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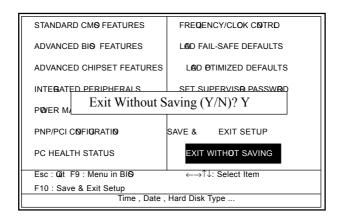
Type "Y" will quit the Setup Unility and save the user setup value to RTC CMOS RAM.

Type "N" will return Setup Unility.

## 2.14 Exit Without Saving

### **◎** Figure 14. Exit Without Saving

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Type "Y" will quit the Setup Unility without saving to RTC CMOS RAM.

Type "N" will return Setup Unility.

Date : / / Warranty Card/Technical Fault Report M/B Model No.:\_\_\_\_\_ Vender Serial No. Date of Purchasing: Hardare Configuration Used: CPU RAM (Brand, MB) Video Card Hard Drive ther Card Diagnostic Softare Used: **Fault Description:** 

# The 7KTA/AP Mainboard layout

