

# TABLE OF CONTENT

---

---

## Chapter 1

|                      |   |
|----------------------|---|
| 1-1 Preface.....     | 1 |
| 1-2 Key Feature..... | 1 |

## Chapter 2

|  |    |
|--|----|
| Installation.....                      | 3  |
| 2-1 Unpacking.....                     | 3  |
| 2-2 Mainboard Layout.....              | 4  |
| 2-3 Installation Steps.....            | 6  |
| 2-3-1 Jumper Settings.....             | 6  |
| 2-3-2 System Memory (DRAM) .....       | 8  |
| 2-4 Central processing Unit (CPU)..... | 8  |
| 2-5 Expansion Cards.....               | 9  |
| 2-6 External Connectors.....           | 10 |

## Chapter 3

|  |    |
|--|----|
| <b>AWARD BIOS SETUP</b> .....                      | 16 |
| 3-1 STANDARD CMOS SETUP.....                       | 17 |
| 3-2 BIOS FEATURES SETUP.....                       | 17 |
| 3-3 CHIPSET FEATURES SETUP.....                    | 21 |
| 3-4 POWER MANAGEMENT SETUP.....                    | 22 |
| 3-4-1 The Description of the Power Management..... | 22 |
| 3-4-2 Description of the Green Functions.....      | 23 |
| 3-5 PNP/PCI CONFIGURATION SETUP.....               | 24 |
| 3-6 LOAD BIOS DEFAULTS.....                        | 25 |
| 3-7 LOAD SETUP DEFAULTS.....                       | 25 |
| 3-8 INTEGRATED PERIPHERALS SETUP.....              | 26 |
| 3-9 SUPERVISOR/USER PASSWORD.....                  | 28 |
| 3-10 IDE HDD AUTO DETECTION.....                   | 28 |
| 3-11 SAVE & EXIT SETUP.....                        | 30 |
| 3-12 EXIT WITHOUT SAVING.....                      | 30 |
| 3-13 I/O & MEMORY MAP.....                         | 30 |
| 3-14 TIME & DMA CHANNELS MAP.....                  | 32 |
| 3-15 INTERRUPT MAP.....                            | 32 |
| 3-16 RTC & CMOS RAM MAP.....                       | 32 |
| 3-17 BIOS REFERENCE-POST CODES.....                | 33 |

---

---

# Chapter 1

## 1-1 Preface

Welcome to use the [J-791AR2/J-791AR2/L](#) mainboard. This mainboard supports Pentium® II processor PC/AT compatible system with AGP slot, ISA bus and PCI local bus. Also this board including some special designs, like ACPI/APM power management & powerful performance , We think you will enjoying your personal computer because of your right choice.

## 1.2 Key Feature

The [J-791AR2/J-791AR2/L](#) is designed for the PC user who wants a great many features in a small package. This motherboard:

- **SDRAM Clock selectable (66MHz/100MHz),in 100MHz Bus Frequency,let users more choice when use SDRAM.**
- **CPU BUS Frequency can setting by either jumper or BIOS SETUP.**
- **AGP:** Accelerated Graphics Port for 533MB/sec data transfer rate and 66MHz Bus clock to improve graphic functions.
- **Easy Installation:** Is equipped with BIOS supports auto detection of hard drives and plug and play to make setup of hard drives and expansion cards virtually automatic.
- **Chipset:** VIA APOLLO PRO VT82C691/VT82C596 PCI/AGPset with I/O subsystems.
- **Multi-Speed Support:** Supports Intel Pentium® II processor from 233MHz to 450MHz on Slot-1.
- **DRAM Memory Support:** Support 3x168-pin DIMMs of 16MB, 32MB, 64MB, 128MB, 256MB from a memory size between 16MB to 768M.
- **AGP, ISA and PCI Expansion Slots:** Provides an AGP slot, two 16-bit ISA slots, and four 32-bit PCI slots.
- **Super Multi-I/O:** Provides two high-Speed UART compatible serial ports and one parallel port with EPP and ECP capabilities. Two floppy drives of either 5.25" or 3.5" (1.44MB or 2.88MB) are also supported without an external card.
- **PCI Bus Master IDE Controller and ULTRA DMA 33:** On-board PCI Bus Master IDE controller with two connectors that supports four IDE devices in two channels, provides faster data transfer rates, and supports Enhanced IDE devices such as Tape Backup, CD-ROM drives and LS-120. This controller also supports PIO Modes 3 and 4 and Bus Master IDE DMA 33Mbyte/Sec.
- **ACPI supporting for OS Directed Power Management.**
  - Ring-In Wake up:** When Ring-In the system can wake up from SMI Mode.
  - Ring-In Power On:** When Ring-In the system can power on automatic by this function.
  - Wake On LAN:** Workstation can power on by Server in Network.
  - RTC Power On:** Enabled RTC Power On function, you can setting RTC alarm to power on the system at the time length you setting.

---

---

**Power Button:** Press the button will place the system power on/off.

**CPU fan auto-stopping when in suspend mode.**

**Support Software Power Off function.**

- **Power Support:** Efficient PWM switching power instead of traditional Linear Voltage Regulator to prevent power component from being burned-out.
- **PC 97 ready**
- **PS/2 Mouse:** This motherboard support PS/2 mouse set.
- **USB Port Connector:** This motherboard supports two USB port connectors for USB devices.
- **ATX Form Factor:** Dimensions 30.5cmx18cm.
- **Optional IrDA:** This motherboard supports an optional infrared port module for wireless interface, with independent 3rd UART (32-byte FIFO).
- **Fans:** Support two Extra fan connectors to evacuate the heat from CPU, and the heat within the computer case.
- **PC HEALTH MONITOR:** (Only in J-791AR2/L)

To track PC system temperature system Voltage, and Fan speed. When system temperature is over warning temperature by HEALTH Monitor Utility will have alarm to warn users.

---

---

## Chapter 2

### Hardware Installation

#### 2-1 Unpacking

This mainboard package should contain the following:

- The [J-791AR2/J-791AR2/L](#) mainboard
- USER'S MANUAL for mainboard
- Cable set for IDE x1, Floppy x1
- CD for Drivers PACK

The mainboard contains sensitive electronic components which can be easily damaged by electron-static, so the mainboard should be left in its original packing until it is installed.

Unpacking and installation should be done on a grounded anti-static mat.

The operator should be wearing an anti static wristband, grounded at the same point as the anti-static mat.

Inspect the mainboard carton for obvious damage. Shipping and handling may cause damage to your board. Be sure there are no shipping and handling damages on the board before proceeding.

After opening the mainboard carton, extract the system board and place it only on a grounded anti-static surface component side up. Again inspect the board for damage.

Press down on all of the socket IC's to make sure that they are properly inserted. Do this only on with the board placed on a firm flat surface.

***Warning: Do not apply power to the board if it has been damaged.***

You are now ready to install your mainboard. The mounting hole pattern on the mainboard matches the IBM-AT system board.

It is assumed that the chassis is designed for a standard IBM™ XT/AT™ main board mounting. Place the chassis on the anti-static mat and remove the cover.

Take the plastic clips, Nylon stand-off and screws for mounting the system board, and keep them separate.

## 2.2 Mainboard Layout

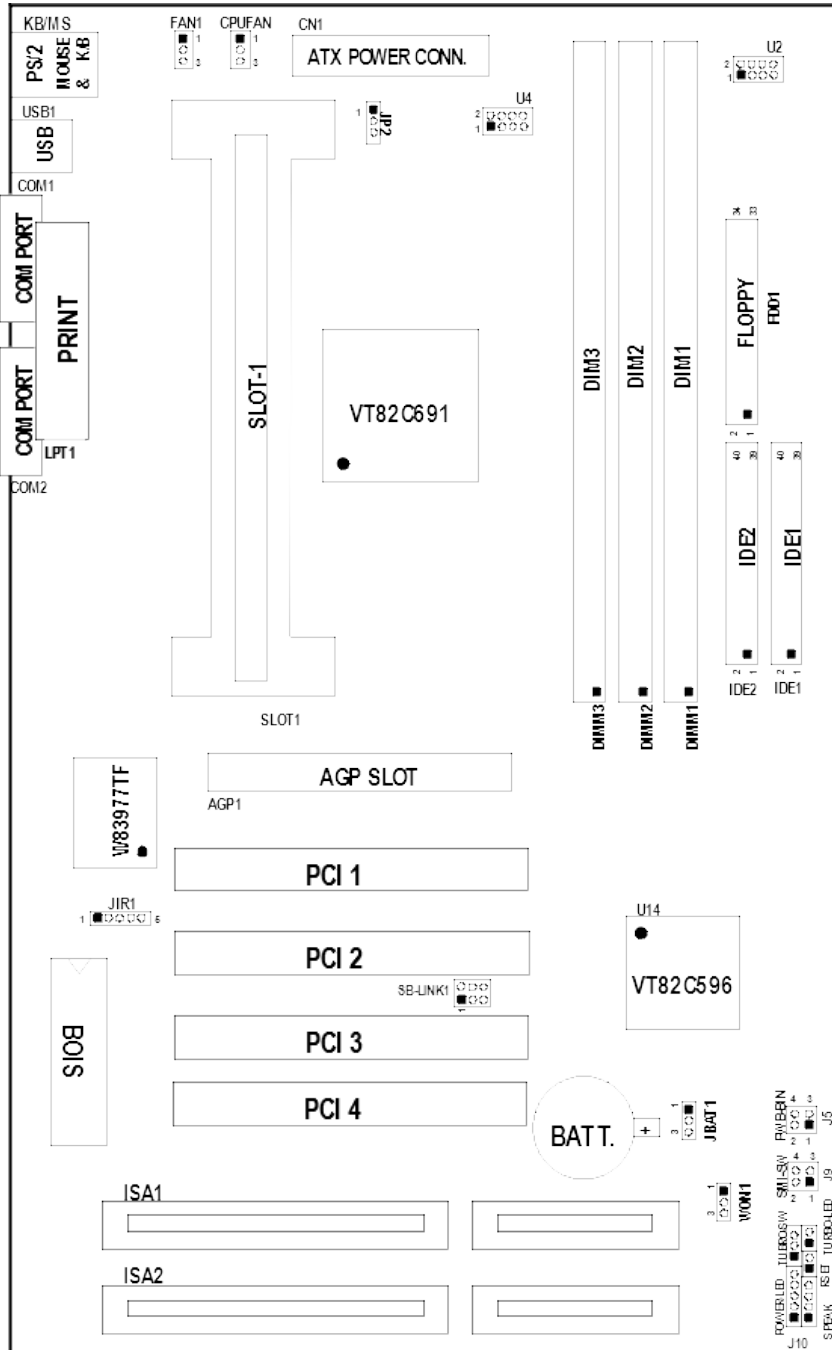


Figure 2-1

---

---

## **Jumpers**

- |          |   |
|----------|---|
| 1) U4    | p. 7 Jumper CPU Ratio Selection   |
| 2) JBAT1 | p. 7 CMOS RAM (Normal/Clear CMOS Data) ( <b>Yellow color selector</b> ) |
| 3) U2    | p. 7 Jumper for CPU BUS Frequency Selection                             |

## **Connectors**

- |                       |   |
|-----------------------|---|
| 1) CN1                | p. 10 ATX Power connector (20-pin Block)                  |
| 2) KB/MS              | p. 11 PS/2 Keyboard & PS/2 Mouse connector (6-pin Female) |
| 3) LPT1               | p. 11 Parallel Port connector (25-pin Block)              |
| 4) COM1, COM2         | p. 11 Serial Port COMA & COMB (10-pin Block)              |
| 5) FDD1               | p. 12 Floppy Driver connector (34-pin Block)              |
| 6) IDE1               | p. 12 Primary IDE connector (40-pin Block)                |
| 7) IDE2               | p. 12 Secondary IDE connector (40-pin Block)              |
| 8) SMI_SW(J9 2&4)     | p. 13 SMI Suspend Switch lead (2-pin)                     |
| 9) HDLED (J9 1&3)     | p. 13 IDE activity LED connector (2-pin)                  |
| 10) TBLED             | p. 13 Turbo LED switch (2-pin)                            |
| 11) RESET             | p. 13 Reset Switch lead (2-pin)                           |
| 12) KEYLOCK/Power LED | p. 13 Keyboard Lock Switch Power/LED (5-pin)              |
| 13) SPEAK             | p. 13 Speaker connector (4-pin)                           |
| 14) JIR1              | p. 13 Infrared Module connector (5-pin)                   |
| 15) USB1              | p. 14 USB Port connector                                  |
| 16) CPUFAN            | p. 14 CPU Fan connector                                   |
| 17) FAN1              | p. 14 Extra fanning system connectors                     |
| 18) PWR_BTN(J5 2&4)   | p. 15 Power-On Button connector                           |
| 19) SB_LINK1          | p.15 SBLINK connector                                     |
| 20) WON 1             | p.15 Wake On Lan connector                                |

## **Expansion Slots**

- |                |   |
|----------------|---|
| 1) DIMM Slots  | p. 8 DRAM Memory Expansion slots            |
| 2) Slot-1      | p. 8 Slot for Central Processing Unit (CPU) |
| 3) AGP Slot    | p. 9 AGP Expansion slot                     |
| 4) ISA 1,2     | p. 9 16-bit ISA Bus Expansion slots         |
| 5) PCI 1,2,3,4 | p. 9 32-bit PCI Bus Expansion slots         |

## 2-3 Installation Steps

Before using your computer, you must follow the steps as follows:

1. Set Jumpers on the Motherboard
2. Install the CPU
3. Install DRAM Modules
4. Install Expansion card
5. Connect Cables, Wires, and Power Supply
6. Setup the BIOS Software

### 2-3-1 Jumper Settings

#### 1. Jumper for CPU Type Selection:U4,U2

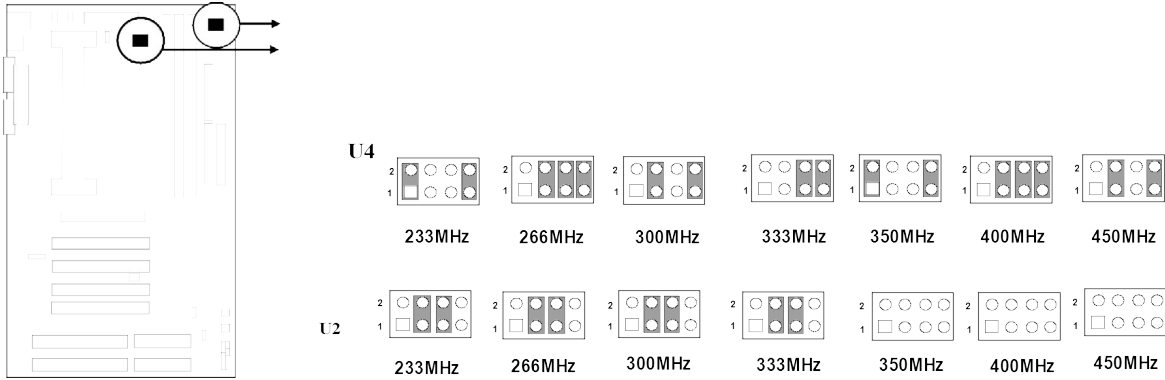
| Jumper         | U4       |     |     |     | U2  |     |     |     |
|----------------|----------|-----|-----|-----|-----|-----|-----|-----|
|                | CPU TYPE | 1-2 | 3-4 | 5-6 | 7-8 | 1-2 | 3-4 | 5-6 |
| Pentium II 233 | ON       | OFF | OFF | ON  | OFF | ON  | ON  | OFF |
| Pentium II 266 | OFF      | ON  | ON  | ON  | OFF | ON  | ON  | OFF |
| Pentium II 300 | OFF      | ON  | OFF | ON  | OFF | ON  | ON  | OFF |
| Pentium II 333 | OFF      | OFF | ON  | ON  | OFF | ON  | ON  | OFF |
| Pentium II 350 | ON       | OFF | OFF | ON  | OFF | OFF | OFF | OFF |
| Pentium II 400 | OFF      | ON  | ON  | ON  | OFF | OFF | OFF | OFF |
| Pentium II 450 | OFF      | ON  | OFF | ON  | OFF | OFF | OFF | OFF |

| CPU ratio selector U4 |     |     |     |     |
|-----------------------|-----|-----|-----|-----|
| NO                    | 1-2 | 3-4 | 5-6 | 7-8 |
| Ratio                 |     |     |     |     |
| 2X                    | ON  | ON  | ON  | ON  |
| 2.5X                  | ON  | ON  | OFF | ON  |
| 3.0X                  | ON  | OFF | ON  | ON  |
| 3.5X                  | ON  | OFF | OFF | ON  |
| 4.0X                  | OFF | ON  | ON  | ON  |
| 4.5X                  | OFF | ON  | OFF | ON  |
| 5.0X                  | OFF | OFF | ON  | ON  |
| 5.5X                  | OFF | OFF | OFF | ON  |
| 6.0X                  | ON  | ON  | ON  | OFF |
| 6.5X                  | ON  | ON  | OFF | OFF |
| 7.0X                  | ON  | OFF | ON  | OFF |

| BUS Frequency selector U2 |     |     |     |     |
|---------------------------|-----|-----|-----|-----|
| NO                        | #1  | #2  | #3  | #4  |
| Frequency                 |     |     |     |     |
| 66.6MHz                   | OFF | ON  | ON  | OFF |
| 75MHz                     | OFF | OFF | ON  | OFF |
| 95MHz                     | ON  | OFF | OFF | OFF |
| 100MHz                    | OFF | OFF | OFF | OFF |

※ The Bus Frequency also can setting by “CPU HOST/PCI clock” item in “CHIPSET FEATURES SETUP” section in BIOS SETUP. We don't recommend to choose over clock

value in your system, because it will have problem if your CPU, DRAM, IDE Drive, Add on card can not working in over clock.



## 2. SDRAM Clock Selector: U2 7-8

This selector can choose SDRAM clock Synchronous or Asynchronous to CPU Bus frequency. When choose Asynchronous can let users have more choice of NON-PC100 SDRAM at over 66 MHz Bus frequency .

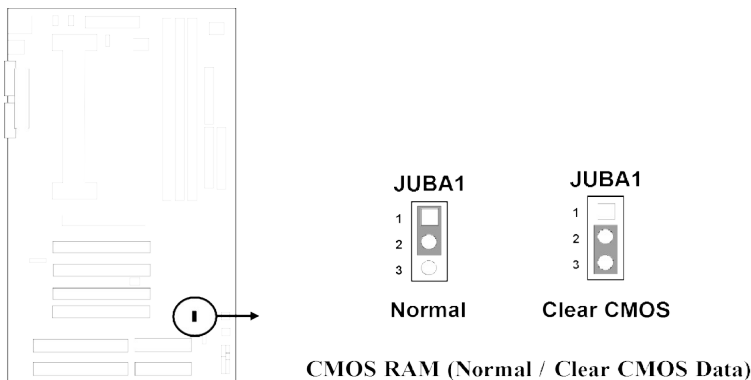
- U2 7-8 ON ———SDRAM clock same as AGP clock (66MHz) Asynchronous.
- U2 7-8 OFF ———SDRAM clock same as CPU Bus clock (66MHz,75MHz,95MHz, 100MHz,112MHz): Synchronous.

## 3. CMOS RAM: JBAT1 (Yellow color selector)

**WARNING: Make sure your computer is POWER OFF when you are CLEAR CMOS.**

Connect a jumper Cap over this jumper for a few seconds, will clears information stored in the CMOS RAM Chip that input by user, such as hard disk information and passwords. After CLEAR CMOS, you must enter the BIOS setup (by holding down <DEL> during power-up) to re-enter BIOS information (see BIOS SETUP).

| Selections | JBAT1             |
|------------|-------------------|
| Normal     | 1-2 (Default)     |
| Clear CMOS | 2-3 (momentarily) |





---

---

## 2-3-2 System Memory (DRAM)

This motherboard support tree168-pin DIMM Module, using +3V unbuffer synchronous DRAM in either EDO or SDRAM with Max. memory size of 768MB.

| DIMM1 | DIMM2 | DIMM3 | Total Memory Combination |
|-------|-------|-------|--------------------------|
| 16MB  | none  | none  | 16MB                     |
| 16MB  | 16MB  | none  | 32MB                     |
| 16MB  | 16MB  | 16MB  | 48MB                     |
| 32MB  | none  | none  | 32MB                     |
| 32MB  | 32MB  | none  | 64MB                     |
| 32MB  | 32MB  | 32MB  | 96MB                     |
| 64MB  | none  | none  | 64MB                     |
| 64MB  | 64MB  | none  | 128MB                    |
| 64MB  | 64MB  | 64MB  | 192MB                    |
| 128MB | none  | none  | 128MB                    |
| 128MB | 128MB | none  | 256MB                    |
| 128MB | 128MB | 128MB | 384MB                    |
| 256MB | none  | none  | 256MB                    |
| 256MB | 256MB | none  | 512MB                    |
| 256MB | 256MB | 256MB | 768MB                    |

## 2-4 Central Processing Unit (CPU)

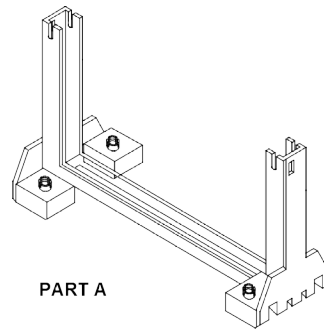
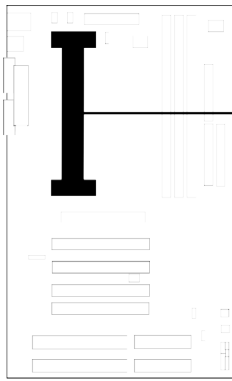
The motherboard provides a Slot-1 for Pentium II processor. The CPU on board must have a fan or heat sink attached to prevent overheating.

***WARNING: Without a fan or heat sink, the CPU will overheat and cause damage to both the CPU and the motherboard.***

To install a CPU, first turn off your system and remove its cover. Locate the Slot- 1 and place RETENTION MODULE as following:

***IMPORTANT: You must set jumper U4,U2 for both “CPU to Bus frequency Ratio” and “Bus Frequency Selection” on [page 7](#) depending on the CPU that you install.***

- ① Attach heat sink to the CPU.
- ② Place Part A on slot-1 and gently screw four corners on top of the mother-board.



## 2-5 Expansion Cards

You must read the documentation come with expansion card for any hardware or software settings that may be required to setup your specific card.

### Installation Procedure:

1. Read the documentation from your expansion card.
2. Set any necessary jumpers on your expansion card.
3. Remove your computer's cover.
4. Remove the bracket on the slot you intend to use.
5. Carefully align the card's connectors and press firmly.
6. Secure the card on the slot with the screw you remove in step 4.
7. Replace the computer's cover.
8. Setup the BIOS if necessary.
9. Install the necessary software drivers for your expansion card.

### Assigning IRQs for Expansion Cards

Some expansion cards may require an IRQ to operate. Generally an IRQ must be exclusively assigned to only one device. In a standard design there are 16 IRQs available but most of them are occupied by the system and leaves 6 free for expansion cards.

Either AGP, ISA or PCI expansion cards may require an IRQs. System IRQs are available to cards installed in the ISA expansion first, and any remaining IRQs can be used by PCI cards. Currently, there are two types of ISA cards. An original ISA expansion card design, know as "Legacy" ISA cards, they request configure the card's jumpers manually and then install it in any available slot on the ISA bus, and other know as Plug and Play. You may use Microsoft's Diagnostic (MSD.EXE) utility included in the DOS directory to see a map of your used and any free IRQs. For Windows 95 users, the "Control Panel" icon in "My Computer", contains a "System" icon which gives you "Device Manager" tab. Double clicking on a specific device give you "Resources" tab which shows the Interrupt number and address. Make sure that no any two of devices use the same IRQs, or your computer will experience problems when those two devices are in use at the same time.

---

---

To simplify this process the motherboard has complied with the Plug and Play (PNP) specification which was developed to allow automatic system configuration whenever a PNP-compliant card is added to the system. For PNP cards, IRQs are assigned automatically from those available.

If the system has both Legacy and PNP ISA cards installed, IRQs are assigned to PNP cards from those not used by Legacy cards. The PCI and PNP configuration of the BIOS setup utility can be used to indicate which IRQs are being used by Legacy cards. For older Legacy cards that does not work with the BIOS, you can contact your vendor for an ISA Configuration Utility.

An IRQ number is automatically assigned to PCI expansion cards after those used by Legacy and PNP ISA cards. In the PCI bus design, the BIOS automatically assigns an IRQ to a PCI. To install a PCI card, you need to set something called the INT (interrupt) assignment. Since all the PCI slots on this motherboard use an INTA #, be sure that the jumpers on your PCI cards are set to INT A.

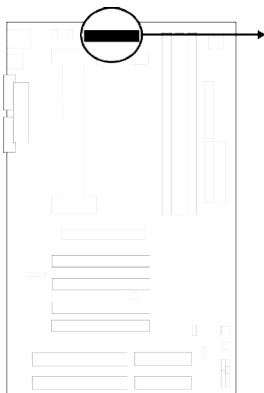
### Assigning DMA Channels for ISA Cards

Some ISA cards, both Legacy and PNP may also need to use a DMA (Direct Memory Access) channel. DMA assignments for this motherboard are handled the same way as the IRQ assignment process described above. You can select a DMA channel in the PCI and PNP configuration section of the BIOS Setup utility. In the BIOS setup, you should choose “Yes” for those IRQ’s and DMA’s you wish to reserve for Legacy cards.

## 2-6 External Connectors

### 1. ATX Power Connector (20-pin block): CN1

ATX Power Supply connector. This is a new defined 20-pin connector that usually comes with ATX case. The ATX Power Supply allows to use soft power on momentary switch that connect from the front panel switch to 2-pins Power On jumper pole on the motherboard. When the power switch on the back of the ATX power supply turned on, the full power will not come into the system board until the front panel switch is momentarily pressed. Press this switch again will turn off the power to the system board.



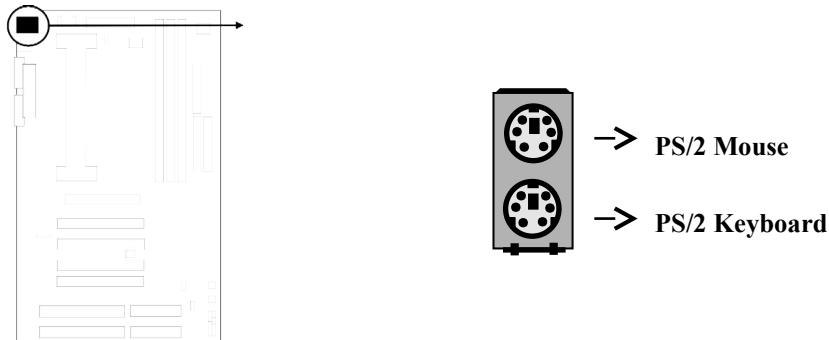
Pin 1 →

| PIN | ROW2          | ROW1                   |
|-----|---------------|------------------------|
| 1   | 3.3V          | 3.3V                   |
| 2   | -12V          | 3.3V                   |
| 3   | GND           | GND                    |
| 4   | Soft Power On | 5V                     |
| 5   | GND           | GND                    |
| 6   | GND           | 5V                     |
| 7   | GND           | GND                    |
| 8   | -5V           | Power OK               |
| 9   | +5V           | +5VSB (for Soft Logic) |
| 10  | +5V           | +12V                   |

ATX Power Connector

### 2. PS/2 Mouse & PS/2 Keyboard Connector: KB/MS

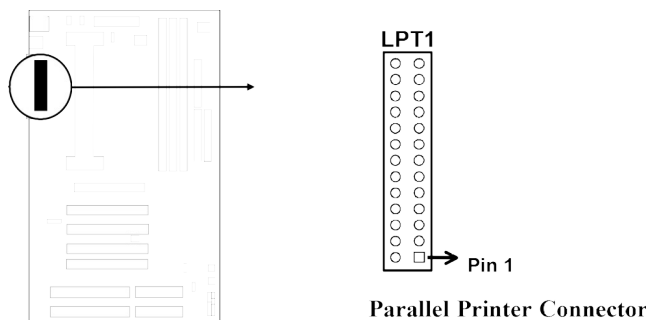
The PS/2 Keyboard is a 6-pin miniature DIN connector. It is for a standard PS/2 style keyboard. May also be known as a 101 enhanced keyboard. The PS/2 Mouse connector is a similar 6-pin, miniature DIN connector.



### 3. Parallel Printer Connector (26-pin block): LPT1

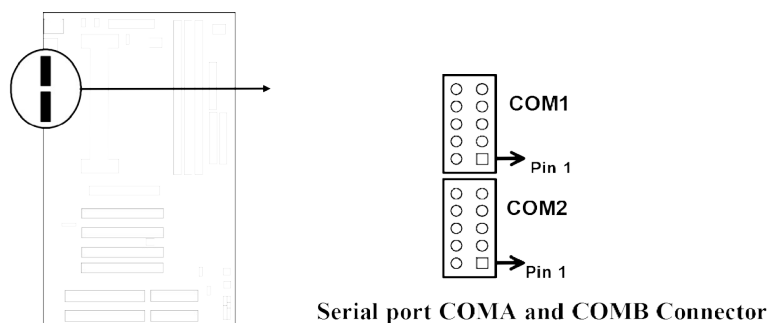
Connection for the included parallel port ribbon cable with mounting bracket. Connect the ribbon cable to this connection and mount the bracket to the case on an open slot. It will then be available for a parallel printer cable.

**NOTE:** Serial printers must be connected to the serial port. You can enable the parallel port and choose the IRQ through BIOS Setup on [page 25](#) “Onboard Parallel Port”.



### 4. Serial port COMA and COMB Connector (Two 10-pin block): COM1, COM2

These connectors support the provided serial port ribbon cables with mounting bracket. Connect the ribbon cables to these connectors and mount the bracket to the case on an open slot. The two serial ports on the mounting bracket will then be used for pointing devices or other serial devices. See [page 25](#) for BIOS configuration of “Onboard Serial Port”

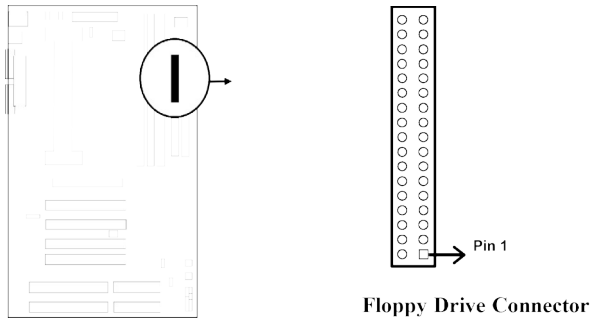


### 5. Floppy drive Connector (34-pin block): FDD1

---

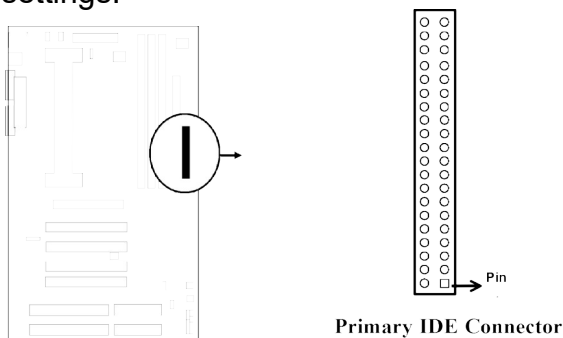
---

This connector supports the provided floppy drive ribbon cable. After connecting the single plug end to motherboard, connect the two plugs at other end to the floppy drives.



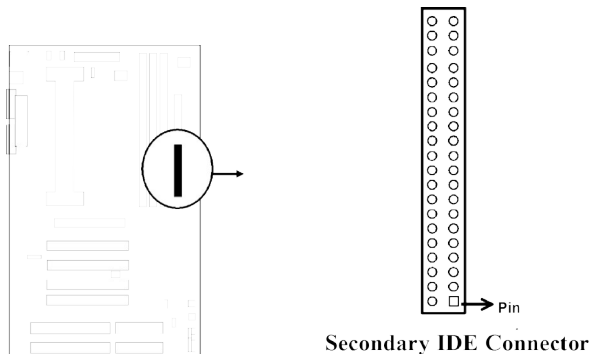
### 6. Primary IDE Connector (40-pin block): IDE1

This connector supports the provided IDE hard disk ribbon cable. After connecting the single plug end to motherboard, connect the two plugs at other end to your hard disk(s). If you install two hard disks, you must configure the second drive to Slave mode by setting its jumpers accordingly. Please refer to the documentation of your hard disk for the jumper settings.

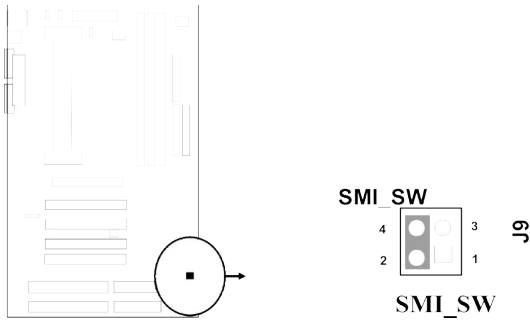


### 7. Secondary IDE Connector (40-pin block): IDE2

This connector connects to the next set of Master and Slave hard disks. Follow the same procedure described for the primary IDE connector. You may also configure two hard disks to be both Masters using one ribbon cable on the primary IDE connector and another ribbon cable on the secondary IDE connector.

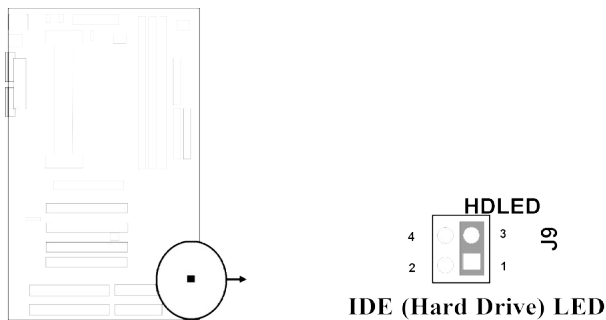


### 8. SMI Suspend Switch lead: SMI\_SW (J9 2&4)



### 9. IDE activity LED: HDLED (J9 1&3)

This connector connects to the hard disk activity indicator light on the case.



### 10. Turbo LED switch: TBLED

The motherboard's turbo function is always on. The turbo LED will remain constantly lit while the system power is on. You may wish to connect the Power LED from the system case to this lead. See the figure on [page 16](#).

### 11. Reset switch lead: RST

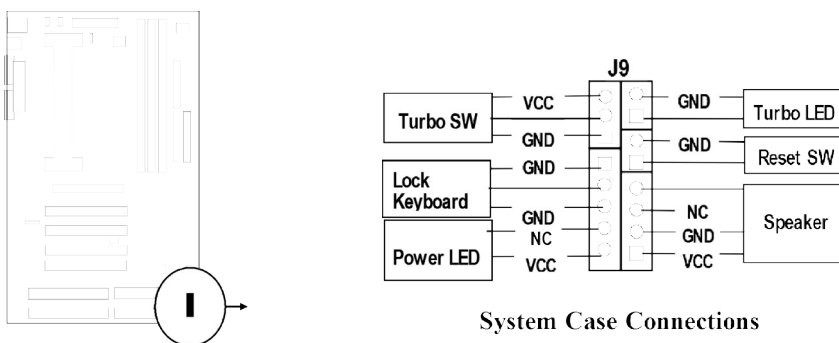
This 2-pin connector connects to the case-mounted reset switch for rebooting your computer without having to turn off your power switch. This is a preferred method of rebooting in order to prolong the life of the system's power supply. See the figure below.

### 12. Keyboard lock switch lead: KEYLOCK

This 5-pin connector connects to the case-mounted key switch for locking the keyboard for security purposes. See the figure below.

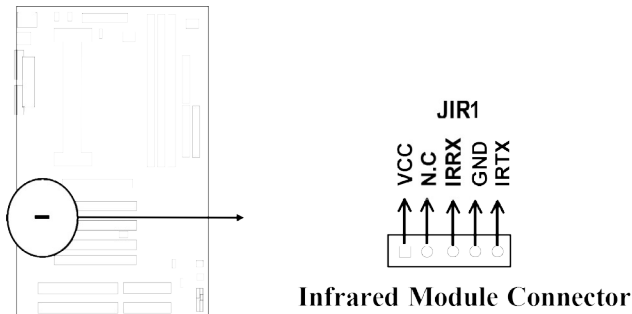
### 13. Speaker connector: SPEAKER

This 4-pin connector connects to the case-mounted speaker. See the figure below.

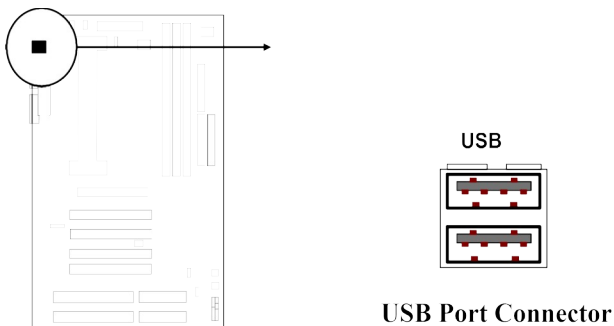


### 14. IR infrared module connector: JIR1

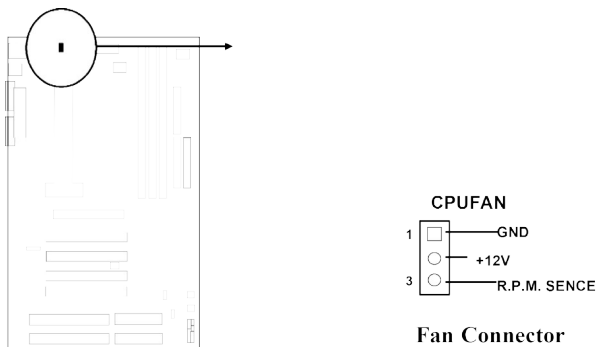
This connector supports the optional wireless transmitting and receiving infrared module. This module mounts to small opening on system cases that support this feature you must also configure the setting through BIOS setup. Use the four pins as shown on the Back View and connect a ribbon cable from the module to the motherboard according to the pin definitions.



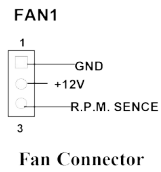
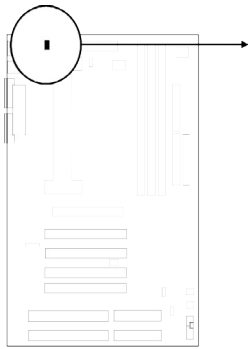
**15. USB Port connector: UBS1**



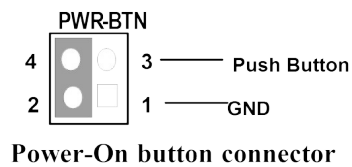
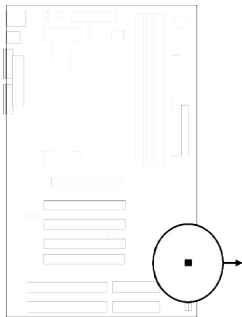
**16. FAN connector: CPUFAN**



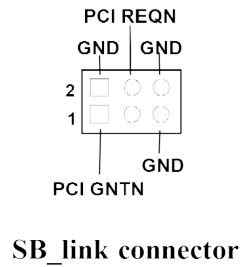
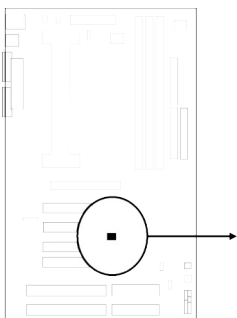
**17. FAN connector: FAN1**



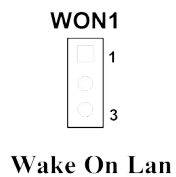
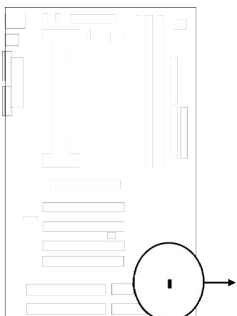
**18. Power-On button connector: PWR\_BTN(J5 2&4)**



**19. SB LINK connector: SB\_LINK1**



**20. Wake On Lan connector: WON1**



## Chapter 3

### AWARD BIOS SETUP





time, type of hard disk drive installed in the system, floppy drive type, and the type of display monitor. Memory size is auto-detected by the BIOS and displayed for your reference. When a field is highlighted (direction keys to move cursor and <Enter> key to select), the entries in the field will be changed by pressing <PgDn> or <PgUp> keys or user can enter new data directly from the keyboard.

**ROM PCI/ISA BIOS (2A6LFJ1B)**  
**STANDARD CMOS SETUP**  
**AWARD SOFTWARE, INC.**

|  |  |
|--|--|
| Date (mm:dd:yy) : THU, Aug, 20 1998  |  |
| Time (hh:mm:ss) : 17 : 22 : 32   |  |
| <b>HARD DISKS      TYPE   SIZE   CYLS   HEAD   PRECOMP   LANDZ   SECTOR   MODE</b> |  |
| Primary Master   | : Auto    0   0   0    0   0    0 Auto   |
| Primary Slave  | : Auto    0   0   0    0   0    0 Auto   |
| Secondary Master   | : Auto    0   0   0    0   0    0 Auto   |
| Secondary Slave  | : Auto    0   0   0    0   0    0 Auto   |
| Drive A : 1.44M , 3.5 in.  | Base Memory : OK<br>Extended Memory : OK<br>Other Memory : 512K<br><hr style="width: 50%; margin: 5px auto;"/> Total Memory : 512K |
| Drive B : None   |  |
| Video : EGA/VGA  |  |
| Halt On : All Errors   |  |
| Esc : Quit            ↑↓→← : Select Item      Pu/Pd/+/- : Modify                   |  |
| F1 : Help            (Shift)F2: Change Color                                       |  |

Figure 3-2

**NOTE:** *If hard disk Primary Master/Slave and Secondary Master/Slave were used Auto, than the hard disk size and model will be auto-detect on display during POST.*

**NOTE:** *The "Halt On:" field is to determine when to halt the system by the BIOS is error occurred during POST.*

## 3-2 BIOS FEATURES SETUP

Select the "BIOS FEATURES SETUP" option in the CMOS SETUP UTILITY menu allows user to change system related parameters in the displayed menu. This menu shows all of the manufacturer's default values of this motherboard. Again, user can move the cursor by pressing direction keys and <PgDn> of <PgUp> keys to modify the parameters. Pressing [F1] key to display help message of the selected item.

This setup program also provide 2 convinent ways to load the default parameter data from BIOS [F6] or CMOS [F7] area if shown data is corrupted. This provides the system a capability to recover from any possible error.

**ROM PCI/ISA BIOS (2A6LFJ1B)**  
**BIOS FEATURES SETUP**  
**AWARD SOFTWARE, INC.**

|  |  |
|--|--|
| Virus Warning        : Disabled<br>CPU Internal Cache    : Enabled | Video BIOS Shadow    : Enabled<br>C8000-CBFFF Shadow    : Disabled |
|--|--|

|  |   |
|--|---|
| External Cache : Enabled<br>CPU L2 Cache ECC Checking : Enabled<br>Quick Power On Self Test : Enabled<br>Boot Sequence : A,C,SCSI<br>Swap Floppy Drive : Disabled<br>Boot Up Floppy Seek : Enabled<br>Boot Up Numlock Status : On<br>Gate A20 Option : Fast<br>Memory Parity/ECC Check : Enabled<br>Typematic Rate Setting : Disabled<br>Typematic Rate (Chars/Sec) : 6<br>Typematic Rate Delay (Msec): 250<br>Security Option : Setup<br>IDE Second Channel Control : Enabled<br>PCI/VGA Palette Snoop : Disabled<br>OS Select For DRAM > 64MB : Non-OS2<br>HDD S.M.A.R.T Capability : Disabled<br>Report No FDD For WIN 95 : YES | CC000-CFFFF Shadow : Disabled<br>D0000-D3FFF Shadow : Disabled<br>D4000-D7FFF Shadow : Disabled<br>D8000-D8FFF Shadow : Disabled<br>DC000-DFFFF Shadow : Disabled |
|  | Esc: Quit    ↑↓→← : Select Item<br>F1 : Help     Pu/Pd/+/-:Modify<br>F5 : Old Values (Shift)F2 : Color<br>F6 : Load BIOS Defaults<br>F7 : Load Setup Defaults     |

Figure 3-3

**NOTE:** The Security Option contains "setup" and "system". The "setup" indicates that the password setting is for CMOS only while the "system" indicates the password setting is for both CMOS and system boot up procedure.

- Virus Warning:** This category flashes on the screen. During and after the system boots up, any attempt to write to the boot sector or partition table of the hard disk drive will halt the system and the following error message will appear, in the mean time, you can run an anti-virus program to locate the problem. Default value is Disabled  
**Enabled:** Activates automatically when the system boots up causing a warning message to appear when anything attempts to access the boot sector or hard disk partition table.  
**Disabled:** No warning message to appear when anything attempts to access the boot sector or hard disk partition table.
- CPU Internal Cache / External Cache:** These two categories speed up memory access. However, it depends on CPU/chipset design. The default value is Enable. If your CPU without Internal Cache then this item "CPU Internal Cache" will not be show.  
**Enabled:** Enable cachehis category speeds up Power On Self Test. (POST) after you power on the computer.  
**Disabled:** Disable cache
- Quick Power On Self Test:** If it is set to Enable, BIOS will shorten or skip some check items during POST.

---

---

**Enabled:** Enable quick POST

**Disabled:** Normal POST

- **Boot Sequence:** This category determines which drive computer searches first for the DOS (Disk Operating System). Default value is A,C,SCSI.  
System will first search for floppy disk drive, then hard disk drive and then SCSI device. The options are: C, A, SCSI; C, CDROM, A; CDROM, C, A; D, A, SCSI; E, A, SCSI; F, A, SCSI; SCSI, A, C; SCSI, C, A; C only; LS/ZIP, C.
- **Swap Floppy Drive:** The swap floppy drive. Default value is Disabled.  
**Enabled:** Floppy A & B will be swapped under the DOS  
**Disabled:** Floppy A & B will be not swap
- **Boot Up Floppy Seek:** During POST, BIOS will determine if the floppy disk drive installed is 40 or 80 tracks. 360K type is 40 tracks while 760K, 1.2M and 1.44M are all 80 tracks. The default value is Enabled.  
**Enabled:** BIOS searches for floppy disk drive to determine if it is 40 or 80 tracks.  
Note that BIOS can not tell from 720K, 1.2M or 1.44M drive type as they are all 80 tracks.  
**Disabled:** BIOS will not search for the type of floppy disk drive by track number.  
Note that there will not be any warning message if the drive installed is 360K.
- **Boot Up NumLock Status:** The default value is On.  
**On:** Keypad is number keys.  
**Off:** Keypad is arrow keys.
- **Boot UP System Speed:** It selects the default system speed-the speed that the system will run at immediately after power up.  
**High:** Set the speed to high.  
**Low:** Set the speed to low.

**NOTE: The board default value is LOW in the field. Boot the system to controller turbo or De-turbo by Onboard (Turbo Switch).**

- **Gate A20 Option:** The default value is Fast.  
**Normal:** The A20 signal is controlled by keyboard controller or chipset hardware.  
**Fast:** Default: Fast. The A20 signal is controlled by Port 92 or chipset specific method.
- **Typematic Rate Setting:** This determines the typematic rate.  
**Enabled:** Enable typematic rate and typematic delay programming.  
**Disabled:** Disable typematic rate and typematic delay programming. The system BIOS will use default value of this 2 items and the default is controlled by keyboard.
- **Typematic Rate (Chars/Sec):**

|    |                            |    |                            |
|----|----------------------------|----|----------------------------|
| 6  | : 6 characters per second  | 8  | : 8 characters per second  |
| 10 | : 10 characters per second | 12 | : 12 characters per second |
| 15 | : 15 characters per second | 20 | : 20 characters per second |

---

---

24 : 24 characters per second                      30 : 30 characters per second

- **Typematic Delay (Msec):** When holding a key, the time between the first and second character displayed.
  - 250 : 250 msec
  - 500 : 500 msec
  - 750 : 750 msec
  - 1000 : 1000 msec
- **Security Option:** This category allows you to limit access to the system and Setup, or just to Setup. The default value is Setup.
  - System:** The system will not boot and access to Setup will be denied if the correct password is not entered at the prompt.
  - Setup:** The system will boot, but access to Setup will be denied if the incorrect password is entered at the prompt.

**NOTE:** *To disable security, select **PASSWORD SETTING** at Main Menu and then you will be asked to enter password. Do not type anything and just press <Enter>, it will disable security. Once the security is disabled, the system will boot and you can enter Setup freely.*

- **Video BIOS Shadow:** It determines whether video BIOS will be copied to RAM, however, it is optional from chipset design. Video Shadow will increase the video speed.
  - Enabled:** Video shadow is enabled
  - Disabled:** Video shadow is disabled
- C8000 - CBFFF Shadow:  
CC000 - CFFFF Shadow:  
D0000 - D3FFF Shadow:  
D4000 - D7FFF Shadow:  
D8000 - DBFFF Shadow:  
DC000 - DFFFF Shadow:  

These categories determine whether optional ROM will be copied to RAM by 16K byte or 32K byte per/unit and the size depends on chipset.

  - Enabled:** Optional shadow is enabled.
  - Disabled:** Optional shadow is disabled.

### 3-3 CHIPSET FEATURES SETUP

Choose the "CHIPSET FEATURES SETUP" in the CMOS SETUP UTILITY menu to display following menu.

ROM PCI/ISA BIOS (2A6LFJ1B)

CHIPSET FEATURES SETUP  
AWARD SOFTWARE, INC.

|                                    |                                    |
|------------------------------------|------------------------------------|
| Bank 0/1 DRAM Timing : FP/EDO 70ns | Onchip USB : Enabled               |
| Bank 2/3 DRAM Timing : FP/EDO 70ns | USB Keyboard Support : Disable     |
| Bank 4/5 DRAM Timing : FP/EDO 70ns | Auto Detect DIMM/PCI Clk : Enabled |

|  |  |
|--|--|
| SDRAM Cycle Length : 3<br>Memory Hole At 15MB Addr.: Disable<br>Read Around write : Disable<br>AGP Aperture Size : 64M | Spread Spectrum Modulated: Disable<br>CPU HOST/PCI Clock : Default<br>CPU Warning temperature : Disable<br>Current CPU temperature : 40°C/104°F<br>Current CPU FAN Speed : 4500 Only<br>Current FAN1 Speed : 0 in<br>Shutdown Temperature : 60°C/140°F |
|  | Esc: Quit    ↑↓→←: Select Item<br>F1 : Help    Pu/Pd/+/-:Modify<br>F5 : Old Values (Shift)F2 : Color<br>F6 : Load BIOS Defaults<br>F7 : Load Setup Defaults  |

Figure 3-4

- **Bank 0/1,2/3** : The DRAM timing of Bank 0/1,2/3 in this field is set by the system board manufacturer, depending on whether the board had fast paged DRAMs or EDO(extended data output)DRAMs.  
The Choice : Normal,Medium,Fast,Turbo,FP/EDO 60ns,FP/EDO 70ns.
- **SDRAM Cycle latency** : The field sets the CAS latency timing.  
The Choice : 2,3.
- **Read Around Write** : DRAM optimization feature : If a memory read is addressed to a location whose latest write is being held in a buffer before being written to memory, the read is satisfied through the buffer contents ,and the read is not sent to the DRAM.  
The choice : Enabled,Disabled.
- **AGP Aperture Size** : Select the size of the Accelerated Graphics Port(AGP) aperture. The aperture is a portion of the PCI memory address range dedicated for graphics memory address space. Host cycles that hit the aperture range are forwarded to the AGP without any translation. See [www.agpforum.org](http://www.agpforum.org) for AGP information.  
The choice : 4,8,16,32,64,128,256
- **OnChip USB** : This should be enabled if your system has a USB inatlated on the system board and you wish to use it. Even when so equipped,if you add a higher performance controller,you will need to disable this feature.  
The choice : Enabled,Disabled.
- **CPU HOST/PCI Clock** : This field can select CPU HOST Clock/PCI Clock 66/33MHz, 75/37MHz,83/33MHz,95/32MHz,100/33MHz,112/37MHz,115/38MHz,120/40MHz,124/31MHz,133/33MHz,140/35MHz,Default. When choose Default the frequency is setting by U2. We recommend users setting the standard frequency of CPU, because over clock usually make system unstable or hang up. If users can't enter BIOS SETUP to setting default value, please hold "INS" Key before power on and release it after system have screen.
- **USB Keyboard Support** : Select *Enabled* if your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard .  
The choice : Enabled,Disabled.

### 3-4 POWER MANAGEMENT SETUP

Choose the "POWER MANAGEMENT SETUP" in the CMOS SETUP UTILITY to display the following screen. This menu allows user to modify the power management parameters and

IRQ signals. In general, these parameters should not be changed unless it's absolutely necessary.

ROM PCI/ISA BIOS (2A6LFJ1B)

POWER MANAGEMENT SETUP

AWARD SOFTWARE, INC.

|                                   |                                 |
|-----------------------------------|---------------------------------|
| ACPI function : Enabled           | Primary INTR : ON               |
| Power Management : Disabled       | IRQ3 (COM 2) : Primary          |
| PM Control by APM : Yes           | IRQ4 (COM 1) : Primary          |
| Video Off Option : Suspend->off   | IRQ5 (LPT 2) : Primary          |
| Video Off Method : V/H SYNC+Blank | IRQ6 (Floppy Disk) : Primary    |
| MODEM Use IRQ : 3                 | IRQ7 (LPT 1) : Primary          |
| Soft-off by PWRBTN : Instant-off  | IRQ8 (RTC Alarm) : Disabled     |
| ** PM Timers **                   | IRQ9 (IRQ2 Redir) : Secondary   |
| HDD Power Down : Disabled         | IRQ10 (Reserved) : Secondary    |
| Doze Mode : Disabled              | IRQ11 (Reserved) : Secondary    |
| Suspwnd Mode : Disabled           | IRQ12 (PS/2 Mouse) : Primary    |
| ** PM Events **                   | IRQ13 (Coprocessor) : Primary   |
| VGA : OFF                         | IRQ14 (Hard Disk) : Primary     |
| LPT & COM : LPT/COM               | IRQ15 (Reserved) : Disabled     |
| HDD & FDD : ON                    |                                 |
| DMA/master : OFF                  |                                 |
| Modem Ring Resume : Disabled      |                                 |
| RTC Alarm Resume : Disabled       |                                 |
|                                   | Esc: Quit ↑↓→←: Select Item     |
|                                   | F1: Help Pu/Pd/+/-: Modify      |
|                                   | F5: Old Values (Shift)F2: Color |
|                                   | F6: Load BIOS Defaults          |
|                                   | F7: Load Setup Defaults         |

Figure 3-6

Again, user can move the cursor by pressing direction keys to the field need to be modified and press <PgDn> or <PgUp> to alter item selection. You can only change the content of Doze Mode, Standby Mode, and Suspend Mode when the Power Management is set to 'User Define'.

### 3-4-1 The Description of the Power Management

- Power Management mode selection:

**Disabled:** The system operates in NORMAL conditions (Non-GREEN), and the Power Management function is disabled.

**Max.saving:** This mode will maximize the power saving capability.

**Min.saving:** This mode will minimize the power saving capability.

**User define:** Allow user to define timeout parameters to control power saving timing. Refer item shown below:

- Timeout parameters:

#### HDD Standby

HDD Standby timer can be set from 1 to 15 minute(s).

#### System Doze

The "System Doze" mode timer starts to count when there is no "PM events" occurred. The valid timeout setting is from 1 minute up to 1 hour.

#### System Standby

---

---

The "Standby" mode timer starts to count when "System Doze" mode timer timed out and no "PM events" occurred. Valid range is from 1 minute up to 1 hour.

### **System Suspend**

This function works only when the Pentium CPU is installed. The timer starts to count when "System Standby" mode timer timed out and no "PM Events" occurred. Valid range is from 1 minute up to 1 hour.

## **3-4-2 Description of the Green Functions**

This motherboard supports HDD Power Down, Doze and standby power saving functions when Intel Pentium Processor CPU is installed. The suspend function is also supported when the SMI switch is short (ref. [page 13](#)). The detail description of these functions are provided as following:

### **HDD Standby Mode**

When system stop reading or writing HDD, the timer starts to count. The system will cut off the HDD power when timer ran out of time. The system will not resume operation until either a read from or a write to HDD command is executed again.

### **Doze Mode**

The system hardware will drop down CPU clock from normal working speed when Doze mode timeout occurred.

### **Standby Mode**

When the system standby mode timer ran out, it will enter the standby mode and retain CPU at slow working speed. The screen will be blanked out.

### **Suspend Mode**

When the system suspend timer time out, the system will enter the suspend mode and the chipset will stop CPU clock immediately. The power consumption in Suspend Mode is lower than in standby mode. The screen is also blanked out.

### **PM Events:**

AWARD BIOS defines 15 PM Events in the power management mode (Doze, standby & suspend). The user can initial any PM Events to be "Enable" or "Disable". When the system detects all of the enabled events do not have any activity, it will start the system Doze timer first if the "Power Management" isn't "Disabled". Once the system Doze timer timed out, it will process doze power saving procedure by starting the system standby timer. When the standby timer ran out and all of the "Enabled" events remains silent, the system will enter the standby mode. By now, the system will not only process the standby power saving procedures but also start the system suspend timer. When the suspend timer time out, all of the CPU clock will be stopped by dropping system clock down to zero and remains this way until any one of the "Enabled" event occurred.

## **3-5 PNP/PCI CONFIGURATION SETUP**

This "PNP and PCI configuration Setup" option configures the PCI bus slots. All PCI bus slots on the system use INTA#, thus all installed PCI cards must be set to this value.

ROM PCI/ISA BIOS (2A6LFJ1B)

PNP/PCI CONFIGURATION

AWARD SOFTWARE, INC.

|                  |      |                         |           |
|------------------|------|-------------------------|-----------|
| PNP OS Installed | : No | CPU to PCI Erite Buffer | : Enabled |
|------------------|------|-------------------------|-----------|



|  |   |
|--|---|
| Resources Controlled By : Manual<br>Reset Configuration Data: Disabled<br><br>IRQ-3 assigned to : PCI/ISA PnP<br>IRQ-4 assigned to : PCI/ISA PnP<br>IRQ-5 assigned to : PCI/ISA PnP<br>IRQ-7 assigned to : Legacy ISA<br>IRQ-9 assigned to : PCI/ISA PnP<br>IRQ-10 assigned to : PCI/ISA PnP<br>IRQ-11 assigned to : PCI/ISA PnP<br>IRQ-12 assigned to : PCI/ISA PnP<br>IRQ-14 assigned to : Legacy ISA<br>IRQ-15 assigned to : Legacy ISA<br>DMA-0 assigned to : PCI/ISA PnP<br>DMA-1 assigned to : PCI/ISA PnP<br>DMA-3 assigned to : PCI/ISA PnP<br>DMA-5 assigned to : PCI/ISA PnP<br>DMA-6 assigned to : PCI/ISA PnP<br>DMA-7 assigned to : PCI/ISA PnP | PCI Dynamic Bursting : Enabled<br>PCI Master 0 WS Write : Enabled<br>PCI Delay Transaction : Enabled<br>PCI Master Read Prefetch: Enabled<br>PCI#2 Access #1 Retry : Disabled<br>AGP Master 1 WS Write : Enabled<br>AGP Master 1 WS Read : Disabled<br><br>PCI IRQ Activated By : Level<br>Assign IRQ For USB : Enabled<br>Assign IRQ For VGA : Enabled<br>Assign IRQ For ACPI : IRQ10<br><br>Esc: Quit ↑↓→←: Select Item<br>F1 : Help Pu/Pd/+/-:Modify<br>F5 : Old Values (Shift)F2 : Color<br>F6 : Load BIOS Defaults<br>F7 : Load Setup Defaults |
|--|---|

Figure 3-7

### IRQxx assigned to

These fields indicate whether or not the displayed IRQ for each field is being used by a Legacy (non-PnP) ISA card. Two options are available: "PCI/ISA PnP" and "Legacy ISA". The first option the default value, indicates either that the displayed IRQ is not used or an PCI/ISA PnP is being used to determine if an ISA card is using that IRQ. Second option is for Legacy ISA card that requires a unique IRQ, and you are not using an PCI/ISA PnP, you must set the field for that IRQ to Legacy ISA.

For example: If you install a Legacy ISA card that requires IRQ10 lets say, then set "IRQ10 assigned to Legacy ISA".

### DMAxx assigned to

These fields indicate whether or not the displayed DMA channel for each field is being used by a Legacy (non-PnP) ISA card. Available options include: "PCI/ISA PnP" and "Legacy ISA". When is option "PCI/ISA PnP" indicates either that the displayed DMA channel is not used or an PCI/ISA PnP being used to determine if an ISA card is using that channel. If you install a Legacy ISA card that requires a unique DMA channel, and you are not using an PCI/ISA PnP, you must set the field for that channel to "Legacy ISA".

## 3-6 LOAD BIOS DEFAULTS

The "LOAD BIOS DEFAULTS" function loads the system default data directly from ROM and initialize associated hardware properly. This function will be necessary only when the system CMOS data is corrupted.

ROM PCI/ISA BIOS (2A6LFJ1B)

CMOS SETUP UTILITY  
AWARD SOFTWARE, INC.

|                     |                        |
|---------------------|------------------------|
| STANDARD CMOS SETUP | INTEGRATED PERIPHERALS |
|---------------------|------------------------|

|  |  |
|--|--|
| <b>BIOS FEATURES SETUP</b><br><b>CHIPSET FEATURES SETUP</b><br><b>POWER MANAGEMENT</b><br><b>PNP/PCI CONFIGUR</b><br><b>LOAD BIOS DEFAULTS</b><br><b>LOAD SETUP DEFAULTS</b> | <b>SUPERVISOR PASSWORD</b><br><b>USER PASSWORD</b><br><b>TECTION</b><br><b>ORMAT</b><br><b>SAVE &amp; EXIT SETUP</b><br><b>EXIT WITHOUT SAVING</b> |
| <b>Esc : QUIT</b> <b>↑↓→← : Select Item</b><br><b>F10 : Save &amp; Exit Setup</b> <b>(Shift) F2 : Change Color</b>   |  |
| <b>Time, Date, Hard Disk Type...</b>   |  |

Figure 3-8

### 3-7 LOAD SETUP DEFAULTS

The "**LOAD SETUP DEFAULTS**" function loads the system default data directly from ROM and initialize associated hardware properly. This function will be necessary only when the system CMOS data is corrupted.

ROM PCI/ISA BIOS (2A69JJ1B)

**CMOS SETUP UTILITY**  
**AWARD SOFTWARE, INC.**

|  |  |
|--|--|
| <b>STANDARD CMOS SETUP</b><br><b>BIOS FEATURES SETUP</b><br><b>CHIPSET FEATURES SETUP</b><br><b>POWER MANAGEMENT SETUP</b><br><b>PNP/PCI CONFIGUR</b><br><b>LOAD BIOS DEFAUL</b><br><b>LOAD SETUP DEFAULTS</b> | <b>INTEGRATED PERIPHERALS</b><br><b>SUPERVISOR PASSWORD</b><br><b>USER PASSWORD</b><br><b>IDE HDD AUTO DETECTION</b><br><b>EXIT WITHOUT SAVING</b> |
| <b>Load SETUP Default (Y/N)? Y</b>   |  |
| <b>Esc : QUIT</b> <b>↑↓→← : Select Item</b><br><b>F10 : Save &amp; Exit Setup</b> <b>(Shift) F2 : Change Color</b>   |  |
| <b>Time, Date, Hard Disk Type...</b>   |  |

Figure 3-9

### 3-8 INTEGRATED PERIPHERALS SETUP

ROM PCI/ISA BIOS (2A69JJ1B)

**INTEGRATED PERIPHERALS**  
**AWARD SOFTWARE, INC.**

|  |  |
|--|--|
| <b>OnChip IDE Channel0 : Enabled</b><br><b>OnChip IDE Channel1 : Enabled</b><br><b>IDE Prefetch Mode : Enabled</b> | <b>Onboard Parallel Port : 378/IRQ7</b><br><b>Onboard Parallel Mode : SPP</b><br><b>ECP Mode Use DMA : 3</b> |
|--|--|

|  |  |
|--|--|
| IDE HDD Block Mode : Enabled<br>Primary Master PIO : Auto<br>Primary Slave PIO : Auto<br>Secondary Master PIO : Auto<br>Secondary Slave PIO : Auto<br>Primary Master UDMA : Auto<br>Primary Slave UDMA : Auto<br>Secondary Master UDMA : Auto<br>Secondary Slave UDMA : Auto<br>Init Display First : PCI Slot<br>Onboard FDD Controller : Enabled<br>Onboard Serial Port 1 : 3F8/IRQ4<br>Onboard Serial Port 2 : 2F8/IRQ3<br>UART 2 Mode : Normal<br>IR Function Duplex : Half<br>Rx/D , Tx/D Active : Hi,Hi | Parallel Port EPP Type : EPP1.9<br><br><br><br><br><br><br><br><br><br><br>Esc: Quit    ↑↓→← : Select Item<br>F1 : Help     Pu/Pd/+/-:Modify<br>F5 : Old Values (Shift)F2 : Color<br>F6 : Load BIOS Defaults<br>F7 : Load Setup Defaults |
|--|--|

Figure 3-10

**NOTE:** *If you don't use the Onboard IDE connector, than use On-card (PCI or ISA card) IDE connector. You will set Onboard Primary IDE: Disabled an Onboard Secondary IDE: Disabled from CHIPSET FEATURES SETUP UTILITY. The Onboard PCI IDE cable should be equal to or less than 18 inches (45 cm).*

- **IDE HDD Block Mode:** The default value is Enabled.  
**Enabled:** Enabled IDE HDD Block Mode. The HDD transfer rate is better than Disable.  
**Disabled:** Disable IDE HDD Block Mode.
- **IDE Primary Master PIO:** The default value is Auto.  
**Auto:** BIOS will automatically detect the Onboard Primary Master PCI IDE HDD Accessing mode.  
**Mode0~4:** Manually set the IDE Accessing mode.
- **IDE Primary Slave PIO:** The default value is Auto.  
**Auto:** BIOS will automatically detect the Onboard Primary Slave PCI IDE HDD Accessing mode.  
**Mode0~4:** Manually set the IDE Accessing mode.
- **IDE Secondary Master PIO:** The default value is Auto.  
**Auto:** BIOS will automatically detect the Onboard Secondary Master PCI IDE HDD Accessing mode.  
**Mode0~4:** Manually set the IDE Accessing mode.
- **IDE Secondary Slave PIO:** The default value is  
**Auto:** BIOS will automatically detect the Onboard Secondary Slave PCI IDE HDD Accessing mode.  
**Mode0~4:** Manually set the IDE Accessing mode.
- **Onboard Primary PCI IDE:** The default value is Enabled.  
**Enabled:** Enable Onboard 1 st channel IDE port.

- 
- Disabled:** Disable Onboard 1 st channel IDE port. When use On-card (PCI or ISA card) IDE connector.
  - **Onboard Secondary PCI IDE:** The default value is Enabled.
    - Enabled:** Enable Onboard 2 nd channel IDE port.
    - Disabled:** Disable Onboard 2 nd channel IDE port. When use On-card (PCI or ISA card) IDE connector.
  - **PCI Slot IDE 2nd Channel:** The default value is Enabled.
    - Enabled:** Enable secondary IDE port and BIOS will assign IRQ15 for this port.
    - Disabled:** Disable secondary IDE port and IRQ15 is available for other device.
  - **Onboard FDC Controller:** The default value Enabled.
    - Enabled:** Enable the Onboard floppy drive interface controller.
    - Disabled:** Disable the Onboard floppy drive interface controller. When use On-card ISA FDC's controller.
  - **Onboard Serial Port 1:** This field allows the user to select the serial port. The default value is COM1.
    - COM1:** Enable Onboard Serial port 1 and address is 3F8H.
    - COM2:** Enable Onboard Serial port 1 and address is 2F8H.
    - COM3:** Enable Onboard Serial port 1 and address is 3E8H.
    - COM4:** Enable Onboard Serial port 1 and address is 2E8H.
    - Disabled:** Disable Onboard CHIP's Serial port 1.
  - **Onboard Serial port 2:** This field allows the user to select the serial port. The default value is COM2.
    - COM1:** Enable Onboard Serial port 2 and address is 3F8H.
    - COM2:** Enable Onboard Serial port 2 and address is 2F8H.
    - COM3:** Enable Onboard Serial port 2 and address is 3E8H.
    - COM4:** Enable Onboard Serial port 2 and address is 2E8H.
    - Disabled:** Disable Onboard CHIP's Serial port 2.
  - **Onboard Parallel port:** This field allows the user to select the LPT port. The default value is 378H.
    - 378H:** Enable Onboard LPT port and address is 378H.
    - 278H:** Enable Onboard LPT port and address is 278H.
    - 3BCH:** Enable Onboard LPT port and address is 3BCH.
    - Disabled:** Disable Onboard CHIP's LPT port.

**NOTE:** *Parallel Port address is 378H/3BCH that selects the routing of IRQ7 for LPT1.  
Parallel Port address is 278H that selects the routing of IRQ5 LPT1.*

- **Onboard Parallel port Mode:** This field allows the user to select the parallel port mode. The default value is ECP + EPP.
  - Normal:** Standard mode. IBM PC/AT Compatible bidirectional parallel port.
  - EPP:** Enhanced Parallel Port mode.
  - ECP:** Extended Capabilities Port mode.
  - EPP+ECP:** ECP Mode & EPP Mode.
- **Power On Function:** This field allower the user to select Power On method by button, password or hot-key.



|            |                         |                    |
|------------|-------------------------|--------------------|
|            |                         |                    |
| Esc : Quit | ↑↓→← : Select Item      | Pu/Pd/+/- : Modify |
| F1 : Help  | (Shift)F2: Change Color |                    |

Figure 3-11

**NOTE: HDD Modes**

*The Award BIOS supports 3 HDD modes : NORMAL, LBA & LARGE*

**NORMAL mode**

Generic access mode in which neither the BIOS nor the IDE controller will make any transformations during accessing.

The maximum number of cylinders, head & sectors for NORMAL mode are 1024, 16 & 63.

|               |                |        |
|---------------|----------------|--------|
|               | no. Cylinder   | (1024) |
| x             | no. Head       | ( 16)  |
| x             | no. Sector     | ( 63)  |
| x             | no. per sector | ( 512) |
| 528 Megabytes |                |        |

If user set this HDD to NORMAL mode, the maximum accessible HDD size will be 528 Megabytes even though its physical size may be greater than that!

**LBA (Logical Block Addressing) mode**

A new HDD accessing method to overcome the 528 Megabyte bottleneck. The number of cylinders, heads & sectors shown in setup may not be the number physically contained in the HDD.

During HDD accessing, the IDE controller will transform the logical address described by sector, head & cylinder into its own physical address inside the HDD.

The maximum HDD size supported by LBA mode is 8.4 Gigabytes which is obtained by the following formula:

|               |                  |        |
|---------------|------------------|--------|
|               | no. Cylinder     | (1024) |
| x             | no. Head         | ( 255) |
| x             | no. Sector       | ( 63)  |
| x             | bytes per sector | ( 512) |
| 8.4 Gigabytes |                  |        |

**LARGE mode**

Extended HDD access mode supported by Award Software.

Some IDE HDDs contain more than 1024 cylinder without LBA support (in some cases, user do not want LBA). The Award BIOS provides another alternative to support these kinds of LARGE mode:

| <u>CYLS.</u> | <u>HEAD</u> | <u>SECTOR</u> | <u>MODE</u> |
|--------------|-------------|---------------|-------------|
| 1120         | 16          | 59            | NORMAL      |
| 560          | 32          | 59            | LARGE       |

BIOS tricks DOS (or other OS) that the number of cylinders is less than 1024 by dividing it by 2. At the same time, the number of heads is multiplied by 2. Reverse transformation process will be made inside INT 12h in order to access the right HDD address the right HDD address!

---

---

Maximum HDD size:

no. Cylinder (1024)  
x no. Head ( 32)  
x no. Sector ( 63)  
x bytes per sector ( 512)  
1 Gigabytes

**NOTE:**

*To support LBA or LARGE mode of HDDs, there must be some softwares involved. All these softwares are located in the Award HDD Service Routine (INT 13h). It may be failed to access a HDD with LBA (LARGE) mode selected if you are running under a Operating System which replaces the whole INT 13h.*

*UNIX operating systems do not support either LBA or LARGE and must set to the Standard mode. UNIX can support drives larger than 528MB.*

### **3-11 SAVE & EXIT SETUP**

The "SAVE & EXIT SETUP" option will bring you back to boot up procedure with all the changes you just made which are recorded in the CMOS RAM.

### **3-12 EXIT WITHOUT SAVING**

The "EXIT WITHOUT SAVING" option will bring you back to normal boot up procedure without saving any data into CMOS RAM. All of the old data in the CMOS will not be destroyed.

### **3-13 I/O & MEMORY MAP**

#### MEMORY MAP

| Address Range | Size | Description  |
|---------------|------|--|
| 00000-7FFFF   | 512K | Conventional memory                                |
| 80000-9FBFF   | 127K | Extended Conventional memory                       |
| 9FC00-9FFFF   | 1K   | Extended BIOS data area if PS/2 mouse is installed |
| A0000-C7FFF   | 160K | Available for Hi DOS memory                        |
| C8000-DFFFF   | 96K  | Available for Hi DOS memory and adapter ROMs       |
| E0000-EEFFF   | 60K  | Available for UMB                                  |
| EF000-EFFFF   | 4K   | Video service routine for Monochrome & CGA adapter |
| F0000-F7FFF   | 32K  | BIOS CMOS setup utility                            |
| F8000-FCFFF   | 20K  | BIOS runtime service routine (2)                   |
| FD000-FDFFF   | 4K   | Plug and Play ESCD data area                       |
| FE000-FFFFF   | 8K   | BIOS runtime service routine (1)                   |

---

---

## I/O MAP

|         |                                      |
|---------|--------------------------------------|
| 000-01F | DMA controller (Master)              |
| 020-021 | INTERRUPT CONTROLLER (Master)        |
| 022-023 | CHIPSET control registers. I/O ports |
| 040-05F | TIMER control registers              |
| 060-06F | KEYBOARD interface controller (8042) |
| 070-07F | RTC ports & CMOS I/O ports           |
| 080-09F | DMA register                         |
| 0A0-0BF | INTERRUPT controller (Slave)         |
| 0C0-0DF | DMA controller (Slave)               |
| 0F0-0FF | MATH COPROCESSOR                     |
| 1F0-1F8 | HARD DISK controller                 |
| 278-27F | PARALLEL port 2                      |
| 2B0-2DF | GRAPHICS adapter controller          |
| 2F8-2FF | SERIAL port 2                        |
| 360-36F | NETWORK ports                        |
| 378-37F | PARALLEL port 1                      |
| 3B0-3BF | MONOCHROME & PARALLEL port adapter   |
| 3C0-3CF | EGA adapter                          |
| 3D0-CDF | CGA adapter                          |
| 3F0-3F7 | FLOPPY DISK controller               |
| 3F8-3FF | SERIAL port-1                        |

## 3-14 TIME & DMA CHANNELS MAP

**TIME MAP:**           TIMER Channel 0 System timer interrupt  
                          TIMER Channel 1 DRAM REFRESH request  
                          TIMER Channel 2 SPEAKER tone generator

**DMA CHANNELS:**   DMA Channel 0 Available  
                          DMA Channel 1 Onboard ECP (Option)  
                          DMA Channel 2 FLOPPY DISK (SMC CHIP)  
                          DMA Channel 3 Onboard ECP (Default)  
                          DMA Channel 4 Cascade for DMA controller 1  
                          DMA Channel 5 Available



---

---

DMA Channel 6 Available  
DMA Channel 7 Available

### 3-15 INTERRUPT MAP

**NMI:** Parity check error

**IRQ (H/W):**

- 0 System TIMER interrupt from TIMER 0
- 1 KEYBOARD output buffer full
- 2 Cascade for IRQ 8-15
- 3 SERIAL port 2
- 4 SERIAL port 1
- 5 PARALLEL port 2
- 6 FLOPPY DISK (SMC CHIP)
- 7 PARALLEL port 1
- 8 RTC clock
- 9 Available
- 10 Available
- 11 Available
- 12 PS/2 Mouse
- 13 MATH coprocessor
- 14 Onboard HARD DISK (IDE1) channel
- 15 Onboard HARD DISK (IDE2) channel

### 3-16 RTC & CMOS RAM MAP

**RTC & CMOS:**

- 00 Seconds
- 01 Second alarm
- 02 Minutes
- 03 Minutes alarm
- 04 Hours
- 05 Hours alarm
- 06 Day of week
- 07 Day of month
- 08 Month
- 09 Year
- 0A Status register A
- 0B Status register B
- 0C Status register C
- 0D Status register D
- 0E Diagnostic status byte
- 0F Shutdown byte
- 10 FLOPPY DISK drive type byte
- 11 Reserve
- 12 HARD DISK type byte
- 13 Reserve
- 14 Equipment type
- 15 Base memory low byte
- 16 Base memory high byte
- 17 Extension memory low byte

18 Extension memory high byte  
 19-2d  
 2E-2F  
 30 Reserved for extension memory low byte  
 31 Reserved for extension memory high byte  
 32 DATE CENTURY byte  
 33 INFORMATION FLAG  
 34-3F Reserve  
 40-7F Reserved for CHIPSET SETTING DATA

### 3-17 BIOS REFERENCE-POST CODES

ISA PORT codes are typically output to port address 80h.

| Post | Name                                   | Description  |
|------|--|--|
| CO   | Turn Off Chipset Cache                 | OEM Specific-Cache controller.   |
| 1    | Processor Test 1                       | Processor Status (1 FLAGS) Verification.<br>Tests the following processor status flags carry, zero, sign, overflow.<br>The BIOS will set each of these flags, verify they are set, then turn each flag off and verify it is off.   |
| 2    | Processor Test 2                       | Read/ Write/ Verify all CPU registers except SS, SP, and BP with data pattern FF and OO.   |
| 3    | Initialize Chips                       | Disable NMI, PIE, AIE, UEI, SOWV.<br>Disable video, parity checking, DMA.<br>Reset math coprocessor.<br>Clear all page registers, CMOS shutdown byte.<br>Initialize timer 0, 1, and 2, including set EISA timer to a known state.<br>Initialize DMA Controllers 0 and 1.<br>Initialize interrupt controllers 0 and 1.<br>Initialize EISA extended registers. |
| 4    | Test Memory Refresh Toggle             | RAM must be periodically refreshed in order to keep the memory from decaying.<br>This function assures that the memory refresh function is working properly.   |
| 5    | Blank video, Initialize keyboard       | Keyboard controller initialization.  |
| 6    | Reserved                               |  |
| 7    | Test CMOS Interface and Battery Status | Verifies CMOS is working correctly, detects bad battery.   |
| BE   | Chipset Default Initialization         | Program chipset registers with power on BIOS defaults.   |
| C1   | Memory presence test                   | OEM Specific-Test to size on-board memory.   |
| C5   | Early Shadow                           | OEM Specific-Early Shadow enable for fast boot.  |
| C6   | Cache presence test                    | External cache size detection.   |
| 8    | Setup low memory                       | Early chip set initialization.<br>Memory presence test.  |

|       |                              |   |
|-------|------------------------------|---|
|       |                              | OEM chip set routines.<br>Clear low 64K of memory.<br>Test first 64K memory.  |
| 9     | Early Cache Initialization   | Cyrix CPU initialization.<br>Cache initialization.  |
| A     | Setup Interrupt Vector Table | Initialization first 120 interrupt vectors with SPURIOUS_INT_HDLR and initialize INT 00h-1Fh according to INT_TBL     |
| B     | Test CMOS RAM Checksum       | Test CMOS RAM Checksum, if bad, or insert key pressed, load defaults.   |
| C     | Initialize keyboard          | Detect type of keyboard controller (optional) Set NUM_LOCK status.  |
| D     | Initialize Video Interface   | Detect CPU clock.<br>Read CMOS location 14h to find out type of video in use.<br>Detect and Initialize Video Adapter. |
| E     | Test Video Memory            | Test video memory, write sign-on message to screen.<br>Setup shadow RAM.Enable shadow according to Setup.             |
| F     | Test DMA Controller 0        | BIOS checksum test.<br>Keyboard detect and initialization.  |
| 10    | Test DMA Controller 1        |   |
| 11    | Test DMA Page Registers      | Test DMA Page Registers.  |
| 12-13 | Reserved                     |   |
| 14    | Test Timer Counter 2         | Test 8254 Timer 0 Counter 2.  |
| 15    | Test 8259-1 Mask Bits        | Verify 8259 Channel 1 masked interrupts by alternately turning off and on the interrupt lines.                        |
| 16    | Test 8259-2 Mask Bits        | Verify 8259 Channel 2 masked interrupts by alternately turning off and on the interrupt lines.                        |

|       |  |  |
|-------|--|--|
| 17    | Test Stuck 8259's Interrupt Bits       | Turn off interrupts then verify no interrupt mask register is on.                                    |
| 18    | Test 8259 Interrupt Functionality      | Force an interrupt and verify the interrupt occurred.  |
| 19    | Test Stuck NMI Bits (Parity I/O Check) | Verify NMI can be cleared.   |
| 1A    |  | Display CPU clock.   |
| 1B-1E | Reserved                               |  |
| 1F    | Set EISA Mode                          | If EISA non-volatile memory checksum is good, execute EISA initialization. If not, execute ISA tests |

|       |   |  |
|-------|---|--|
|       |   | an clear EISA mode flag.<br>Test EISA Configuration Memory Integrity (checksum & communication interface).   |
| 20    | Enable Slot 0                               | Initialization slot 0 (System Board).  |
| 21-2F | Enable Slot 1-15                            | Initialize slot 1 through 15.  |
| 30    | Size Base and Extended Memory               | Size base memory from 256K to 640K and extended memory above 1MB.  |
| 31    | Test Base and Extended Memory               | Test base memory from 256K to 640K and extended memory above 1MB using various patterns.<br>NOTE: This will be skipped in EISA mode and can be "skipped" with ESC key in ISA mode. |
| 32    | Test EISA Extended Memory                   | If EISA Mode flag is set then test EISA memory found in slots initialization.<br>NOTE: This will be skipped in ISA mode and can be "skipped" with ESC key in EISA mode.            |
| 33-3B | Reserved                                    |  |
| 3C    | Setup Enabled                               |  |
| 3D    | Initialize & install Mouse                  | Detect if mouse is present, initialize mouse, install interrupt vectors.   |
| 3E    | Setup Cache Controller                      | Initialize cache controller.   |
| 3F    | Reserved                                    |  |
| BF    | Chipset Initialization                      | Program chipset registers with Setup values  |
| 40    |   | Display virus protest disable or enable.   |
| 41    | Initialize Floppy Drive & Controller        | Initialize floppy disk drive controller and drives.  |
| 42    | Initialize Hard Drive & controller          | Initialize hard drive controller and any drives.   |
| 43    | Detect & Initialize Serial/Parallel Ports   | Initialize any serial and parallel ports (also game port).   |
| 44    | Reserved                                    |  |
| 45    | Detect & Initialize Math Coprocessor        | Initialize math coprocessor.   |
| 46    | Reserved                                    |  |
| 47    | Reserved                                    |  |
| 48-4D | Reserved                                    |  |
| 4E    | Manufacturing POST Loop or Display Messages | Reboot if Manufacturing POST Loop pin is set. Otherwise display any messages (i.e., any non-fatal errors that were detected during POST) and enter                                 |

|       |                        |   |
|-------|------------------------|---|
|       |                        | Setup.  |
| 4F    | Security Check         | Ask password security (optional).   |
| 50    | Write CMOS             | Write all CMOS values back to RAM and clear screen.   |
| 51    | Pre-boot Enable        | Enable parity checker.<br>Enable NMI, Enable cache before boot.   |
| 52    | Initialize Option ROMs | Initialize any option ROMs present from C8000h to EFFFFh.<br>NOTE: When FSCAN option is enabled, will initialize from C8000h to F7FFFh. |
| 53    | Initialize Time Value  | Initialize time value in 40h:BIOS area.   |
| 60    | Setup Virus Protect    | Setup virus protect according to Setup.   |
| 61    | Set Boot Speed         | Set system speed for boot.  |
| 62    | Setup Num Lock         | Setup Num Lock status according to Setup.   |
| 63    | Boot Attempt           | Set low stack.<br>Boot via INT 19h.   |
| B0    | Spurious               | If interrupt occurs in protected mode.  |
| B1    | Unclaimed NMI          | If unmasked NMI occurs, display.<br>Press F1 to disable NMI, F2 reboot.   |
| E1-EF | Setup Pages            | E1-Page 1, E2-Page 2, etc.  |
| FF    | Boot                   |   |