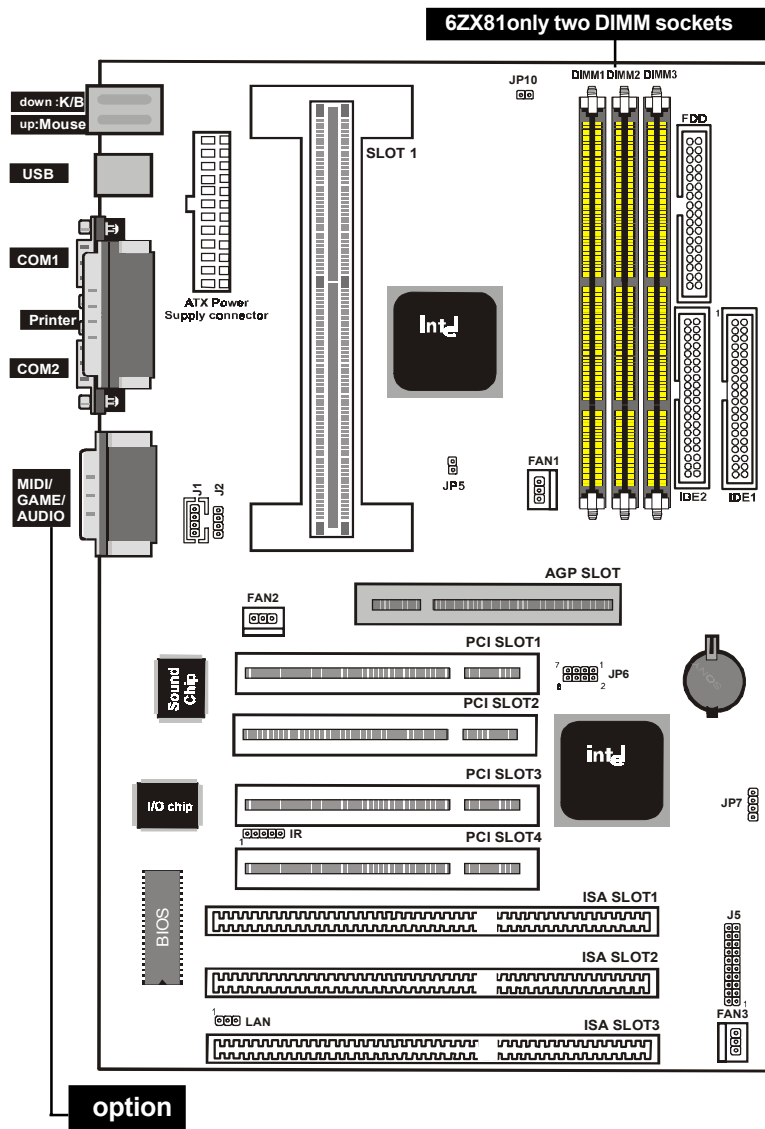


The 6BX/ZX/VIA81/VIA81P Mainboard layout



CPU/System Fan Connector:Fan1/2

Pin	Assignment
1	Ground
2	+12VDC
3	Signal

CPU/System Fan Connector:Fan3

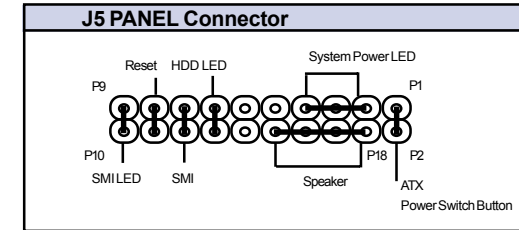
Pin	Assignment
1	Ground
2	+12VDC
3	Ground

Wake-On LAN Header:LAN

Pin	Assignment
1	5VSB
2	Ground
3	Signal

CMOS Function Selection:JP7

JP7 CMOS Clear	
2-3 (default)	Normal
3-4	Clear CMOS
1-4	External Battery



CD Audio-In Connectors:J1/J2

Pin J1	Assignment
1	CD-R
2	GND
3	CD-L
4	GND

Pin J2	Assignment
1	CD-R
2	GND
3	GND
4	CD-L

CPU External (Bus) Frequency Selection - JP6

JP6 CPU external frequency				
	P	P	P3	P1
	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
	P3	P	P4	P2
	1-2	3-4	-	-8
20X	ON	ON	ON	ON
2 X	O	ON	ON	ON
30X	ON	O	ON	ON
3 X	O	O	ON	ON
40X	ON	ON	O	ON
4 X	O	ON	O	ON
0X	ON	O	O	ON
X	O	O	O	ON
0X	ON	ON	ON	O
X	O	ON	ON	O
0X	ON	O	ON	O
X	O	O	ON	O
80X	ON	ON	O	O
Note				
ON : Close OFF : Open				

CPU Clock Select :JP5/JP10

		SB
P	C ose	MH e aut
	Open	100MH
P10	C ose	100MH e aut
	Open	133MH