6**A**15

User's Manual Version 1.0

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Table of Contents

Introduction

1. Motherboard Description

1.1 Features	
1.1.1 Hardware	2
1.1.2 Software	4
1.1.3 Attachments	4
1.2 Motherboard Installation	
1.2.1 Layout of Motherboard	5
1.3 Motherboard Connectors	
1.3.1 Front Panel Connectors(J1)	7
1.3.2 Floppy Disk Connectors(FDC1)	8
1.3.3 Hard Disk Connectors(IDE1/IDE2)	8
1.3.4 ATX 20-pin Power Connector(PW1)	9
1.4 Back Panel Connectors	
1.4.1 PS/2 Mouse/Keyboard CONN.	10
1.4.2 USB Connectors	10
1.5 Serial and Parallel Interface ports	11
1.6 CPU Installation	
1.6.1 CPU Installation Product(Socket 370)	13
1.6.2 CPU Clock Setting(JP8)	14
1.7 Jumper Setting	
1.7.1 CPU Fan Connector(FAN1/2/3)	1.5
1.7.2 Wake-On-Modem Header(WOM)	16
1.7.3 Wake-On-LAN Header(WOL)	16
1.7.4 AMR Set Select(J1)	16
1.7.5 CMOS Function Selection(JP9)	16
1.7.6 KB/MS PWR-ON(JP1)	17
1.7.7 CPU Vcore Range(JP2)	17
1.7.8 CPU Type Select(JP3/JP13)	17
1.7.9 BIOS Flash Setting(JP10)	17
1.7.10 Thermistor(TR2)(option)	18
1.7.11 SB-LINK	1.8

Table of Contents	
Table of Contents	
1.7.12 Debug LED (ERR1)	18
1.8 DRAM Installation	
1.8.1 DIMM	30
1.8.2 How to install a DIMM Module	30
1.9 Audio Subsystem	
1.9.1 CD-Audio-IN Connectors(CD1/CD2)	31
1.9.2 Speaker Output select Jumper(JP11)	32
2 DIOC Cotum	
2. BIOS Setup	
2.1 Main Menu	36
2.2 Standard CMOS Features	39
2.3 Advanced BIOS Features	42
2.4 Advanced Chipset Features	46
2.5 Integrated Peripherals	49
2.6 Power Management Setup	55
2.7 PnP/PCI Configurations	60
2.8 PC Health Status	64
2.9 Frequency/Voltage Control	66
2.10 Load Fail-Safe Defaults	68
2.11 Load Optimized Defaults	69
2.12 Set Supervisor/user Password	70
2.13 Save & Exit Setup	72
2.14 Exit Without Saving	73

Introduction

System @rview

This amual was written to help you start using this product as quickly and smothly as possbile. Inside you will find adequate explanations to solve mst problem. In order for this reference atterial to be of greatest use, refer to the "expanded table of contents" to find relevant topics. This board incorporates the systemI/O, and PCI IDE into one board that provides a total PC solution. The minboard, Intel Celeron/Copperime PII/PIII processor base PC ATX systemsupport single processors with ISA Bus, PCI Local Bus, and AGP Bus to support upgrades to your system perforance. It is ideal for uti-tasking and fully supports MS-DOS, Windows, Windows NT, Windows ME, Windows 2000, Novell, OS/2, Windows95/98, UNIX, SCO UNIX etc.

This amual also explains how to install the ainboard for operation, and how to setup your CMOS configuration with the BIOS setup program

1.Motherboard

1.1 Features

1.1.1 Hardare

CPU

- -Intel Pentiur**h**II processor and Intel Celeron Processor in FC-PGA package.
- -Supports processor 370-Pin Socket .
- -Socket processor 300MHz~933MHz or higher processor.

Chipset

- -North Bridge System hipset: Intel 815 support a 66/100/133 FSB.
- -South Bridge System hipset: Intel ICH.

Biggest memory capacity

6A815 is equipped with three DIMM socket to support (16MB, 32MB, 64MB, 128MB.256MB) 168 pin 3.3v SDRAM SPD(Special Presence Detect).

Maximmory up to 512MB.

AB for fast V& solution

- -AGP specification copliant.
- -AGP 66 MHz 3.3v/1.5v for 2X/4X device support.

PCI Expansion Slot

Provide five 32 bit PCI slots.

6-Board IDE

- -An IDE controller on the ICH chipset provides IDE HDD/CD-ROM with PIO, Bus Master and Ultra DMA 66 operation rules
- -Can connect up to four IDE device.

6-Board Peripherals

- -1 floppy port supports 2 FDD with 360K,720K,1.2M, 1.44M and 2.88M byte.
- -2 serial ports (COM1+COM2).
- -2 USB ports.
- -1 VGA ports.
- -1 parallel port supports SPP/EPP/ECP mde.

Audio

- -ICH chip integrated.
- -AC'97 CODEC on board.

BIO

- The annboard BIOS provides "Plug & lay" BIOS which detects the peripheral devices and expansion cards of the board autoatically.
- The ainboard provides a Desktop Manageant Interface (DMI) function which records your minboard specifications.
- BIOS support CD-ROM, SCSI, LAN BOOT, Teperature sensor, Wake on ordenLAN, AlarrBus CLK setup with BIOS.

Debug LED

Supports BIOS Port 80H POST Code output to debug LED.

WD(Wake @LAN)

Supports systemower up from AN ring up.

WM (Wake OMDEM)

Supports systemower up from Moderning up.

IrDA Port

Support this serial fast commication up to 115.2Kbps.

Support Ring on by modem/Alarm on

Support Systemower up from Moderning up or tier of SystemRequired enabled in Ring on by orderand Alarmon in BIOS

1.1.2 Softwre

BIO

- -AWARD legal BIOS.
- -Supports APM 1.2.
- -Supports USB Function.
- -Supports ACPI.

Deration System

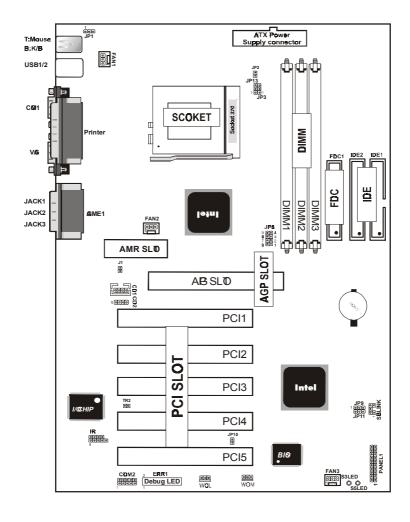
-Offers the highest perfor**ance** for MS-DOS, Windows, Windows NT, Windows ME, Windows 2000, Novell, OS/2, Windows95/98, UNIX, SCO UNIX etc.

1.1.3 Attachments

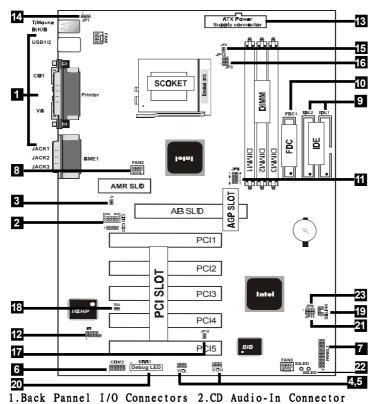
- -HDD UDMA66 Cable.
- -FDD Cable.
- -Flash Menry Written for BIOS Update.
- -COM2 Cable.
- -Fully Setup Driver CD build in Utility(Ghost, Anitivirus, Adobe Acrobat. . .).

1.2 Motherboard Installation

1.2.1 Layout of Motherboard



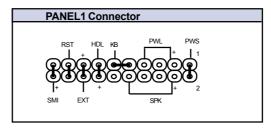
1.3 Motherboard Connectors



- 3.AMR Set Function(J1)
- 5. Wake-On-LAN Connector
- 7. Front Panel Connector
- 9. IDE Connectors
- 11.CPU Clock Selection(JP8)
- 13.ATX Power Connector
- 15.CPU Vcore Range(JP2)
- 17.BIOS Flash Select(JP10)
- 19.SB-LINK Connector
- 21. Speaker Output Select(JP11) 22. Suspend Type LED
- 23.CMOS Function Selection(JP9)
- 6 / Chapter 1 Motherboard Desciption

- 4. Wake-On MODEM Connector
- 6. Front COM2 Connector
- 8.Fan connectors(Fan1/2/3)
- 10.Floppy Connectors
- 12. IR Connector
- 14.KB/MS PWR-ON(JP1)
- 16.CPU Type Select(JP3/JP13)
- 18. Thermistor (TR2) (option)
- 20.Debug LED (ERR1)

1.3.1 Front Panel Connector(PANEL1)



Speaker Connector (SPK

An offboard speaker can be installed onto the otherboard as a anufacturing option. An offboard speaker can be connected to the otherboard at the front pannel connector. The speaker (onboard offboard) provides error beep code inforation during the Power Self-Test when the coputer cannot use the vedio interface. The speaker is not connected to the audio subsystem and does not receive output fronthe audio subsystem

Hrd Drive LED Connector (HL)

This connector supplies power to the cabinet IDE activity LED. Read and write activity by devices connected to the Pri**ar**y or Secondary IDE connectors will cause the LED to light up.

SMI Suspend Switch Lead (EXT)

This allows the user to amually place the system to a suspend or or Green or where system ic activity will be instantly decreased to save electricity and expand the life of certain components when the systems not in use. This 2-pin connector (see the figure below) connects to the case-munted suspend switch. If you do not have a switch for the connector, you any use the Turbo Switch" instead since it does not have a function. SMI is activated when it detects a short to open ment and therefore leaving it shorted will not cause any problem It any require one or two pushes depending on the position of the switch. Wake-up can be controlled by settings in the BIOS but the keyboard will always allow wake-up (the SMI lead cannot wake-up the system If you want to use this connector, "Suspend Switch" in the Power Manageemt Setup of the BIOS SOFT-WARE section should be on the default setting of Enable.

ATX Power Switch (PWS)

The systemower is controlled by a mentary switch connected to this lead. Pushing the button once will switch the systemon. The systemower LED lights when the system power is on .

Power LED Lead (PWL)

The systemower LED lights when the systemower is on.

SMI LED Lead (SMI)

The system ILED lights when the system uspend is on.

Kyboard Lock (K)

The header is for setting keyboard locked.

Reset Switch Lead (RST)

The connector can be connected to a **num**tary SPST type switch that is normally open. When the switch is closed, the **num**therboard resets and runs the POST

1.3.2 Floppy Disk Connector(FDC1)

This connector supports the provided floppy drive ribbon cable. After connecting the single end to the board, connect the two plug on the other end to the floppy drives.

1.3.3 Hrd Disk Connectors(IDE1/IDE2)

These connectors support the provided IDE hard disk ribbon cable. After connecting the single end to the board, connect the two plugs at the other end to your hard disk .

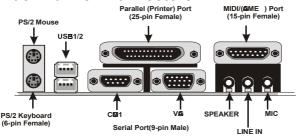
If you install two hard disks, you mst configure the second drive to Slave mde by setting its juper settings. BIOS now supports SCSI device or IDE CD-ROM boot up (see "HDD Sequence SCSI/IDE First" &Boot Sequence" in the BIOS Features Setup of the BIOS SOFTWARE) (Pin 20 is remved to prevent inserting in the wrong orientation when using ribbon cables with pin 20 plugged).

1.3.4 ATX 20-pin Power Connector(PW1)

This connector supports the power button on-board. Using the ATX power supply, functions sush as ModerRing Wake-Up and Soft Power Off are supported on this ntherboard. This power connector supports instant power-on functionality, which mans that the systemvill boot up instantly when the power connector is inserted on the board.

Pin	Signal	Pin	Signal
1	3.3V	2	3.3V
3	IS D	4	5V
5	IS D	6	5V
7	IS D	8	PW-Ø
9	5VSB	10	12V
11	3.3V	12	-12V
13	IS D	14	PS-10)
15	IS D	16	IS D
17	IS D	18	-5V
19	5V	20	5V

1.4 Back Pannel Connectors



1.4.1 PS/2 Mouse / Wyboard CON.

The otherboard provides a standard PS/2 ouse / Keyboard ini DIN connector for attaching a PS/2 ouse. You can plug a PS/2 ouse / Keyboard directly into this connector.

1.4.2 USB Connectors: USB1/2

The otherboard provides a OHCI(Open Host Controller Interface)Universal Serial Bus Roots for attaching USB devices such as:keyboard, ouse and other USB devices. You can plug the USB devices directly into this connector.



Pin	Signal
1	+5v
2	USBP0-(USBP1-)
3	USBP0+(USBP1+)
4	S D

1.5 Serial and Parallel Interface Ports

This systemons equipped with two serial ports and one parpllel port. Both types of interface ports will be explained in this chapter.

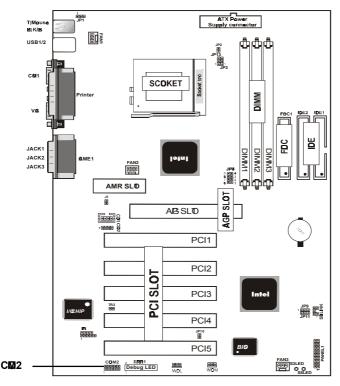
The Serial Interface: CM1/CM2

The serial interface port is sontins refered to as an RS-232 port or an asynchronous comication port. Mice, printers, mdes and other peripheral devices can be connected to a serial port. The serial port can also be used to connect your coputer system If you wish to transfer the contents of your hard disk to another system tean be accoplished by using each archine's serial port.



The serial port on this systemand peripherals used to be equipped with only 25-pin connector. Should you need to connect your 9-pin serial port to an older 25-pin serial port, you can purchase a 9-to-25 pin adapter.

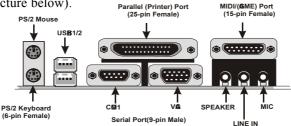
Signal	DB9 Pin	DB25 Pin
DCD	1	8
RX	2	3
TX	3	2
DTR	4	20
⊗ D	5	7
DSR	6	6
RTS	7	4
CTS	8	5
RI	9	22



Parallel Interface Port

Unlike the serial ports, parallel interface port has been standardized and should not present any difficulty interfacing peripherals to your systemSoutions called Centronics port, the parallel port is alorst exclusively used with printers. The parallel port on your systemas a 25-pin, DB 25 connector(see picture below).

Parallel (Printer) Port

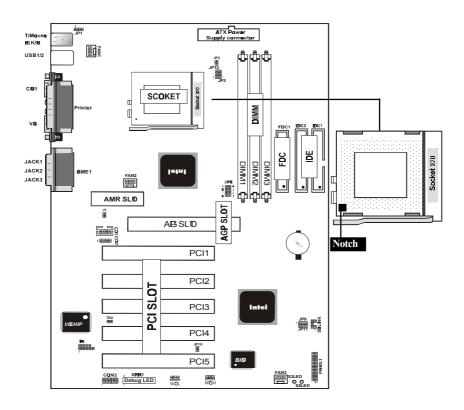


12 / Chapter 1 Motherboard Desciption

1.6 CPU Installation

1.6.1 CPU Installation Procedure: Socket 370

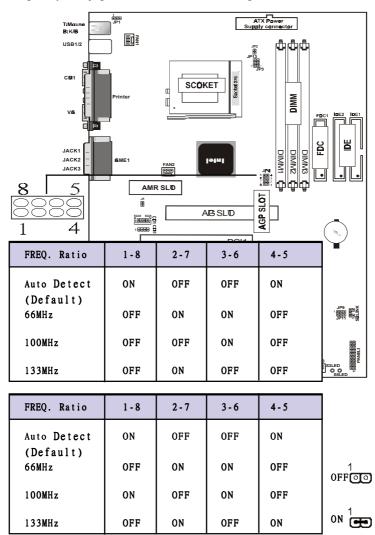
- 1.Pull the lever sideways away fronthe socket then raise the lever to a 90-degree angle.
- 2.Locate Pin 1 in the socket and look for the white dot or cut edge in the CPU. Match Pin 1 with the white dot/cut edge then insert the CPU.
- 3. Press the lever down to coplete the installation.
- 4. Make sure the spec of heatsink is good enough.



Chapter 1 Motherboard Desciption /13

1.6.2 CPU Clock Selection: JP8

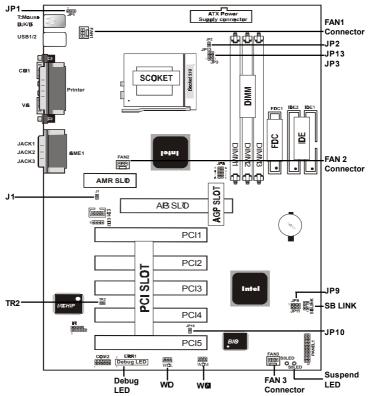
Overclocking is operating a CPU/Processor beyond its specified frequency.JP8 juper is used for overclocking.



14 / Chapter 1 Motherboard Desciption

1.7 Jumper Setting

A juper has two or ore pins that can be covered by a plastic juper cap, allowing you to select different system options.



1.7.1 CPU/System Fan Connector:Fan1/2/3

Pin	Assignment
0 1	Ground
0 2 2	+12VDC
0 3 3	Signal

Chapter 1 Motherboard Desciption /15

1.7.2 Wake-@ Modem Header:WM

Pin	Assignment
ြ ₁ 1	5VSB
 2	Ground
<u></u> 3 3	Signal

1.7.3 Wake-@LAN Header:WD

Pin	Assignment
1 1	5VSB
 	Ground
© ₃ 3	Signal

1.7.4 AMR Set Function:J1

Pin	Assignment
<u> </u>	Enabled AMR Slot (Default)
00	Disabled AMR Slot

1.7.5 CM9 Function Selection: JP9

Pin	Assignment
1 - 2	Normal (Default)
2 - 3	Clear CMOS
2-3	Crear CMOS

NOE:

(Please followhe procedure as below clear CMS data.)

(1)Remove AC power line.(2)JP9(2-3)Closed.(3)Wait five seconds.(4)JP9(1-2) Closed.(5)AC Power on.(6)Reset your desired password or clear CMO data.

1.7.6 KB/MS PWR-10:JP1

The JP1 Juper is for setting keyboard power. This function is provided by keyboard and PS/2 muse Wake-up function.

Assignment
Diabled
Enabled (Default)

1.7.7 CPU Vcore Range:JP2

Pin	Assignment
9	1.30V-2.05V Short (Default)
00	1.30V-3.50V Open

1.7.8 CPU Type Select:JP3/JP13

Pin	Assignment
1 - 2	Intel CPU (Default)
2-3	Cyrix CPU
-	,,,,,,

1.7.9 BIO Flash Setting:JP10

Pin	Assignment
9	Locked(defacult)
00	Unlocked

1.7.10 Thermisor:TR2 (option)

This used to check the systemeperature. The TR2 a 2-pin connector which can be inserted with a 20cmength thermstor.

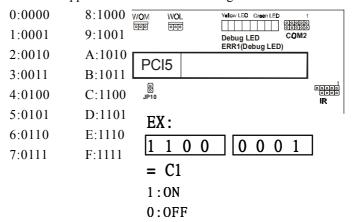
1.7.11 SB-LINK Connector

The otherboard provides one infrared SB-LINK feature connector for support PCI sound cards.

Pin	Assignment
1	GNT#
2 6 5	GND
₃ ಄	KEY
4 ੴ	REQ#
5 2 1	GND
6	SERIRQ

1.7.12 Debug LED:ERR1

The otherboard provides one infrared Debug LED feature LED for support BIOS POST 80H Debug Code.



Please refer POST Error Code.

If shows C1 that mans mory errors.

PST Error Code

ΡΘ Γ (hex)	Description
CFh	Test CMOS R/W functionality.
C0h	Early chipset initialization:
	-Disable shadow RAM
	-Disable L2 cache (socket 7 or below)
	-Programasic chipset registers
C1h	Detect enry
	-Auto-detection of DRAM size, type and
	ECC.
	-Auto-detection of L2 cache (socket 7
	or below)
C3h	Expand copressed BIOS code to DRAM
C5h	Call chipset hook to copy BIOS back
	to E000 & 000 shadow RAM.
01h	Expand the Xgroup codes locating in
	physical address 1000:0
02h	Reserved
03h	Initial Superio_Early_Init switch.
04h	Reserved
05h	1. Blank out screen
	2. Clear CMOS error flag
06h	Reserved
07h	1. Clear 8042 interface
	2. Initialize 8042 self-test
08h	1. Test special keyboard controller for
	Winbond 977 series Super I/O chips.
	2. Enable keyboard interface.

PØΓ (hex)	Description
09h	Reserved
0Ah	1.Disable PS/2 muse interface
	(optional).
	2. Auto detect ports for keyboard & muse
	followed by a port & nterface swap
	(optional).
	3.Reset keyboard for Winbond 977 series
	Super I/O chips.
0Bh	Reserved
0Ch	Reserved
0Dh	Reserved
0Eh	Test F000h segunt shadow to see
	whether it is R/W-able or not. If
	test fails, keep beeping the speaker.
0Fh	Reserved
10h	Auto detect flash type to load
	appropriate flash R/W codes into the
	run tienarea in F000 for ESCD &
	DMI support.
11h	Reserved
12h	Use walking 1's algorithmo check
	out interface in CMOS circuitry.
	Also set real-timclock power
	status, and then check for override.
13h	Reserved
14h	Programhipset default values into
	chipset. Chipset default values are
	MODBINable by OEM custoers.

PØΓ (hex)	Description
15h	Reserved
16h	Initial onboard clock generator if
	Early_Init_Onboard_Generator is defined.
	See also POST 26h.
17h	Reserved
18h	Detect CPU information including
	brand, SMI type (Cyrix or Intel) and
	CPU level (586 or 686).
19h	Reserved
1Ah	Reserved
1Bh	Initial interrupts vector table. If no
	special specified, all H/W interrupts
	are directed to SPURIOUS_INT_HDLR &
	S/W interrupts to SPURIOUS_soft_HDLR.
1Ch	Reserved
1Dh	Initial EARLY_PM_INIT switch.
1Eh	Reserved
1Fh	Load keyboard atrix (notebook platfor)m
20h	Reserved
21h	HPM initialization (notebook platform
22h	Reserved
23h	1.Check validity of RTC value:
	e.g. a value of 5Ah is an invalid value for RTC
	inute.
	2. Load CMOS settings into BIOS stack. If
	CMOS checksurfails, use default value
	instead.

PØΓ (hex)	Description
24h	Prepare BIOS resource ap for PCI &PnP
	use. If ESCD is valid, take into consideration
	of the ESCD's legacy inforation.
25h	Early PCI Initialization:
	-Enu er ate PCI bus nuber.
	-Assign enery & O resource.
	-Search for a valid VGA device & GA BIOS,
	and put it into C000:0.
26h	1.If Early_Init_Onboard_Generator is
	not defined Onboard clock gen erator
	initialization. Disable respective clock
	resource to epty PCI &DIMM slots.
	1.Init onboard PWM
	2.Init onboard H/W mitor devices
27h	Initialize INT 09 buffer
28h	Reserved
29h	1.Program PU internal MTRR (P6
	& II) for 0-640K em ry address.
	2.Initialize the APIC for Pentiumlass CPU.
	3.Programarly chipset according to CMOS
	setup. Exaple: onboard IDE controller.
	4.Measure CPU speed.
2Ah	Reserved
2Bh	Invoke Video BIOS
2Ch	Reserved

PØΓ (hex)	Description
2Dh	1.Initialize double-byte language
	font (Optional)
	2. Put infor at ion on screen display,
	including Award title, CPU type,
	CPU speed, full screen logo.
2Eh	Reserved
2Fh	Reserved
30h	Reserved
31h	Reserved
32h	Reserved
33h	Reset keyboard if Early_Reset_KB is
	defined e.g. Winbond 977 series Super
	I/O chips. See also POST 63h.
34h	Reserved
35h	Test DMA Channel 0
36h	Reserved
37h	Test DMA Channel 1.
38h	Reserved
39h	Test DMA page registers.
3Ah	Reserved
3Bh	Reserved
3Ch	Test 8254
3Dh	Reserved
3Eh	Test 8259 interrupt as k bits for channel 1.
3Fh	Reserved
40h	Test 8259 interrupt as k bits for channel 2.
41h	Reserved
42h	Reserved

POT (hex)	Description
43h	Test 8259 functionality.
44h	Reserved
45h	Reserved
46h	Reserved
47h	Initialize EISA slot
48h	Reserved
49h	1.Calculate total nor y by testing the
	last double word of each 64K page.
	2.Programwrite allocation for AMD K5
	CPU.
4Ah	Reserved
4Bh	Reserved
4Ch	Reserved
4Dh	Reserved
4Eh	1.PrograrMTRR of M1 CPU
	2.Initialize L2 cache for P6 class CPU
	&programCPU with proper cacheable
	range.
	3.Initialize the APIC for P6 class CPU.
	4.On MP platformadjust the cacheable range
	to saller one in case the cacheable ranges
	between each CPU are not identical.
4Fh	Reserved
50h	Initialize USB Keyboard &Mouse.
51h	Reserved
52h	Test all entry (clear all extended entry
	to 0)

PØΓ (hex)	Description
53h	Clear password according to H/W juper
	(Optional)
54h	Reserved
55h	Display nuber of processors (ulti-
	processor platform
56h	Reserved
57h	1.Display PnP logo
	2.Early ISA PnP initialization
	-Assign CSN to every ISA PnP device.
58h	Reserved
59h	Initialize the cobined Trend Anti-Virus
	code.
5Ah	Reserved
5Bh	(Optional Feature)
	Show assage for entering AWDFLASH.
	EXE from (optional)
5Ch	Reserved
5Dh	1.Initialize Init_Onboard_Super_IO
	2.Initialize Init_Onbaord_AUDIO.
5Eh	Reserved
5Fh	Reserved
60h	Okay to enter Setup utility; i.e. not until
	this POST stage can users enter the
	CMOS setup utility.
61h	Reserved
62h	Reserved
63h	Reset keyboard if Early_Reset_KB is
	not defined.
	not defined.

PØΓ (hex)	Description
63h	Reset keyboard if Early_Reset_KB is
	not defined.
64h	Reserved
65h	Initialize PS/2 Mouse
66h	Reserved
67h	Prepare entry size inforation for
	function call: INT 15h ax=E820h
68h	Reserved
69h	Turn on L2 cache
6Ah	Reserved
6Bh	Programhipset registers according to
	itemdescribed in Setup & uto-
	configuration table.
6Ch	Reserved
6Dh	1.Assign resources to all ISA PnP
	devices.
	2.Auto assign ports to onboard COM
	ports if the corresponding item Setup
	is set to "AUTO".
6Eh	Reserved
6Fh	1.Initialize floppy controller
	2.Set up floppy related fields in 40: hardware.
70h	Reserved
71h	Reserved
72h	Reserved
73h	Reserved
74h	Reserved

PØΓ (hex)	Description
75h	Detect & anstall all IDE devices:
	HDD, LS120, ZIP, CDROM
76h	(Optional Feature)
	Enter AWDFLASH.EXE if:
	-AWDFLASH.EXE is found in floppy
	drive.
	-ALT+F2 is pressed.
77h	Detect serial ports ∥ ports.
78h	Reserved
79h	Reserved
7Ah	Detect & nstall co-processor
7Bh	Reserved
7Ch	Init HDD write protect.
7Dh	Reserved
7Eh	Reserved
7Fh	Switch back to text md e if full screen
	logo is supported.
	-If errors occur, report errors &vait
	for keys.
	-If no errors occur or F1 key is pressed
	to continue:
	◆Clear EPA or custoination logo.
80h	Reserved
81h	Reserved
82h	1.Call chipset power amageant hook.
	2.Recover the text fond used by EPA
	logo (not for full screen logo)
	3.If password is set, ask for password.

POT (hex)	Description	
83h	Save all data in stack back to CMOS	
84h	Initialize ISA PnP boot devices	
85h	1.USB final Initialization	
	2.Switch screen back to text mde	
86h	Reserved	
87h	NET PC: Build SYSID Structure.	
88h	Reserved	
89h	1.Assign IRQs to PCI devices	
	2.Set up ACPI table at top of the	
	emry.	
8Ah	Reserved	
8Bh	1.Invoke all ISA adapter ROMs	
	2.Invoke all PCI ROMs (except	
	VGA)	
8Ch	Reserved	
8Dh	1.Enable/Disable Parity Check	
	according to CMOS setup	
	2.APM Initialization	
8Eh	Reserved	
8Fh	Clear noise of IRQs	
90h	Reserved	
91h	Reserved	
92h	Reserved	
93h	Read HDD boot sector inforation for	
	Trend Anti-Virus code	

PØT (hex)	Description
94h	1.Enable L2 cache
	2.Programaylight Saving
	3.Prograrboot up speed
	4. Chipset final initialization.
	5. Power amageemt final
	initialization
	6.Clear screen & isplay sumry table
	7.PrograrK6 write allocation
	8.Program class write comining
95h	Update keyboard LED & Sypentic rate
96h	1.Build MP table
	2.Build & pdate ESCD
	3.Set CMOS century to 20h or 19h
	4.Load CMOS tieninto DOS tier
	tick
	5.Build MSIRQ routing table.
FFh	Boot attept (INT 19h)

1.8 DRAM Installation

1.8.1 **DIMM**

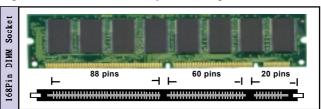
DRAM Access Tim3.3V Unbuffered SDRAM/ PC66/ PC100 and PC133 Type required.

DRAM Type:8MB,16MB,32MB,64MB,128MB,256MB DIMM Module.(168 pin)

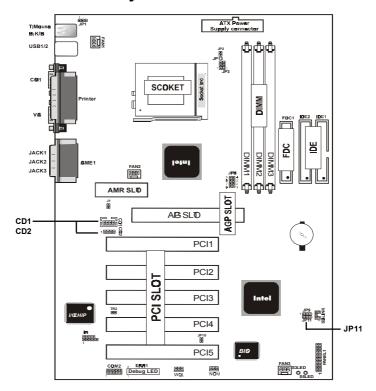
Bank	Memory module
DIMM 1	16MB,32MB,64MB,128MB,256MB
(Bank 0-1)	168 pin,3.3v SDRAM
DIMM 2	16MB,32MB,64MB,128MB,256MB
(Bank 2-3)	168 pin 3.3v,SDRAM
DIMM 3	16MB,32MB,64MB,128MB,256MB
(Bank 4-5)	168 pin 3.3v,SDRAM
	Total System Memory(Max 512MB)

1.8.2 How install a DIMM Module

- 1. The DIMM socket has a "Plastic Safety Tab" and the DIMM mory ordule has an asymtrical notch", so the DIMM mory ordule can only fit into the slot in one direction.
- 2.Push the tabs out. Insert the DIMM **enry ord**ules into the socket at a 90-degree angle then push down vertically so that it will fit into place.
- 3. The Mounting Holes and plastic tabs should fit over the edge and hold the DIMM norm in place.



1.9 Audio Subsystem



1.9.1 CD Audio-In Connectors:CD1/CD2

Assignment
IS D
CD-L
NSD
CD-R

Assignment
CD-L
IS D
IS D
CD-R

1.9.2 Speaker Otput Select: JP11

Pin	Assignment
1 - 2	Output to PC-SPK (Default)
2 - 3	Output to AC-97 SPK

2. BIO Setup

Introduction

This chapter discusses the Award Setup programult into the ROM BIOS. The Setup programultows user to maify the basic systemonfiguration. This special information is then stored in battery-backed RAM so that it retains the setup information when the power is turned off.

The Award BIOS installed in your coputer system ROM (Read Only Meory) is a customersion of an industry standard BIOS. This mans that it supports Intel Celeron/Copperine PII/PIII Processor. The BIOS provides critical low-level support for standard devices such as disk drives and serial and parallel ports.

The rest of this **an**ual is intended to guide you through the process of configuring your systemsing Setup.

Plug and Play Support

This AWARD BIOS supports the Plug and Play Version 1.0A specification. ESCD(Extended SystemConfiguration Data)write is supported.

EPA Gen PC Support

This AWARD BIOS supports Version 1.03 of the EPA Green PC specification.

APM Support

These AWARD BIOS supports Version 1.1&2 of the Advanced Power Manageant (APM) specification. Power amageant features are inhemnted via the System Manageant Interrupt (SMI). Sleep and Suspend power amageant ordes are supported. Power to the hard disk drives and video unitors can be amaged by this AWARD BIOS.

PCI Bus Support

This AWARD BIOS also supports Version 2.1 of the Intel PCI (Peripheral Coponent Interconnect)local bus specification.

DRAM Support

SDRAM (Synchronous DRAM) are supported.

Support CPU

This AWARD BIOS supports the Intel Celeron/Copperine PII/PIII Processor.

Using Setup

In general, you use the arrow keys to highlight itempress <Enter>to select, use the <PgUp>and <PgDn>keys to change entries, press<F1>for help and press <Esc>to quit. The following table provides me detail about how to navigate in the Setup programy using the keyboard.

Kystroke	Function	
Up arrow	Move to previous item	
Down arrow	Move to next item	
Left arrow	Move to the itemn the left(mu bar)	
Right arrow	Move to the itemn the right(mu bar)	
Esc	Main Menu:Quit without saving changes	
	Subemus:Exit Current page to the next higher	
	level em u	
Move Enter	Move to itengou desired	
PgUp key	Increase the nueric value or rake changes	
PgDn key	Decrease the numeric value or make changes	
+Key	Increase the nueric value or rake changes	
-Key	Decrease the numeric value or make changes	
Esc Key	Main emu-Quit and not save changes into	
	CMOS	
	Status Page Setup Menu and option Page Setup	
	Menu -Exit Current page and return to Main	
	Menu	
F1 Key	General help on Setup navigation keys.	
F5 Key	Load previous values from MOS	
F6 Key	Load the fail-safe defaults from IOS default	
	table	
F7 Key	Load the optimed defaults	
F10 Key	Save all the CMOS changes and exit	

2.1 Main Menu

Once you enter AWARD BIOS CMOS Set up Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from everal setup function. Use the arrow keys to select arms the item and press<Enter> to accept and enter the sub-enu.

"WARNING"

The information about BIOS defaults on manual (Figure 1,2,3,4,5,6,7,8,9,10,11,12,13,14) is just for reference, please refer to the BIOS installed on the board for update information.

◎ Figure 1. Main Menu

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software

Standard CMO Setup	Frequency/Voltage Control		
Advanced BI 9 Setup	Load Fail-Safe Defaults		
Advanced Chipset Setup	Load Otimized Defaults		
Integrated Peripherals	Set Supervisor Password		
Power Management Setup	Set User Password		
PNP/PCI Configuration	Save & Exit Setup		
PC Health Status	Exit Without Saving		
Esc: Qit F9: Menu in BIO	←→↑↓: Select Item		
F10 : Save & Exit Setup			
Time , Date , Hard Disk Type			

Standard CM**O**Features

This setup page includes all the item standard copatible BIOS

Advanced BIOFeatures

This setup page includes all the itemf BIOS special enchanced features.

Advanced Chipset Features

This setup page includes all the itemf Chipset special enchanced features.

Integrated Peripherals

This selection page includes all the itemf IDE hard drive and Programd Input/Output features.

Power Management Setup

This setup page includes all the itemf power amageent features.

PnP/PCI Configuration

This setup page includes IRQ Setting by user define or default.

PC Halth Status

This page shows the hardware Monitor information of the system

Frequency / Voltage Control

This setup page is control CPU's clock and frequency ratio.

Load Fail-Safed Defaults

Use this emu to load the BIOS default values for the ininh/stable perforance for your system operate.

Load Otimized Defaults

These settings are **pre** likely to configure a workable coputer when so**ething** is wrong. If you cannot boot the coputer seccessfully, select the BIOS Setup options and try to diagnose the problemafter the coputer boots. These settings do not provide optional perforance.

Set Supervisor Password

Change, set, or, disable password. It allows you to lim access to the system and Setup, or just to Setup.

Set User Password

You can specify both a User and a Supervisor password. When you select either password option, you are propted for a 1-6 character password. Enter the password and then retype the password when propted.

Save & Exit Setup

Save CMOS value changes to CMOS and exit setup.

Exit Without Saving

Abandon all CMOS value changes and exit setup.

2.2 Standard CMDFeatures

This item Standard CMOS Setup Menu is divided into 10 categories. Each category includes no, one or ore than one setup item. Use the arrow keys to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item.

◎ Figure 2. Standard CM**®** Setup

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software standard CMOS Setup

Date(mm:dd:yy)	Tue,Jun 6 2000	Item Help
Time (hh:mm:ss)	11:26:10	Menu Level
IDE Primary Master IDE Primary Slave IDE Secondary Master	None None None	Change the day, month, year
IDE Secondary Master Drive A	None 1.44M,3.5 in	and century.
Drive B Video	None E ß /V ß	
Halt 0 Base Memory	All,But Keyboard 640K	
Extended Memory Total	391168K 392192K	

←→↑↓: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7:ptimized Defaults

Main Menu Selections

This table shows the selections that you can **ak**e on the Main Menu.

Item	Otions	Description	
Date	Month DD YYYY	Set the systemate.note that the	
		'Day' autoantically changes	
		when you set the data.	
IDE Pri ar y O	ptions are in its sub	Press <enter> to enter the sub emu</enter>	
Master	em u.	of detailed.	
IDE Pri ar y O	ptions are in its sub	Press <enter> to enter the sub emu</enter>	
Slave	emu.	of detailed.	
IDE Secondary	Options are in its sub	Press <enter> to enter the sub emu</enter>	
Master	emu.	of detailed.	
IDE Secondary	Options are in its sub	Press <enter> to enter the sub emu</enter>	
Slave	em u.	of detailed.	
Drive A	None	Select the type of floppy disk drive	
Drive B	360K,5.25in	installed in your system	
	1.2M,5.25in		
	720K,3.5in		
	1.44M,3.5in		
	2.88M,3.5in		
Video	EGA/VGA	Select the default video device.	
	CGA 40		
	CGA 80		
	MONO		

Item	O tions	Description
Halt On	All Errors	Select the situation in which you
	No Errors	want the BIOS to stop the POST
	All,but Keyboard	process and notify.
	All,but Diskette	
	All,but Disk/Key	
Base Menry N	/A	Displays the anunt of conventional
		enery detected during boot up.
Extended	N/A	Displays the anunt of conventional
Me or y		emry detected during boot up.
Total	N/A	Displays the total emry
Menry		available in the system

2.3 Advanced BI®Setup

© Figure 3. Advanced BIO Setup

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software advanced BIOS Setup

Virus Warning CPU Internal Cache	Disabled Enabled	Item Help
External Cache	Enabled	Menu Level
CPU L2 Cache ECC Checking	Enabled	Mena Level
Processor Number Feature	Enabled	Allows you to
Qick Power @ Self Test	Enabled	choose the
First Boot Device	Floopy	VIRUS warning
Second Boot Device	CD-R Ø	feature for IDE
Third Boot Device	HDD-0	Hard Disk boot
Fourth ther Device	Enabled	sector protection.
Swap Floppy Drive	Disabled	If this function
Boot Up Floppy Seek	Enabled	is enabled and
Boot Up NumLock Status	0	someone attempt
Oate A20 Option	Normal	to write data into
Typematic Rate Setting	Disabled	this area,BIO
Typematic Rate (Chars/Sec)	6	will show a
Typematic Delay (Msec)	250	warning message
Security Otion	Setup	on screen and
Select For DRAM >64MB	Non-62	alarm beep
HDD S.M.A.R.T. Capability	Disabled	
Report No FDD For WIN 95	No	

←→↑: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7: timized Defaults

Virus Warning

This option allows you to choose the VIRUS Warning feature for IDE Hard Disk boot sector protection. If this function is enabled and some attent to write data into this area, BIOS will show a warning essage on screen and alarmeep.

The Choices:Disabled(default), Enabled.

CPU Internal Cache

These two categories speed up **nor**y access. However, it depends on CPU/chipset design.

Enabled(default) Enabled cache. **Disabled** Disabled cache.

External Cache

This fields allow you to Enable or Disable the CPU'S "Level 2" secondary cache. Caching allows better perforance.

Enabled(default) Enabled cache. **Disabled** Disabled cache.

CPU L2 Cache ECC Checking

The iterallows you to enable/disable CPU L2 Cache ECC Checking.

The Choices: Enabled (default), Disabled.

Processor Number Feature

The itenvill show up when you install the PentiurIII processor.

Enabled(default) PentiumProcessor Nuber

Feature.

Disabled Disabled.

Qick Power @Self Test

This category seeds up Power on self-Test(POST) after you power up the coputer. If it is set to Enable, BIOS will shorten or skip somcheck itemduring POST.

Enabled(default) Enabled quick POST.

Disabled Noral POST.

First/Secondary/Third/Fourth ther Device

These BIOS attepts to load the operating systerfrom the devices in the sequence selected in these item **The Choices:**Floppy, LS120, HDD-0, HDD-1, HDD-2, HDD-3, SCSI, CDROM, Enabled, ZIP, LAN, Disabled.

Swap Floppy Drive

If the system as two floppy drives, you can swap the logical drive namassignents.

The Choices: Disabled (default), Enabled.

Boot Up Floppy Seek

Seek disk drives during boot up. Disabled speeds boot-up. **The Choices:Enabled**(default), Disabled.

Boot Up NumLock Status

Select power on state for Nulnck.

O(default) Nupad is nuber keys. Nupad is arrow keys.

Late A20 Otion

Select if chipset or keyboard controller should control

Gate A20.

Normal(default) A pin in the keyboard

controller controls Gate A20.

Fast Lets chipset control Gate A20.

Typematic Rate Setting

Enabled Enabled this option to adjust

the keystroke repeat rate.

Disabled(default) Disabled.

Typematic Rate (Char/Sec)

Range between 6(default) and 30 characters per second. This option controls the speed of repeating keystrokes.

Typematic Delay (Msec)

This option sets the timinterval for displaying the first and the second characters.

The Choices:250(default),500,750,1000

Security Otion

This category allows you to limaccess to the system and

Setup, or just to Setup.

System The systemvill not boot and

access to Setup will be defined if the correct password is not

entered in propt.

Setup(default) The systemvill boot,but

access to Setup will be defined if the correct password is not

entered in propt.

OSelect For DRAM >64MB

Select the operating system that is running with greater

than 64MB of RAM on the system The Choices:Non-82(default), 82

HD S.M.A.R.T. Capability

Enabled Enabled HDD S.M.A.R.T.

Capability.

Disabled(default) Disabled HDD S.M.A.R.T.

Capability.

Report No FDD For Window 95

No(default) Assign IRQ6 For FDD.

Yes FDD Detect IRQ6

Automically.

2.4 Advanced Chipset Setup

This section allows you to configure the system ased on the specific features of the installed chipset. This chipset mages bus speeds and access to system resources, such as DRAM and external cache. It also coordinates commications the PCI bus. It must be stated that these item should never need to be altered. The default settings have been chosen because they provide the best operating conditions for your system. The only timyou ight consider alking any changes would be if you discovered that data was lost while using your system.

◎ Figure 4. Advanced Chipset Setup

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software advanced Chipset Setup

SDRAM CAS Latency/Time	3	Item Help
SDRAM Cycle Time Tras/Trc	7/9	
SDRAM RAS -to- CAS Delay	3	Menu Level
SDRAM RAS Precharge Time	3	
System BIO Cacheable	Disabled	
Video BIO Cacheable	Disabled	
Memory Hole At 15M-16M	Disabled	
CPU Latency Timer	Enabled	
Delayed Transaction	Enabled	
AB Caphic Aperture Size	64MB	
System Memory Frequency	Auto	
6-Chip Video Window Size	64MB	

←→↑↓: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7:ptimized Defaults

SDRAM CAS latency Time

3(default) Slower SDRAM DIMM

Module.

2 Fastest SDRAM DIMM

Module.

SDRAM Cycle Time Tras/Trc

7/9(default) Set SDRAM Tras/Trc Cycle

timin 7/9 SCLKs.

5/7 Set SDRAM Tras/Trc Cycle

timin 5/7 SCLKs.

SDRAM RAS -to- CAS Delay

3(default) Set SDRAM RAS -to- CAS

delay 3 SCLKs.

2 Set SDRAM RAS -to- CAS

delay 2 SCLKs.

SDRAM RAS Precharge Time

3(default) Set SDRAM RAS Precharge

Tiento 3.

2 Set SDRAM RAS Precharge

Tiento 2.

System BIO Cacheable

When enabled, the access to the system IOS ROM address at F0000H-FFFFFH is cached.

The Choices: Diasbled (default), Enabled.

Video BIO Cacheable

Enabled Enabled Video BIOS

Cacheable.

Disabled (default) Disabled Video BIOS

Cacheable.

Memory ble At 15-16M

In order to iprove perforace, certain space in energy can be reserved for ISA cards. This energy ust be apped into the energy's space below 16MB.

The Choices: Diasbled (default), Enabled.

CPU Latency Timer

Enabled(default) Enabled. **Disabled** Disabled.

Delayed Transaction

Enabled(default) Slow speed ISA device in

system

Disabled Disabled.

AB Caphics Aperture Size

64MB(default) AGP Graphics Aperture Size

is 64 MB.

32MB AGP Graphics Aperture Size

is 32 MB.

System Memory Frequency

Auto(default) SystemMeory Frequency to

Auto.

100MM Set systemMenry Frequency

to 100MHZ.

133MM Set system Frequency

to 133MHZ.

©-Chip Video Window Size

64MB(default) Set Graphics Aperture Size to

64 MB.

32MB Set Graphics Aperture Size to

32 MB.

2.5 Integrated Peripherals

◎ Figure 5. Integrated Peripherals

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software Integrated Peripherals

©-Chip Primary PCI IDE	Enabled Enabled Auto Auto Auto Auto Auto Auto Auto Auto	Item Help Menu Level
Use IR Pins Øboard Paraller Port Parallel Port Mode	IR/Rx2Tx2 278/IR Q EPP	

^{←→↑↓:} Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7:ptimized Defaults

6-Chip Primary PCI IDE

Enabled(default) Enabled onboard 1st channel

IDE port.

Disabled Disabled onboard 1st channel

IDE port.

6-Chip Secondary PCI IDE

Enabled(default) Enabled onboard 2nd channel

IDE port.

Disabled Disabled onboard 2nd channel

IDE port.

IDE Primary Master PI(for onboard IDE 1st channel)

Auto(default) BIOS will autoatically detect

the IDE HDD Accessing ode.

Mode 0~4 Manually set the IDE

Accessing mde.

IDE Primary Slave PI(for onboard IDE 2nd channel)

Auto(default) BIOS will automatically detect

the IDE HDD Accessing mde.

Mode 0~4 Manually set the IDE

Accessing mde.

IDE Secondary Master PI(For onboard IDE 1st channel)

Auto(default) BIOS will autoatically detect

the IDE HDD Accessing ode.

Mode 0~4 Manually set the IDE

Accessing mde.

IDE Secondary Slave PI(for onboard IDE 2nd channel)

Auto(default) BIOS will automatically detect

the IDE HDD Accessing orde.

Mode 0~4 Manually set the IDE

Accessing mde.

IDE Primary Master UDMA

Auto(default) BIOS will autoatically detect

the IDE HDD Accessing mde.

Disabled Disabled.

IDE Primary Slave UDMA

Auto(default) BIOS will automically detect

the IDE HDD Accessing ode.

Disabled Disabled.

IDE Secondary Master UDMA

Auto(default) BIOS will automically detect

the IDE HDD Accessing orde.

Disabled Disabled.

IDE Secondary Slave UDMA

Auto(default) BIOS will automically detect

the IDE HDD Accessing mde.

Disabled Disabled.

USB Controller

Enabled (default) Enabled USB Controller. **Disabled** USB Controller.

USB Kyboard Support

Enabled Enabled USB Keyboard

Support.

Disabled(default) Disabled USB Keyboard

Support.

Init Display First

PCI Slot Set Init Display First to PCI

Slot.

Oboard AB(default) Set Init Display First to

onboard AGP.

AC 97 Audio

Auto(default) BIOS will automatically detect

onboard Modem

Disabled Disabled.

IDE HD Block Mode

Enabled(default) Enabled. **Disabled** Disabled.

Power Oby Function

Password Enter from to 7 characters to

set the Keyboard Power On

Password.

bt KyHot Key.Mouse LeftMouse Left.Mouse RightMouse Right.Any KyAny Key.

Button Ody(default) Button Only.

Kyboard 98 If your keyboard have Owner

key button, you can press the key to power on your system

R Power @ Password

Enter from to 7 characters to

set the keyboard Power On

Password.

bl Ky Power 0 First you must to choose the

Ctrl-F1(default) Power On by Hot Key function

Ctrl-F2 then Enter from to 8

Ctrl-F3 characters to set the Hot Key

Ctrl-F4 Power On your system

Ctrl-F5
Ctrl-F6

Ctrl-F7

Flash Write Protect

Enabled(default) DisabledBIOS can't be writed.
BIOS can be writed.

Oboard FDC Controller

Enabled(default) Enabled onboard FDC

Controller.

Disabled Disabled onboard FDC

Controller.

Oboard Serial Port1

Select an address and corresponding interrupt for the first and second serial ports.

The Choices:3F8/IRQ (default),Auto,(2F8/IRQ3), (3E8/IRQ4),(2E8/IRQ3),Disabled.

Oboard Serial Port 2

Auto BIOS will autoatically setup

the Serial Port 2 address.

3F8/IR Enabled onboard Serial Port 2

and address is 3F8.

2F8/IR6(default) Enabled onboard Serial Port 2

and address is 2F8.

3E8/IRQ Enabled onboard Serial Port 2

and address is 3E8.

2E8/IR Enabled onboard Serial Port 2

and address is 2E8.

Disabled Disabled.

UART Mode Select

This itemallows you decide which Infra Red(IR) function of the onboard I/O chip, you wish to use.

The Choices: Normal (default), IrDA, SCR, ASKIR.

UR2 Duplex Mode

This itemallows you decide which Infra Red(IR) function of the onboard I/O chip.

The Choices: If (default), Full.

Oboard Parallel Port

This itemallows you decide access onboard parallel port controller with which I/O address.

Disabled.

378/IRQ

278/IRQ (default)

3BC/IRQ

PWRO After PWR-Fail

The Choices: 6 (default), On.

Parallel Port Mode

SPP Using Parallel port as Standard

Parallel Port.

EPP(default) Using Parallel port as Ex-

tended Parallel Port.

ECP Using Parallel port as Ex-

tended Capabilites Port.

ECP+EPP Using Parallel port as

ECP+EPP mde

ame Port Address

201(default) Set onboard gamport is 201. **209** Set onboard gamport is 209.

Disabled Disabled.

Midi Port Address

300 Set Midi Port address is 300. 330(default) Set Midi Port address is 330.

Midi Port IRQ

10(default) Set Midi Port IRQ 10.5 Set Midi Port IRQ 5.

54 / Chapter 2 BIOS Setup

2.6 Power Management Setup

The Power Manageant Setup allows you to configure your system on effectively save energy while operating in a amner consistent with your own style of coputer use.

◎ Figure 6. Power Management Setup

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software Power Manageent Setup

ACPI Function	Enabled	Item Help
ACPI Suspend Type Power Management Video of Method Video of In Suspend Suspend Type Modem Use IRQ Suspend Mode HDD Power Down Soft-of by PWR-BTTN Wake Up by PCI Card Power oby Ring CPU Thermal-Throttling Resume by Alarm Data (of Month) Alarm Time (of hh:mm:ss) Alarm **Reload @bal Timer Events ** Primary IDE 0 Primary IDE 1 Secondary IDE 1 Secondary IDE 0 Secondary IDE 1 FDD,CM,LPT Port PCI PIRQA-D]#	S1(PS) User Define DPMS Yes Stop tent 3 Disabled Disabled Instant-th Disabled Enabled 50.0% Disabled 0 0 0 Disabled	Menu Level

←→↑: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7:@imized Defaults

ACPI Function

This itendisplay status of the Advanced Configuration and Power Manageent (ACPI).

ACPI Suspend Type

The iterallows you to select the suspend type under ACPI operating system

S1(PS)(default) Power on Suspend. S3(STR) Suspend to RAM.

Power Management

This category allows you to select the type (or degree) of power saving and is directly related to the following mles

- 1.HDD Power Down.
- 2.Doze Mode.
- 3. Suspend Mode.

If you highlight the literal "Press Enter" next to the "Power Manageunt" label and then press the enter key, it will take you a subumu with the following options:

Power Management

This option allows you to set each orde individually. When not disabled, each of the ranges are from in. to 1 hr. except for HDD Power Down which ranges from in. to 15 in. and disable.

The Choices: User Define (default), Min Saving, Max Saving.

HD Power Down

By default, this is "Disabled", canning that no atter the orde the rest of system the hard drive will reann ready. Otherwise, you have a range of choices from to 15 imutes or Suspend. This cans that you can select to have your hard disk drive be turned off after a selected number of imutes or when the rest or the system goes into a suspend orde.

Disabled(default).

Doze Mode/Suspend Mode

The **Doze Mode**, and **Suspend Mode** fields set the Period of timafter each of these **m**les actives. At Max Saving, these **m**les activate sequentially (in the given order) after one imute; at Min Saving after one hour.

Video **6** In Suspend

This field deterines when to activate the video off feature for **om**itor power **am**age**om**t.

The Choices: Yes(default), No

Video 6 Method

This determes the anner in which the mitor is

blanked.

V/BYNC+Blank This selection will cause the

system turn off the vertical

and horizontal.

Synchronization ports and write blanks to the video

buffer.

Blank Screen This option only writes blanks

to the video buffer.

DPMS Support (default)

Stop Cant(default)

Pwr@Suspend

Initial display power amageant signaling.

Suspend Type

Set Susped type is stop grant. Set Suspend type is Power on

Suspend.

Modem Use IRQ

This deterines the IRQ, which can be applied in Modem use.

3(default)

4/5/7/9/10/11/NA

Suspend Mode

Disabled(default) Disabled.

1 min - 1 blur Set the tier to enter Suspend

Mode.

HD Power Down

Disabled(default) Disabled. 1 - 15 mins Enabled.

Soft-6 by PWRBTN

Pressing the power button for **ore** than 4 seconds forces the system enter the Soft-Off state when the system s"hung".

The Choices:Instant-6 (default), Delay 4 Sec.

Wake-Up by PCI card

Enabled Enabled. **Disabled(default)** Disabled.

Power @By Ring

DisabledDisabled.Enabled(default)Enabled.

CPU Termal-Throttling

50.0%(default)

Monitor CPU Temp. will cause system slow down CPU Duty Cycle to 12.5% / 25.0% / 37.5% / 62.5% / 70.5% / 87.5%

Resume by Alarm

Disabled(default) Disabled. **Enabled** Enabled.

Primary IDE 0/1

Disabled(default) Disabled.

Enabled Enabled omitor Priary IDE

0/1 for Green event.

Secondary IDE 0/1

Disabled(default) Disabled.

Enabled Enabled mitor Secondary

IDE 0/1 for Green event

FDD,CM,LPT Port

Disabled(default) Disabled.

Enabled Enabled omitor FDD,COM,

LPT Port.

PCI PIRQA-D]#

Disabled(default) Ignore PCI PIRQ[A-D]#

Active.

Enabled Monitor PCI PIRQ[A-D]#

Active.

2.7 PnP/PCI Configurations

This section describes configuring the PCI bus systemPCI or Personal Coputer Interconnect, is a system hich allows I/O devices to operate at speeds nearing the speed of the CPU itself uses when commicating with its own special coponents. This section covers somewry technical item and it is strongly recommed that only experienced user should ake any changes to the default settings.

◎ Figure 7. PnP/PCI Configurations

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software PnP/PCI Configurations

Reset Configuration Data Resources Controlled By	Disabled Auto(ESCD)	Item Help
IR@esources	Press Enter	Menu Level
PCI/V& Palette Snoop	Disabled	When resources
INT Pin1 Assignment	AUTO	are controlled
INT Pin2 Assignment	AUTO	manually, assign
INT Pin3 Assignment	AUTO	each system
INT Pin4 Assignment	AUTO	interrupt a type, depending on the type of device using the interrupt

←→↑↓: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults

F7: Otimized Defaults

Reset Configuration Data

The systerBIOS supports the PnP feature so the system needs to record which resource is assigned and proceeds resources fromonflict. Every peripheral device has a node, which is called ESCD. This node records which resources are assigned to it. The systemeeds to record and update ESCD to the corry locations. These locations (4K) are reserved at the systerBIOS. If Disabled (Default) is chosen, the systerBIOS will update only when the new configuration varies frorthe last one. If Enabled is chosen, the systems forced to update ESCDs and then is automatically set to the "Disabled" rode.

IRQ3	assigned to:PCI/ISA PnP
IRQ4	assigned to:PCI/ISA PnP
IRQ5	assigned to:PCI/ISA PnP
IRQ6	assigned to:PCI/ISA PnP
IRQ7	assigned to:PCI/ISA PnP
IRQ8	assigned to:PCI/ISA PnP
IRQ9	assigned to:PCI/ISA PnP
IRQ10	assigned to:PCI/ISA PnP
IRQ11	assigned to:PCI/ISA PnP
IRQ12	assigned to:PCI/ISA PnP
IRQ13	assigned to:PCI/ISA PnP
IRQ14	assigned to:PCI/ISA PnP
IRQ15	assigned to:PCI/ISA PnP
DMA-0	assigned to:PCI/ISA PnP
DMA-1	assigned to:PCI/ISA PnP
DMA-2	assigned to:PCI/ISA PnP
DMA-3	assigned to:PCI/ISA PnP
DMA-4	assigned to:PCI/ISA PnP
DMA-5	assigned to:PCI/ISA PnP
DMA-6	assigned to:PCI/ISA PnP
DMA-7	assigned to:PCI/ISA PnP

The above settings will be shown on the screen only if "Manual" is chosen for the resources controlled by function

Legacy is the termwhich signifies that a resource is assigned to the ISA Bus and provides for non-PnP ISA add-on cards. PCI/ISA PnP signifies that a resource is assigned to the PCI Bus or provides for ISA PnP add-on cards and peripherals.

Resources Controlled By

By Choosing "Auto" (default), the system IOS will detect the system sources and automically assign the relative IRQ and DMA channel for each peripheral. By Choosing "Manual", the user will need to assign IRQ &MA for add-on cards. Be sure that there no IRQ/DMA and I/O port conflicts

IR (Resources

When resources are controlled **amually**, assign each system terrupt a type, depending on the type of device using the interrupt.

PCI / V& Palette Snoop

Choose Disabled or Enabled. Som graphic controllers which are not VGA copatible take the output from VGA controller and app it to their display as a way to provide boot information and VGA copatibility.

However, the color inforation coing fronthe VGA controller is drawn fronthe palette table inside the VGA controller to generate the proper colors, and the graphic controller needs to know what is in the palette of the VGA controller. To do this, the non-VGA graphic controller watches for the write access to the VGA palette and registers the snoop data. In PCI based system where the bus, the Write Access to the palette will not show up on the ISA bus if the PCI VGA controller responds to the Write

In this case, the PCI VGA controller should not respond to the Write, it should only snoop the data and perinthe access to be forward to the ISA bus. The non-VGA ISA graphic controller can then snoop the data on the ISA bus. Unless you have the above sistuation, you should disable this option.

Disabled(default) Disabled the function. **Enabled** Enabled the function.

2.8 PC Halth Status

◎ Figure 8. PC Halth Status

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software PC Health Status

CPU Warning Temperature	Disabled	Item Help
Current System Temp. Current CPU1 Temp. Current CPU Fan1 Speed Current CPU Fan2 Speed Current CPU Fan3 Speed IN0(V) IN1(V) IN2(V) +5V +12V -12V	39°C/102°F 44°C/111°F 0PRM 5578PRM 1.61V 1.82V 3.31V 4.99V 11.91V -12.11V	Menu Level
VBAT(V) 5VSB(V)	3.05V 4.75V	
Shut down Temperature	Disabled	

^{←→↑↓:} Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@neral Help F5:Previous Values F6:Fail-Safe Defaults F7:fitimized Defaults

Current Voltage(V) Vcore / VGL / Vcc3/+-12V/5V/5VSB/ VBAT

Detect system voltage status automatically.

Current CPU1/System Temperature (°C/°F)

This field displays the current CPU teperature, if you coputer contain a mitoring system

Current Fan/Power Fan / System Fan Speed

These field displays the current speed of up to System Fans, if you coputer contain a mitoring system

Cro warning remperature (C)	CPU	Warning	Temperature(℃)
-----------------------------	------------	---------	----------------

Disabled(default)	Disabled.	
60°C / 140°F	Monitor CPU Temat 60 140°F.	°C /
50°C / 122°F	Monitor CPU Temat 50 122°F.	°C /
53℃ / 127°F	Monitor CPU Temat 53 127°F.	°C /
56℃ / 133°F	Monitor CPU Temat 56 133°F	°C /
63℃ / 145°F	Monitor CPU Temat 63 145°F	°C /
66℃/151°F	Monitor CPU Temat 66 151°F	°C /
70℃/158°F	Monitor CPU Temat 70	°C /

158°F

Shutdown Temperature(°C./°F)

down Temperature(C/F))
Disabled(default)	Disabled.
60℃/140°F	Monitor CPU Temat 60 °C /
	140° F, if Tem> 60° C / 140° F
	system ill automically
	power off.
65℃ / 149°F	Monitor CPU Temat 65 °C /
	149°F, if Tem>65 °C/149°F
	system ill automically
	power off.
70℃/158°F	Monitor CPU Temat 70 °C /
	158°F, if Tem>70 °C/158°F
	system ill autoatically
	power off.
75℃ / 167°F	Monitor CPU Temat 75 °C /
	167° F, if Tem>75 $^{\circ}$ C / 167° F
	systemvill autoatically
	power off.

2.9 Frequency / Voltage Control

◎ Figure 9. Frequency / Voltage Control

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software Frequency / Voltage Control

Auto Detect DIMM / PCI CLK	Disabled	Item Help
Spread Spectrum CPU Host/PCI/Spread Spec. CPU Clock Ratio	Disabled Default X3	Menu Level

←→↑↓: Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:@heral Help F5:Previous Values F6:Fail-Safe Defaults F7:@timized Defaults

Auto Detect DIMM / PCI CLK

This itemllows you to enable/disable auto detect DIMM / PCI CLOCK.

The Choices: Disabled (default), Enabled.

CPU bst/PCI/Spread Spec.

This itemllows you to select CPU Host Clock (CPU/PCI).

NOTE:

If unfortunately,the system frequency that you are selected is not functioning, there are two without od booting-up the system

Method1:Clear the COMS data by setting the JP9((2-3) closed) as "On" status. All the COMS data will be loaded as default setting.

Method2:Press the<Insert>key and Power button similaneously, after that keep-on pressing the<Insert>key until the Power-on screen showed. This action will boot-up the systemaccording to FSB of the processor..

66 / Chapter 2 BIOS Setup

CPU Clock Ratio

This option will not be shown if you are using a CPU with the locked ratio.

X3/X3.5/X4/X4.5/X5/X5.5/X6/X6.5/X7/X7.5/X8

Spread Spectrum

This function id designed to EMI test only.

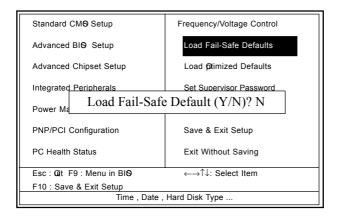
The Choices: Disabled (default), Enabled.

2.10 Load Fail-Safe Defaults

When you press <Enter> on this itemou get a confirmation dialog box with a passage sighar to:

© Figure 10. Load Fail-Safe Defaults

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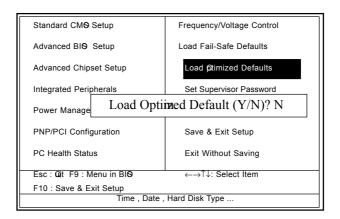
Pressing 'Y' loads the default values that are factory settings for optim performe system perations.

2.11 Load Otimized Defaults

When you press <Enter> on this itemou get a confirmation dialog box with a passage sight to:

◎ Figure 11. Load **⊘**timized Defaults

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software



Pressing 'Y' loads the default values that are factory settings for optim perforance systemperations.

2.12 Set Supervisor / User Password

◎ Figure 12. Set Supervisor / User Password

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software

Standard CM9 Setup	Frequency/Voltage Control		
Advanced BI S Setup	Load Fail-Safe Defaults		
Advanced Chipset Setup	Load Otimized Defaults		
Integrated Peripherals	Set Supervisor Password		
PNP/P Enter Password	Oave a Exit Octup		
PC Health Status	Exit Without Saving		
Esc: Qit F9: Menu in BIO	←→↑↓: Select Item		
F10 : Save & Exit Setup			
Time , Date , Hard Disk Type			

When you select this function, the following **as**sage will appear at the center of the screen to assist you in creating a password.

Enter Password

Type the password, up to eight characters, and press <Enter>. The password you type now will clear any previously entered password from MOS emry. You will be asked to comment the password. Type the password again and press <Enter>. You may also press <ESC> to about the selection and not enter a password. To disable password, just press <Enter> when you are propried to enter password. A ensage will comment you wish to disable the password. Once the password is disabled, the system will boot and you can enter setup freely.

70 / Chapter 2 BIOS Setup

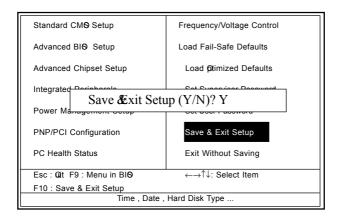
Password Disabled

If you select "Systemat the Security Option of BIOS Features Setup Menu, you willbe propted for the password every timewhen the systems rebooted, or any timewhen you try to enter Setup. If you select "Setup" at Security Option of BIOS Features Setup Menu, you willbe propted only when you try to enter Setup.

2.13 Save & Exit Setup

◎ Figure 13. Save & Exit Setup

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software



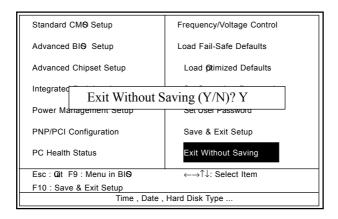
Type "Y" will quit the Setup Unility and save the user setup value to RTC CMOS RAM.

Type "N" will return Setup Unility.

2.14 Exit Without Saving

◎ Figure 14. Exit Without Saving

CMOS Setup Utility-Copyright(C) 1984-2000 Award Software

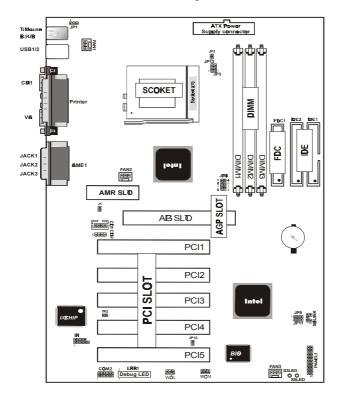


Type "Y" will quit the Setup Unility without saving to RTC CMOS RAM.

Type "N" will return Setup Unility.

Date : / / **G**arantee Sheet/Technical Fault Report M/B Model No.:_____ Vender Serial No. Date of Purchasing: Hardare Configuration Used: CPU RAM Video Card Hard Drive ther Card Diagnostic Softare Used: **Fault Description: Technical Support:** WWW: wacorp.com.tw FAE: fae@acorp.com.tw

The 6A815 Mainboard Layout



Panel Connectors: PANEL1

