

# 5VIA77

## Jumper Setting

Internal Clock	External Clock	Ratio
300 Mhz	100 Mhz	3.0X
300 Mhz	75 Mhz	4.0X
300 Mhz	66 Mhz	4.5X
266 Mhz	75 Mhz	3.5X
266 Mhz	66 Mhz	4.0X
233 Mhz	66 Mhz	3.5X
200 Mhz	66 Mhz	3.0X
166 Mhz	66 Mhz	2.5X

Frequency	JCK2	JCK3	JCK4	AGP	PCI
66 Mhz	2-3	2-3	1-2	66 Mhz	33 Mhz
75 Mhz	1-2	2-3	2-3	75 Mhz	37 Mhz
83.3 Mhz	1-2	2-3	1-2	55 Mhz	27 Mhz
95 Mhz	1-2	1-2	2-3	62 Mhz	31 Mhz
100 Mhz	1-2	1-2	1-2	66 Mhz	33 Mhz
Ration	JP2	JP3	JP4		
1.5X/3.5X	1-2	1-2	1-2		
2.0X	2-3	1-2	1-2		
2.5X	2-3	2-3	1-2		
3.0X	1-2	2-3	1-2		
4.0X	2-3	1-2	2-3		

SDRAM Clock	JCK1
AGP CLK	2-3
CPU CLK	1-2

PCB VER : A3

ON = Close , Off = Open			
CPU Voltage	JV_L1	JV_L2	JV1(1-2, 3-4, 5-6, 7-8, 9-10, 11-12)
3.5v	ON	OFF	1-2
3.3v	ON	OFF	3-4
3.2v	OFF	ON	5-6
2.9v	OFF	ON	7-8
2.8v	OFF	ON	9-10
2.2v	OFF	ON	11-12

FOR PCB VER : A4

CPU Voltage	Jumper Setting
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	<b>1-2</b>	<b>3-4</b>	<b>5-6</b>	<b>7-8</b>
3.5v	ON	ON	ON	ON
3.4v	OFF	ON	ON	ON
3.3v	ON	OFF	ON	ON
3.2v	OFF	OFF	ON	ON
3.1v	ON	ON	OFF	ON
3.0v	OFF	ON	OFF	ON
2.9v	ON	OFF	OFF	ON
2.8v	OFF	OFF	OFF	ON
2.7v	ON	ON	ON	OFF
2.6v	OFF	ON	ON	OFF
2.5v	ON	OFF	ON	OFF
2.4v	OFF	OFF	ON	OFF
2.3v	ON	ON	OFF	OFF
2.2v	OFF	ON	OFF	OFF
2.1v	ON	OFF	OFF	OFF
2.0v	OFF	OFF	OFF	OFF